


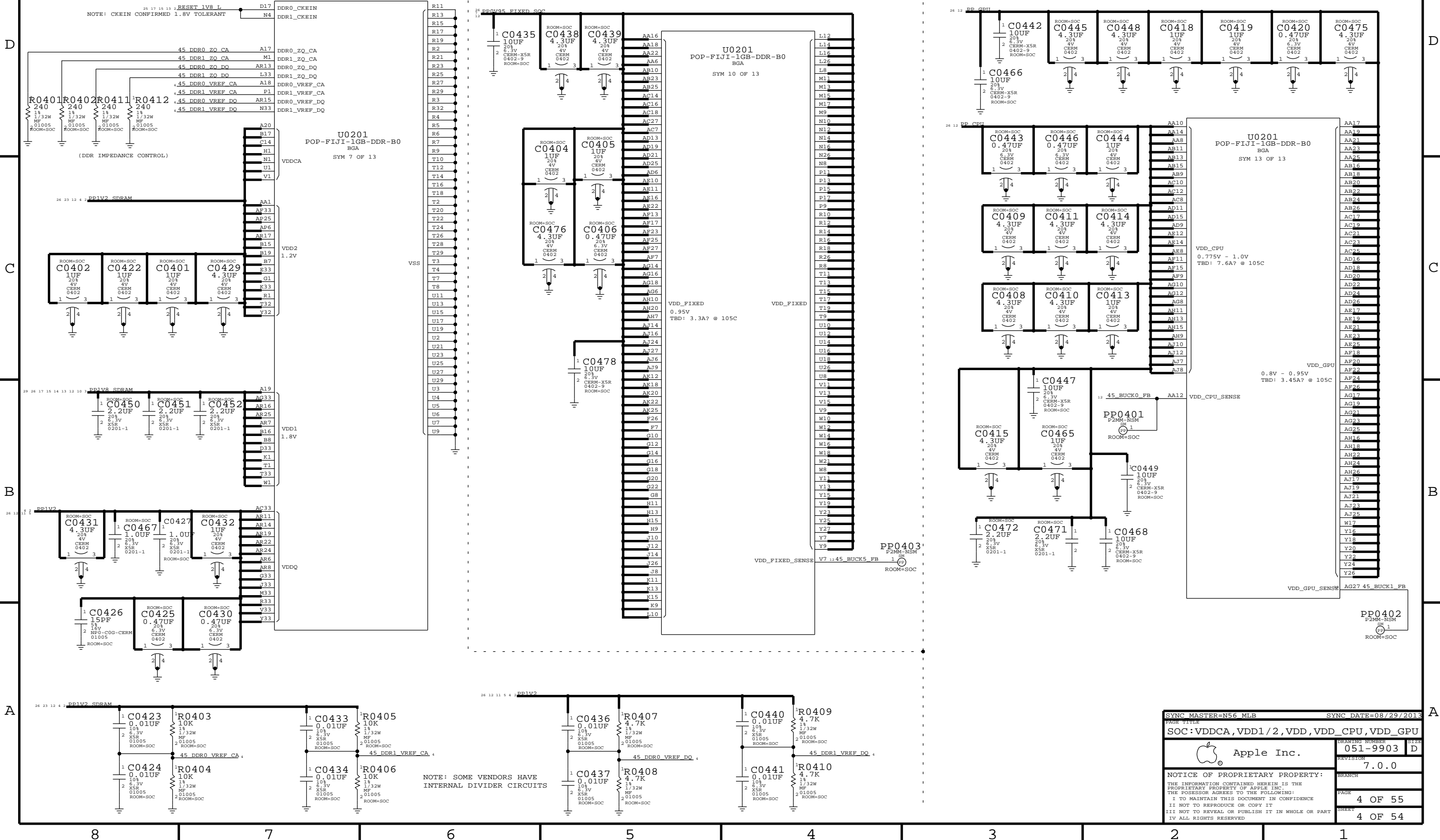
SYNC MASTER=N56 ML6		SYNC DATE=08/29/2013	
PAGE TITLE		SOC: I / OS	
		DRAWING NUMBER 051-9903	
Apple Inc.		SIZE D	
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FIJI: VDDCA, VDD1/2, VDDQ, VDD, VDD_FIXED, VDD_CPU, VDD_GPU

VDDCA, VDD1/2, VDDQ

VDD

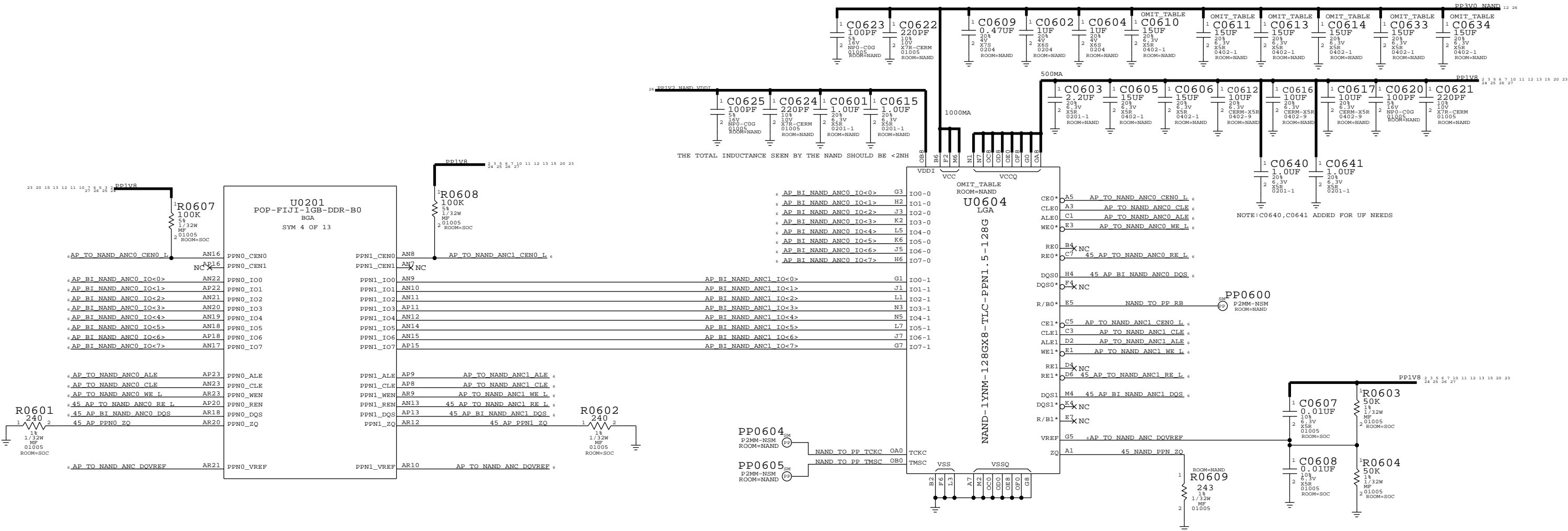
VDD_CPU, VDD_GPU




SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
PAGE TITLE		SOC:VDDCA,VDD1/2,VDD,VDD_CPU,VDD_GPU	
		DRAWING NUMBER	051-9903
		REVISION	7.0.0
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		PAGE	4 OF 55
		SHEET	4 OF 54

FIJI: NAND + 12X17 NAND PKG

SUPPORT FOR PPN1.5 (1.8V IO) ONLY



PP0601 P4MM 1 45 AP BI NAND ANCO IO<6> (IS A STATUS READY BIT)
PP0602 P4MM 1 45 AP TO NAND ANCO RE L
PP0603 P4MM 1 45 AP BI NAND ANCO DQS

SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
PAGE TITLE			
SOC : NAND			
 Apple Inc.		DRAWING NUMBER	051-9903
		REVISION	7.0.0
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		PAGE	6 OF 55
		SHEET	6 OF 54

D

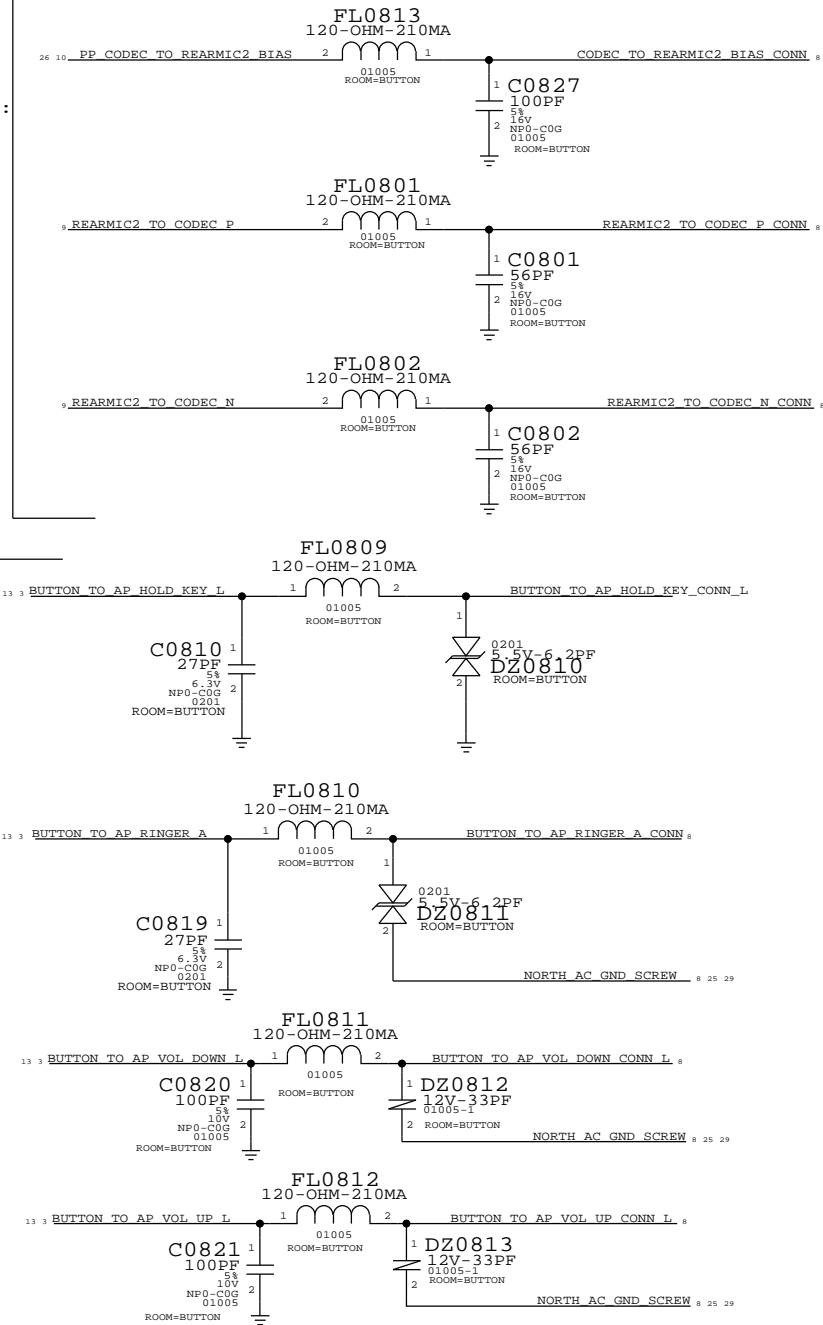
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8 7 6 5 4 3 2 1

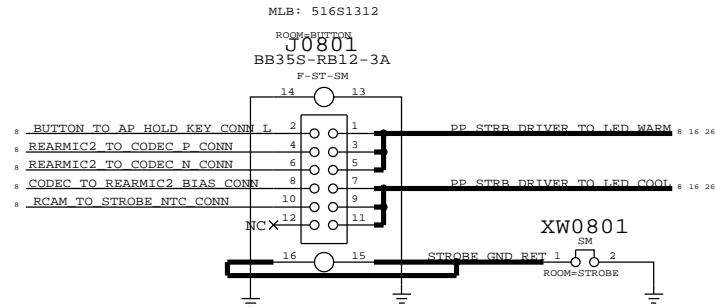
BUTTON FLEX (BUTTONS, ANC REF MIC, STROBE, STROBE_NTC, WIFI FLEX PAC)

MIC2 (ANC REF MIC):
MIC2/4 BIAS,
MIC2_P,_N

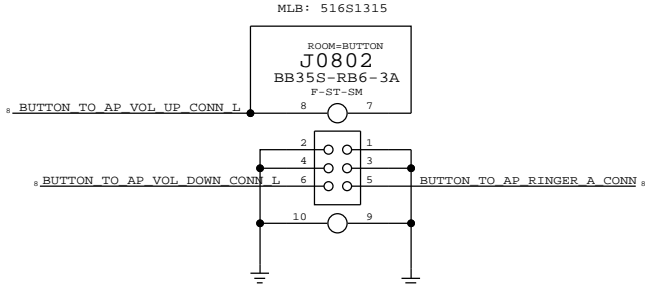
BUTTONS:
RINGER, HOLD,
VOL_UP/DOWN,



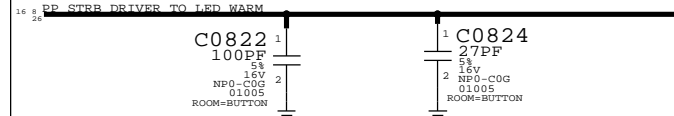
RIGHT BUTTON B2B



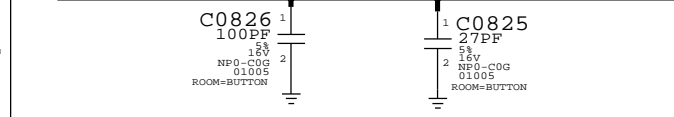
LEFT BUTTON B2B



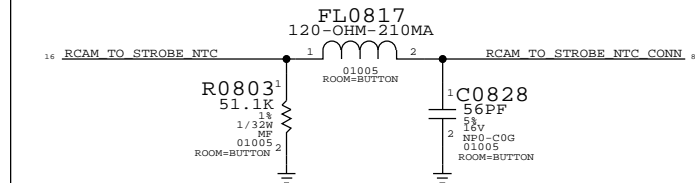
STROBE:
LED WARM




STROBE:
LED COOL



STROBE:
NTC

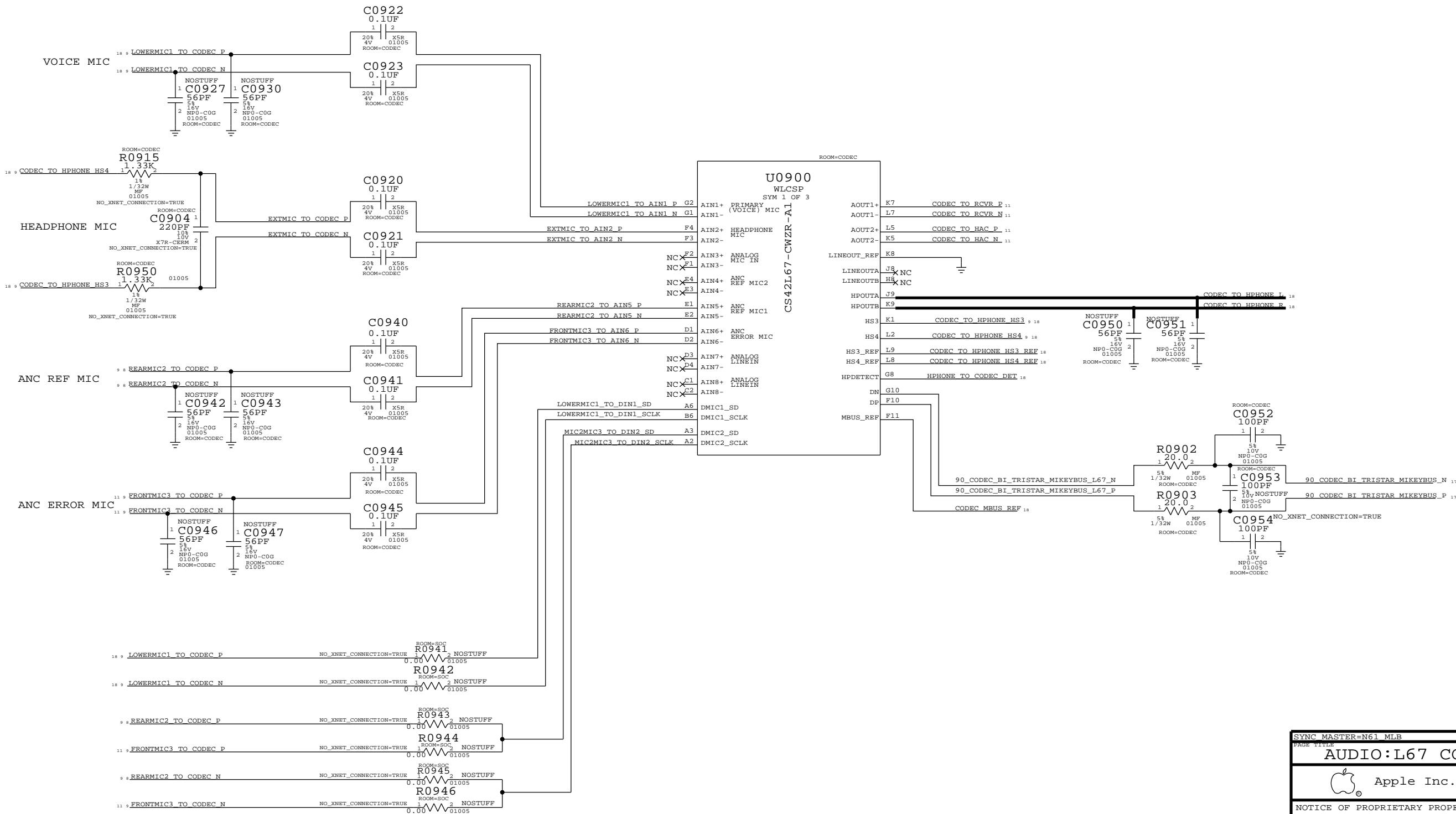



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
IO:BUTTON FLEX CONN			
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		REVISION	7.0.0
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		PAGE	8 OF 55
		SHEET	8 OF 54

L67 AUDIO CODEC

AUDIO I/O

(ANALOG MIC IN, DIG MIC IN, HPOUT, LINEOUT, RECEIVER OUT, MIKEYBUS)



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
AUDIO:L67 CODEC		(1/2)	
	Apple Inc.	DRAWING NUMBER	051-9903
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D

C

B

A

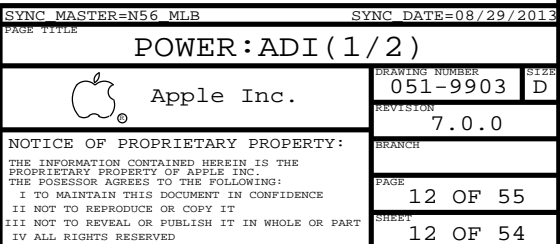


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(BUCK, LDO, VIBE DRIVER, 32K, CHARGER)



ADI PMU

(AMUX, GPIO, BUTTONS, ADC, THERMISTORS, SYSTEM I/F, GND)

D

D

C

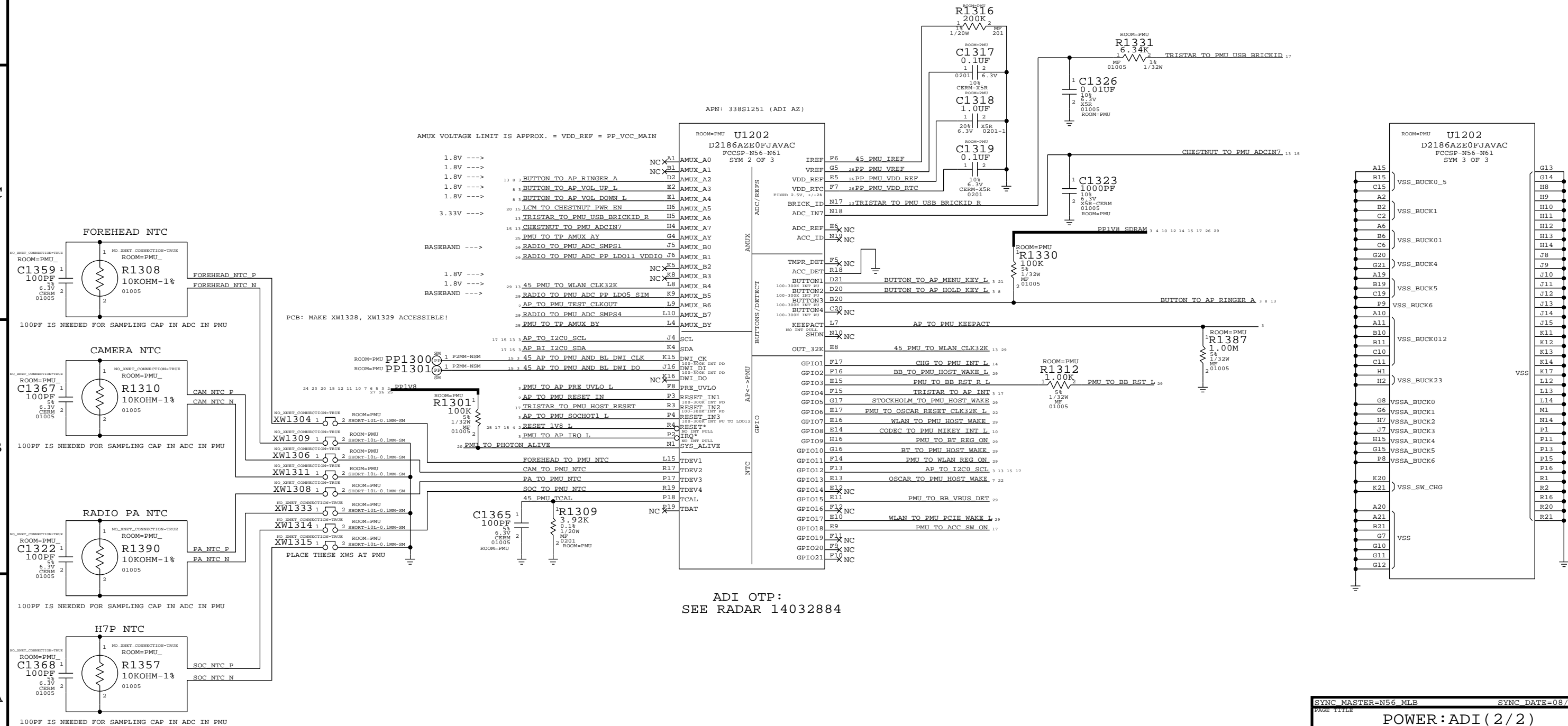
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B

B

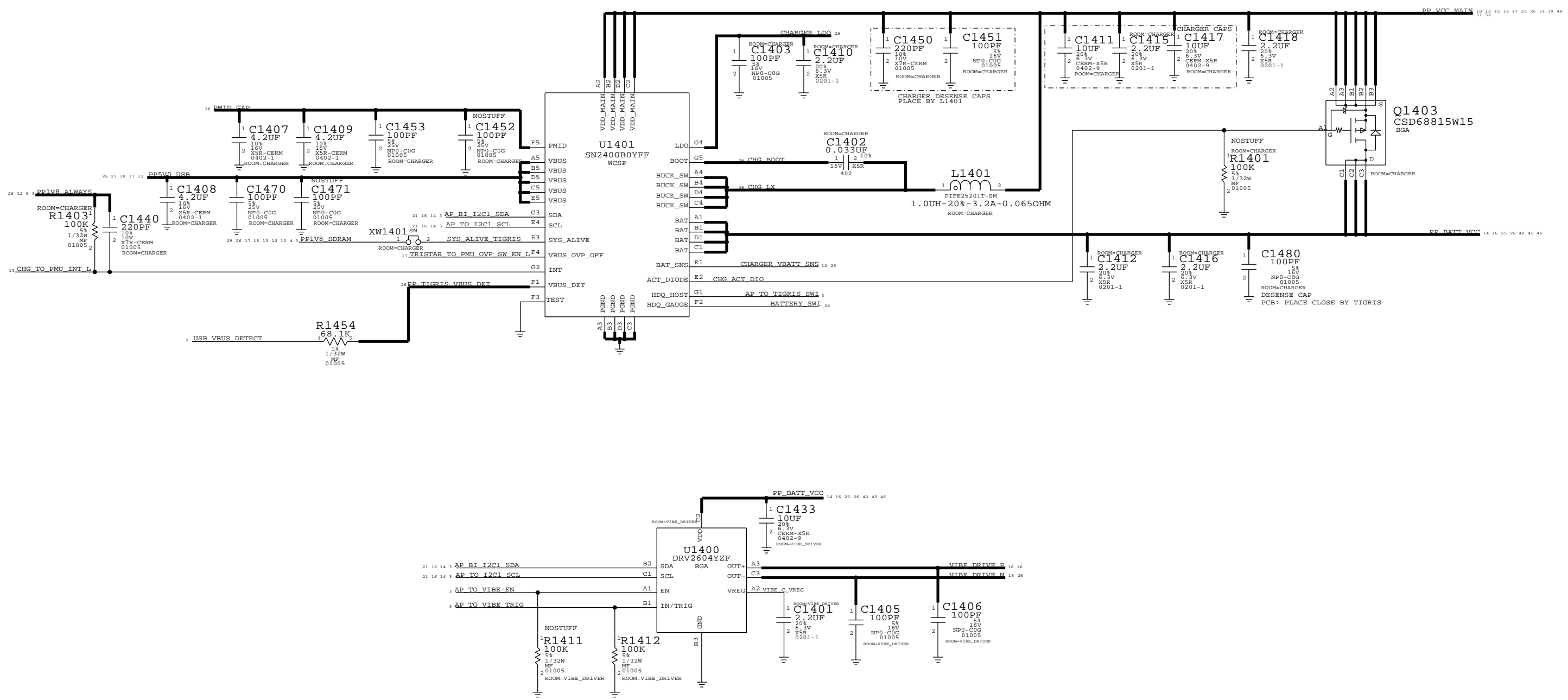
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A



SYNC MASTER=N56 MLB		SYNC DATE=08/29/2013	
PAGE TITLE			
POWER:ADI (2 / 2)			
 Apple Inc.	DRAWING NUMBER	051-9903	SIZE D
	REVISION	7.0.0	
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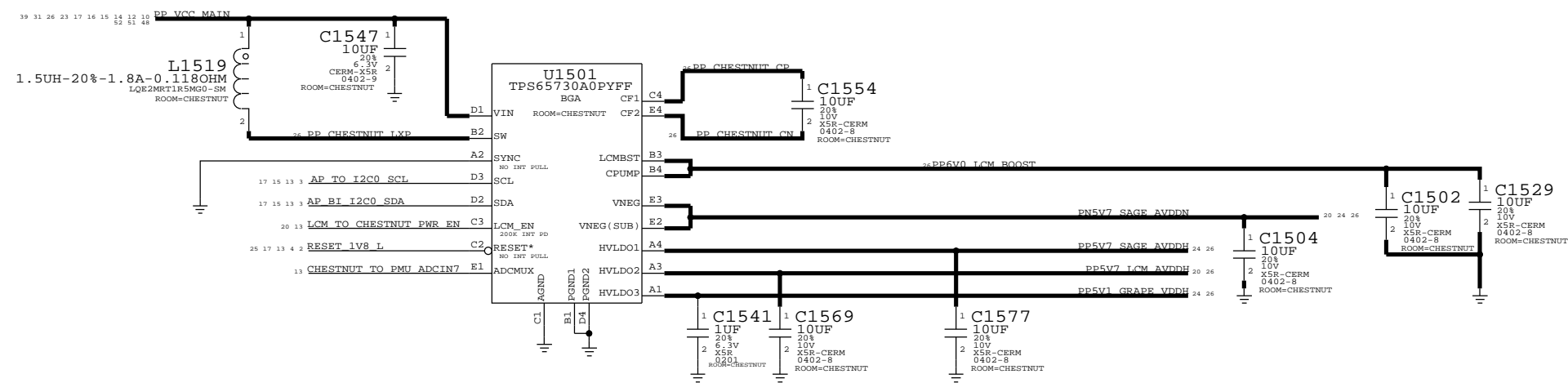
TIGRIS CHARGER & VIBE DRIVER



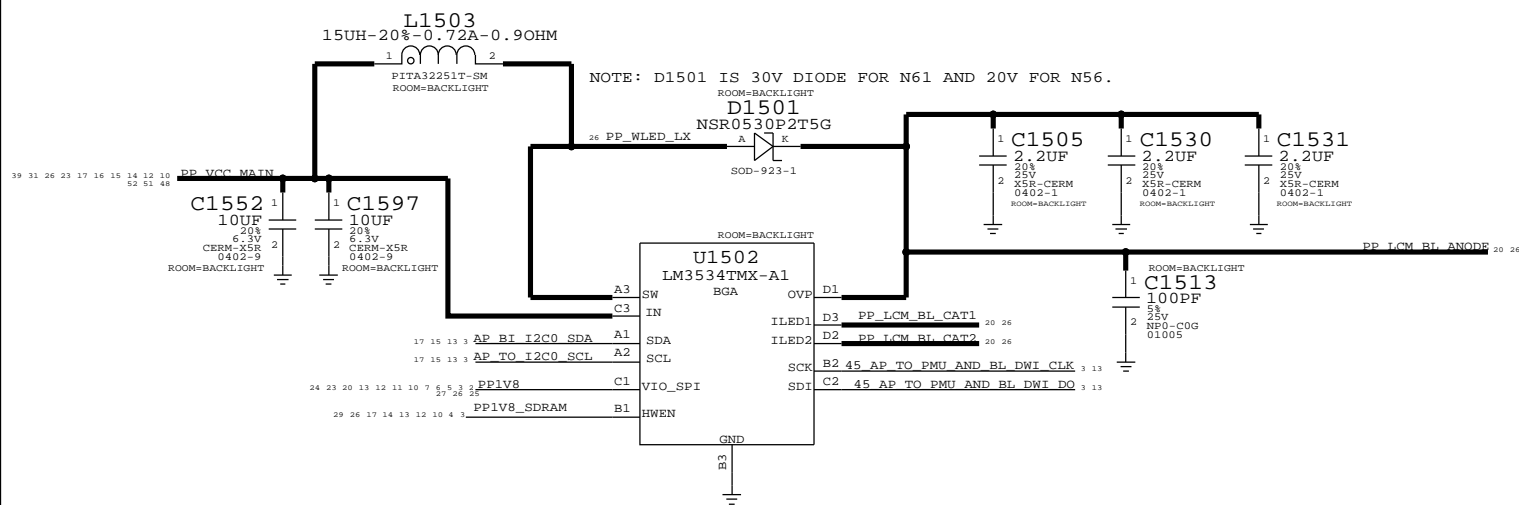
PAGE TITLE		
POWER:TIGRISR,VIBE DRIVER		
 Apple Inc.	DRAWING NUMBER	051-9903
	REVISION	7.0.0
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		PAGE 14 OF 55
		SHEET 14 OF 54

CHESTNUT, BACKLIGHT DRIVER, MESA BOOST

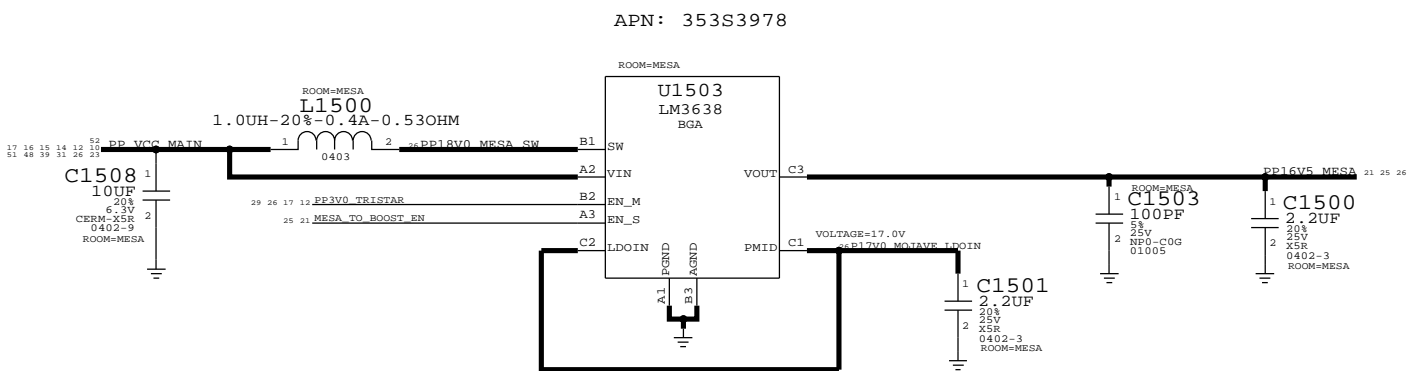
D500 DISPLAY PMU (TI CHESTNUT, 338S1149)



D500 BACKLIGHT DRIVER



MESA BOOST A0

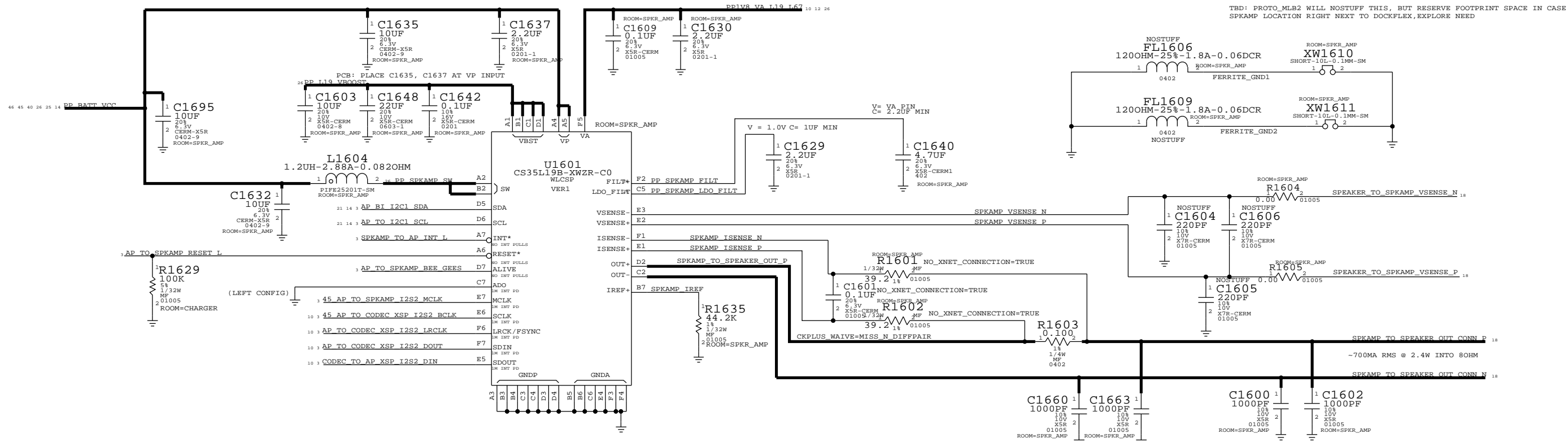


PAGE TITLE		PAGE NUMBER	
DISPLAY:CHESTNUT, BACKLIGHT DRIVER		051-9903	
REVISION		7.0.0	
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SPEAKER AMP, LED DRIVER

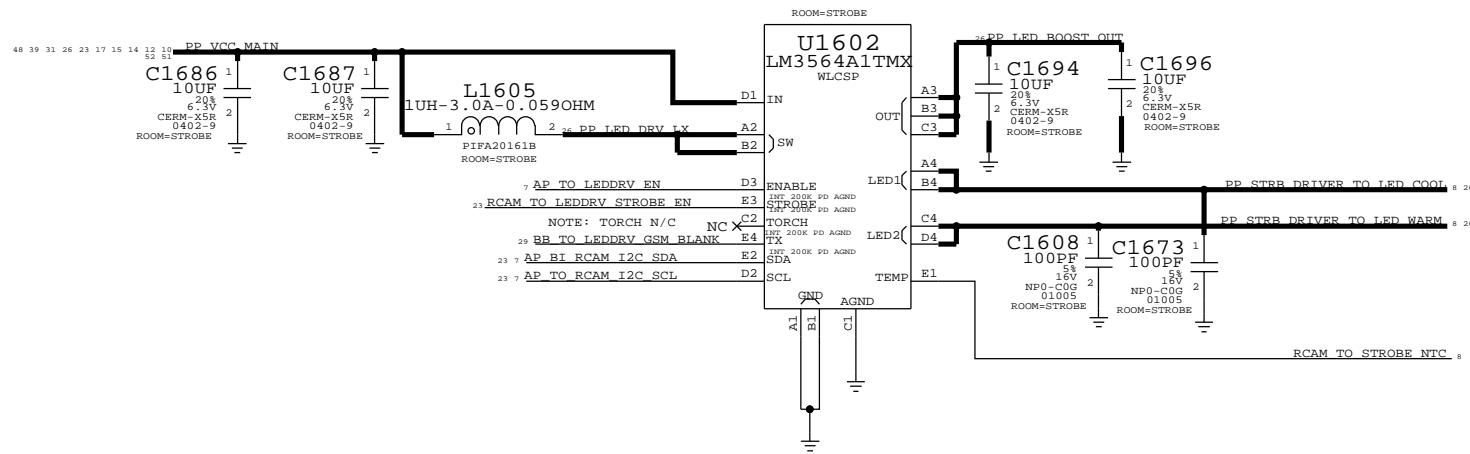
SPEAKER AMP

I2C ADDRESS: 1000000X



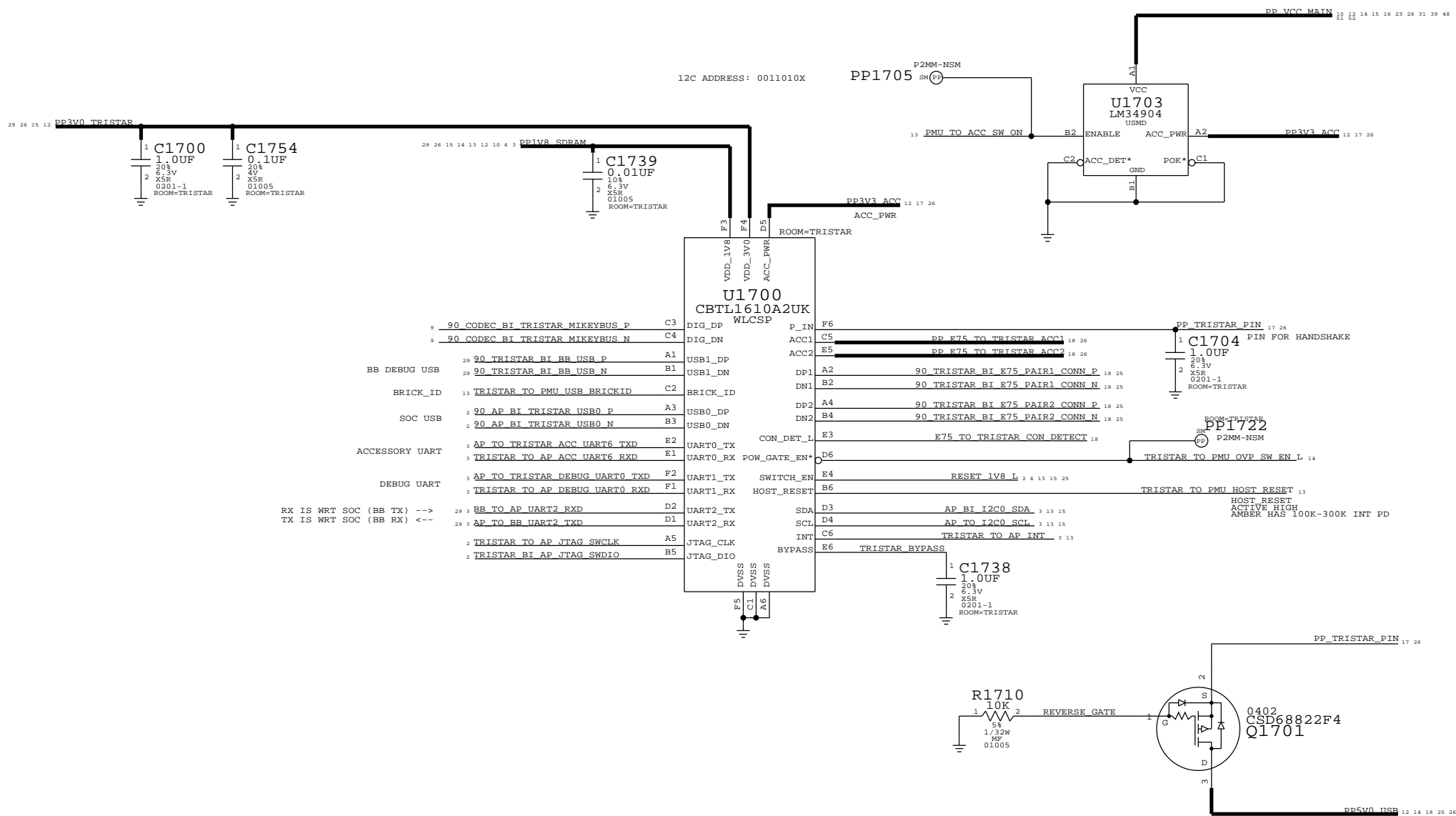
STROBE DRIVER


TI: APN 353S3899



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE		AUDIO:SPKR AMP,STROBE	
DRAWING NUMBER		051-9903	SIZE D
REVISION		7.0.0	BRANCH
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TRISTAR2



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
IO:TRISTAR2			
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		BRANCH	
		PAGE	17 OF 55
		SHEET	17 OF 54

DOCKFLEX B2B (USB VBUS, SPEAKER, ANTENNA LAT SW CTRL, MIC1 (PRIMARY MIC), ACC DET/ID/PWR, E75 DIFFPAIRS)

D

LOWER MIC1
(PRIMARY
VOICE MIC)

C

HEADPHONE

B

CODEC TO
HEADPHONE

A

ACCESSORY:
VIBE
DRIVE

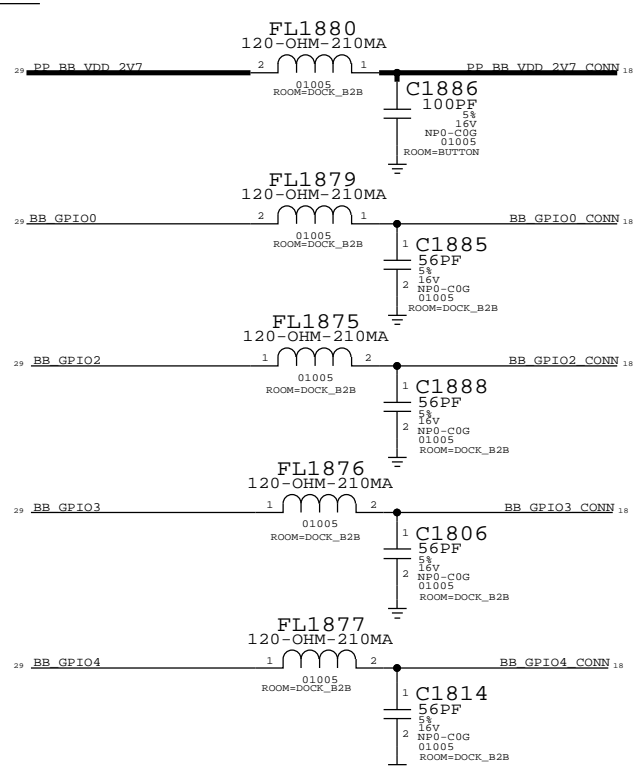
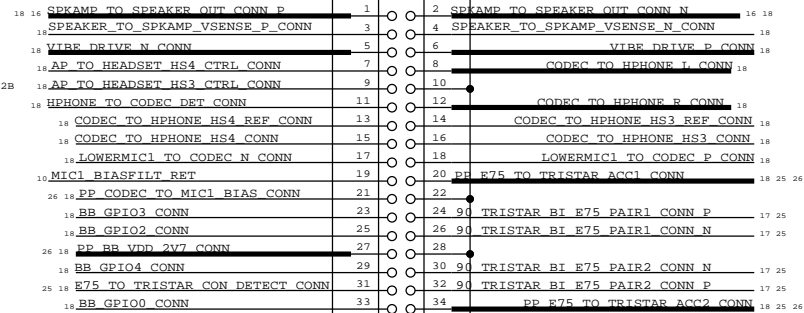
SPEAKER:
LEADS,
VSENSE

TRISTAR

USB
VBUS

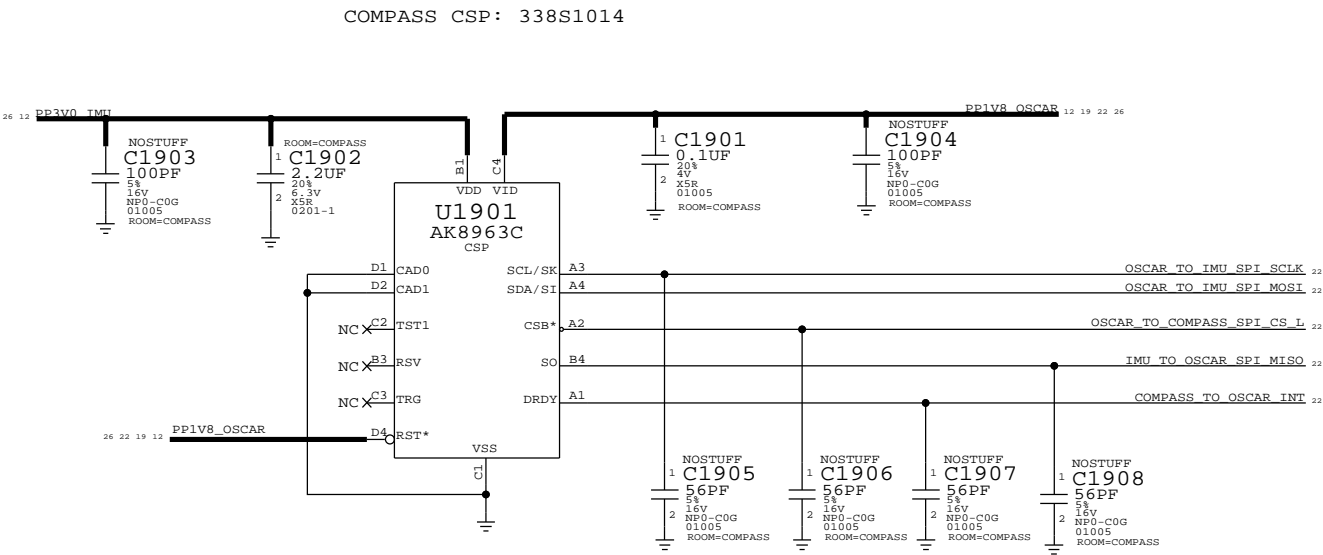
ANTENNA

MLB: 516S1281 (RCPT)
ROOM=DOCK_B2B J1817
24-5859-036-201-829
F-ST-SM



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE		IO:DOCK FLEX CONN	
Apple Inc.		DRAWING NUMBER	051-9903
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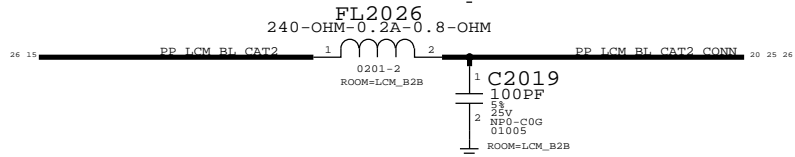
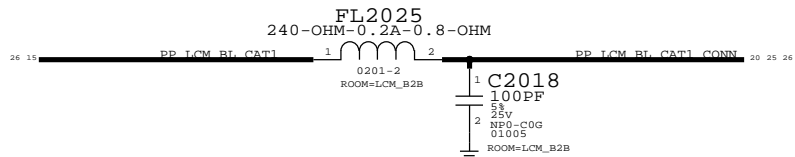
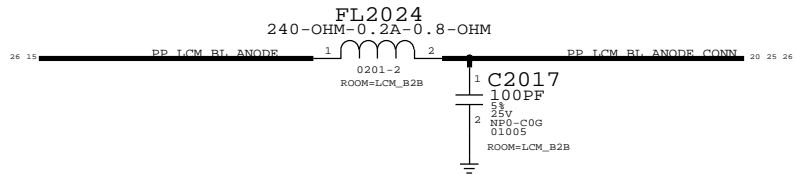
COMPASS - AKM COMPASS IN POR LOCATION



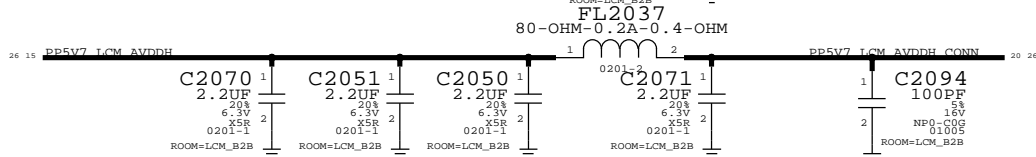
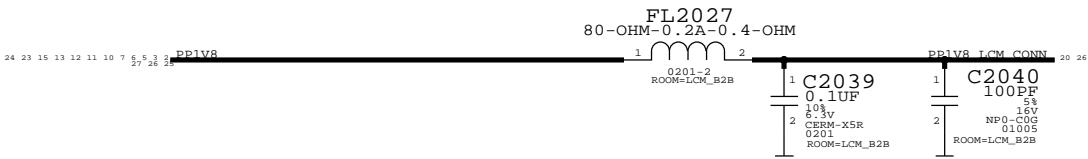
LCD B2B

Backlight

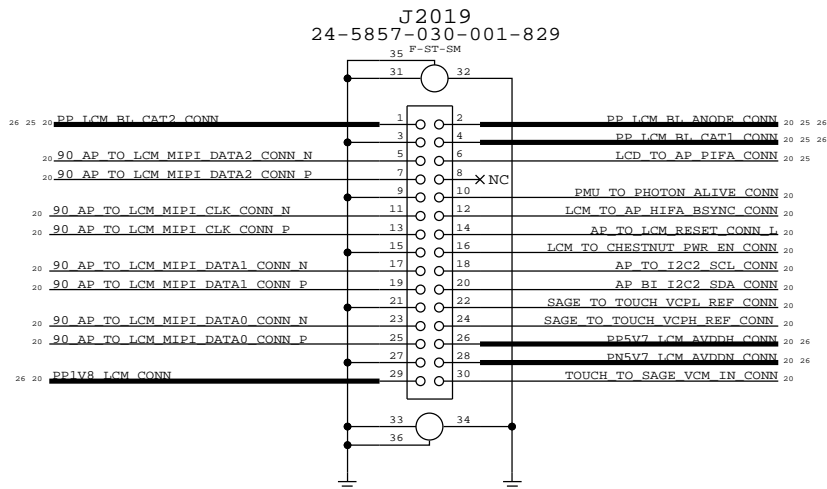
(N56 HAS A 2ND SET OF BL SIGNALS ON P. 19).



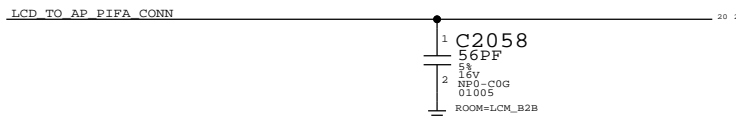
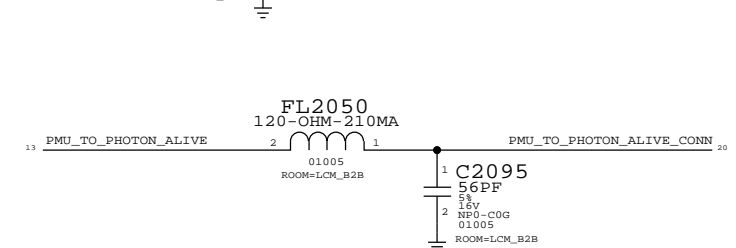
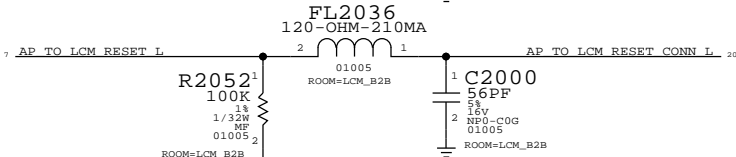
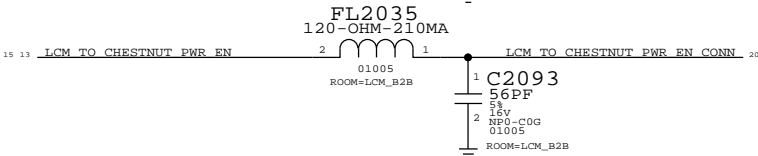
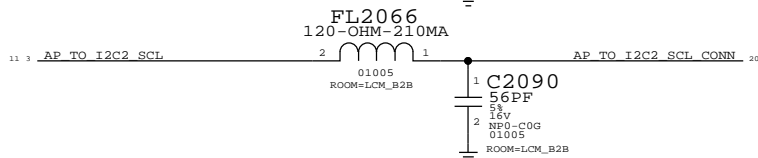
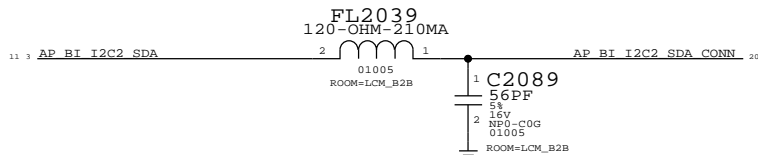
LCM Supplies



THIS ONE ON MLB ----> 516S1164

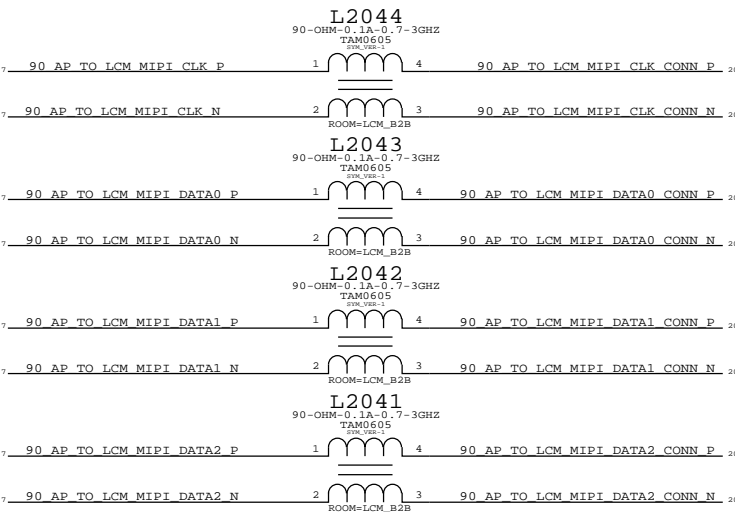


Digital Interfaces

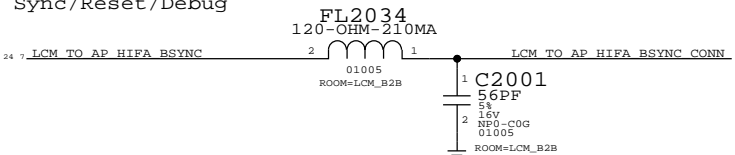


MIPI Common Mode Chokes

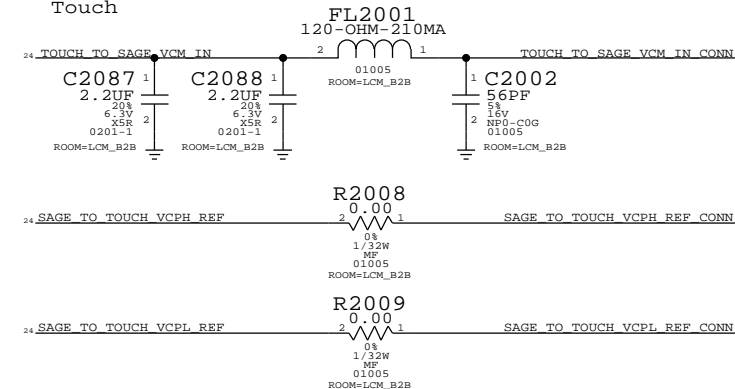
(N56 HAS A 4TH MIPI LANE ON P. 19).




Sync/Reset/Debug

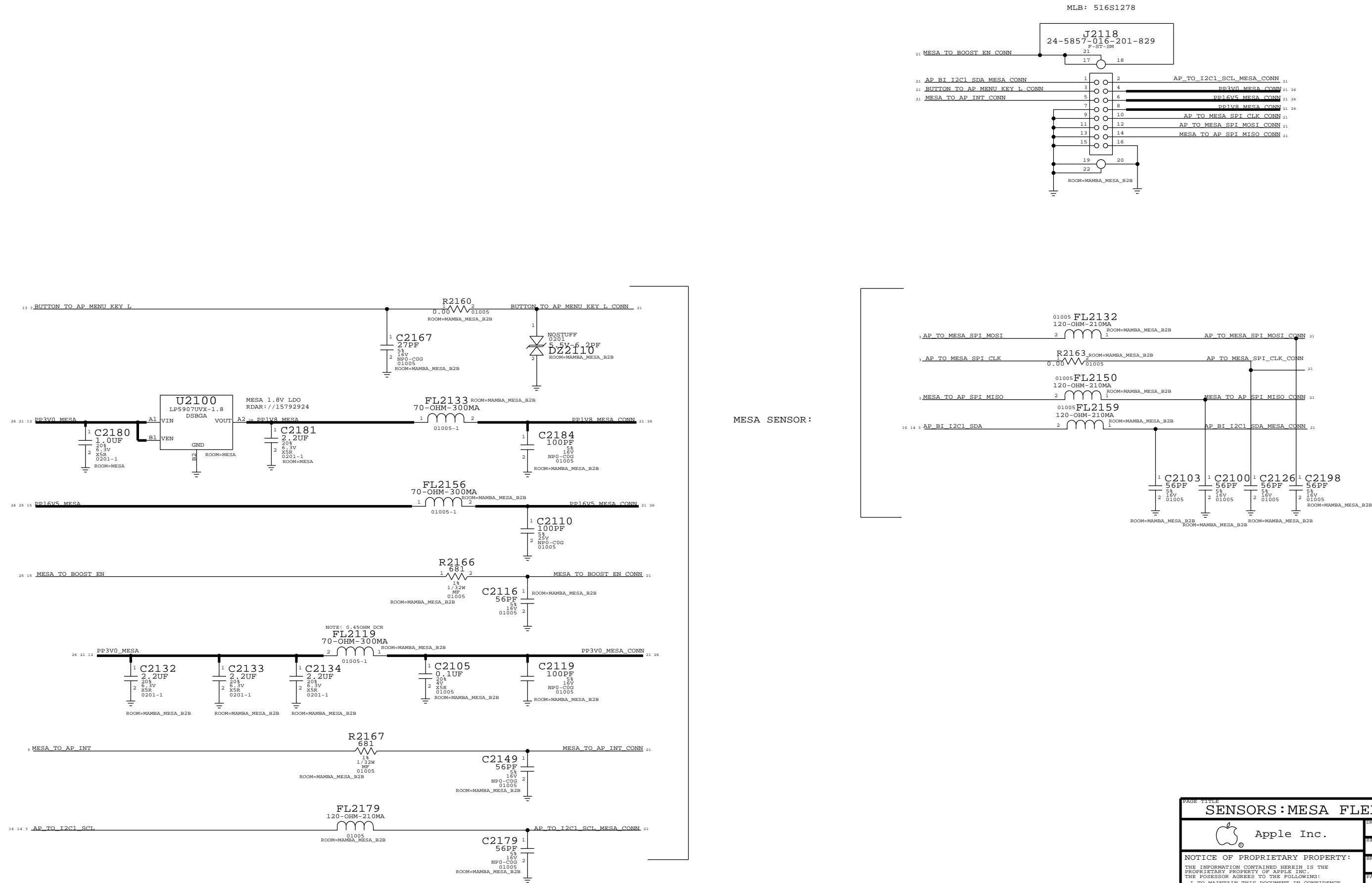


Touch

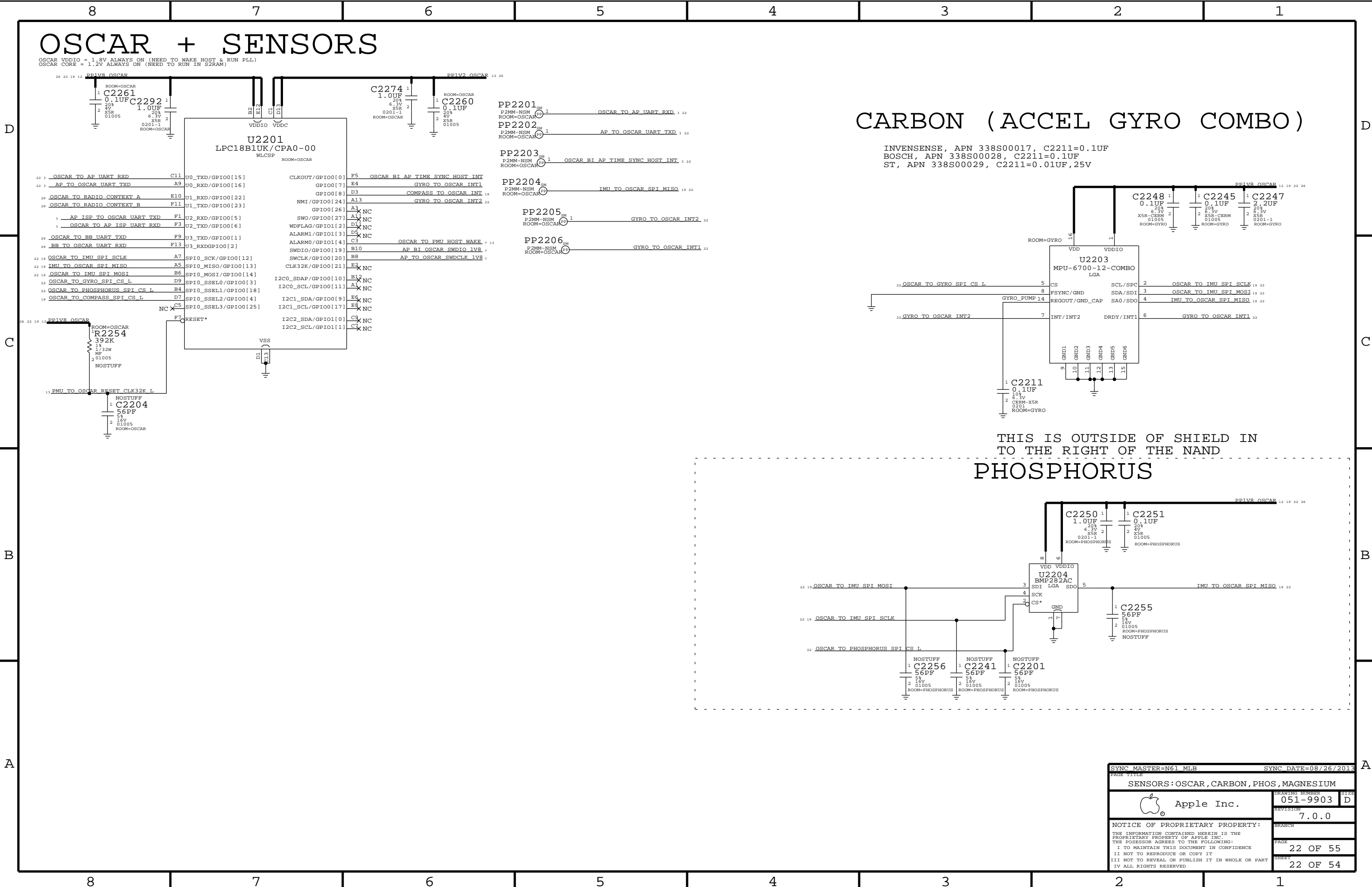



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
DISPLAY:FLEX CONN			
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MESA CONNECTOR



PAGE TITLE		
SENSORS:MESA FLEX CONN		
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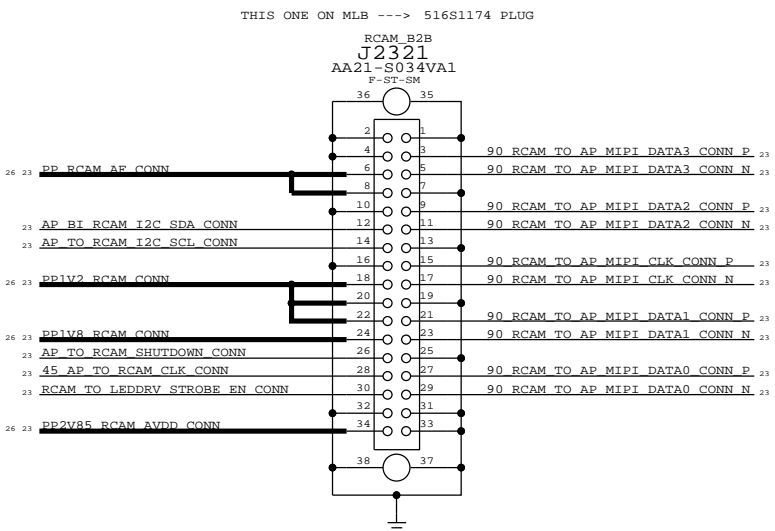
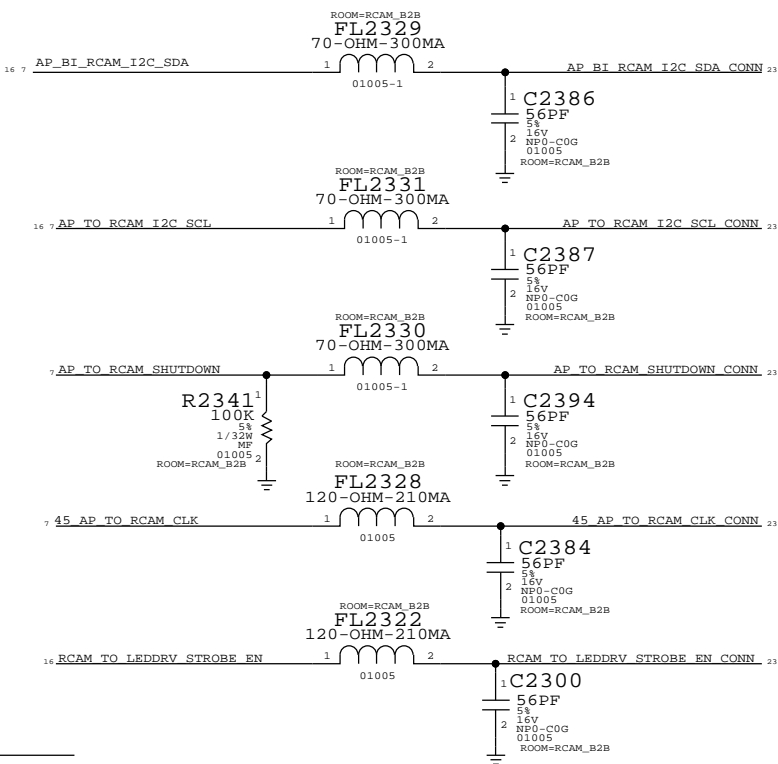
SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
SENSORS : OSCAR , CARBON , PHOS , MAGNESIUM			
 Apple Inc.		DRAWING NUMBER	SIZE
		051-9903	D
		REVISION	
		7.0.0	
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		SHEET	22 OF 54

RCAM B2B (REAR CAMERA CONNECTOR)

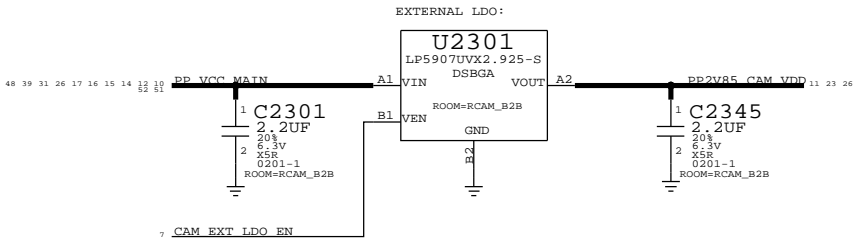
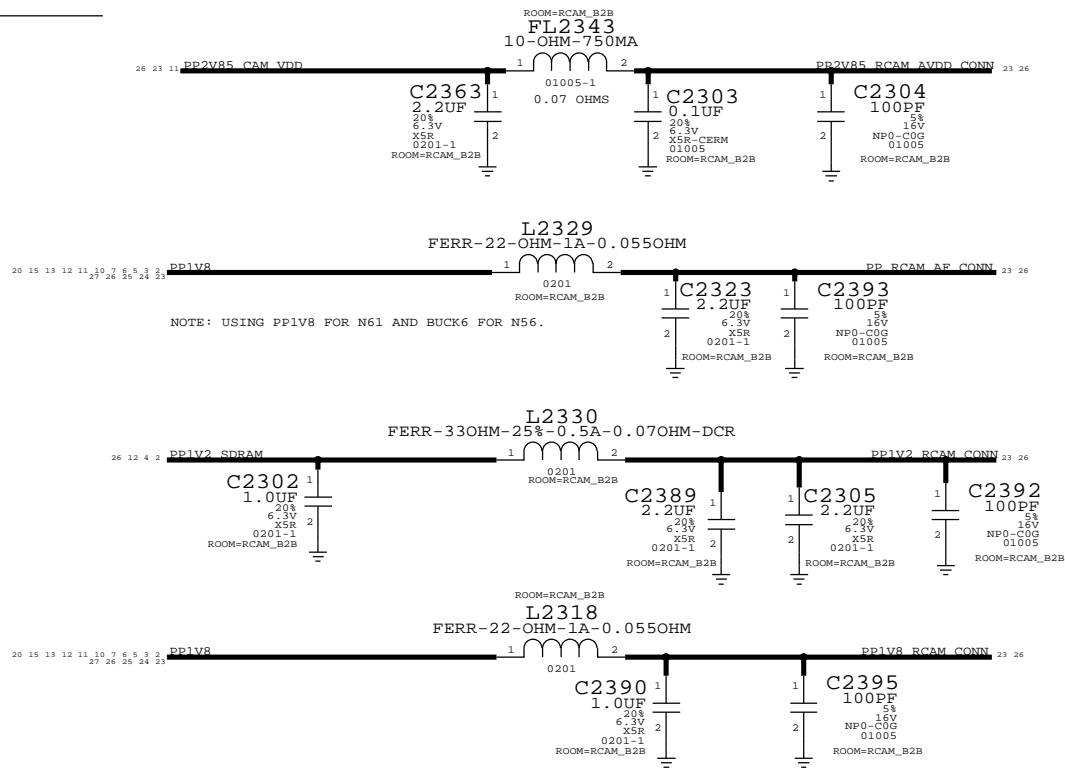
RCAM:
4-LANE MIPI


RCAM:
DIGITAL I/F
(I2C, CTRL, CLK)

RCAM:
POWER:
(1.8V DVDD)
(2.8V AVDD)
(1.2V VCC)
(1.8V/2V AF)



RCAM/FCAM AVDD RAIL EXT. LDO:



SYNC MASTER=N61 MLB		SYNC DATE=08/26/2013	
PAGE TITLE			
CAMERA:REAR FLEX CONN			
 Apple Inc.		DRAWING NUMBER	051-9903
		REVISION	7.0.0
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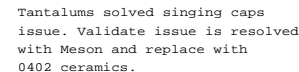
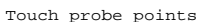
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R

A

APN: 343S0638





APN: 343S0694

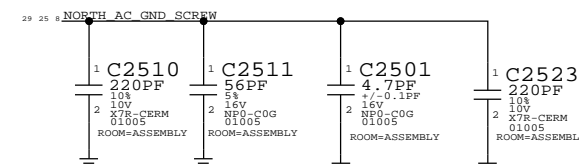
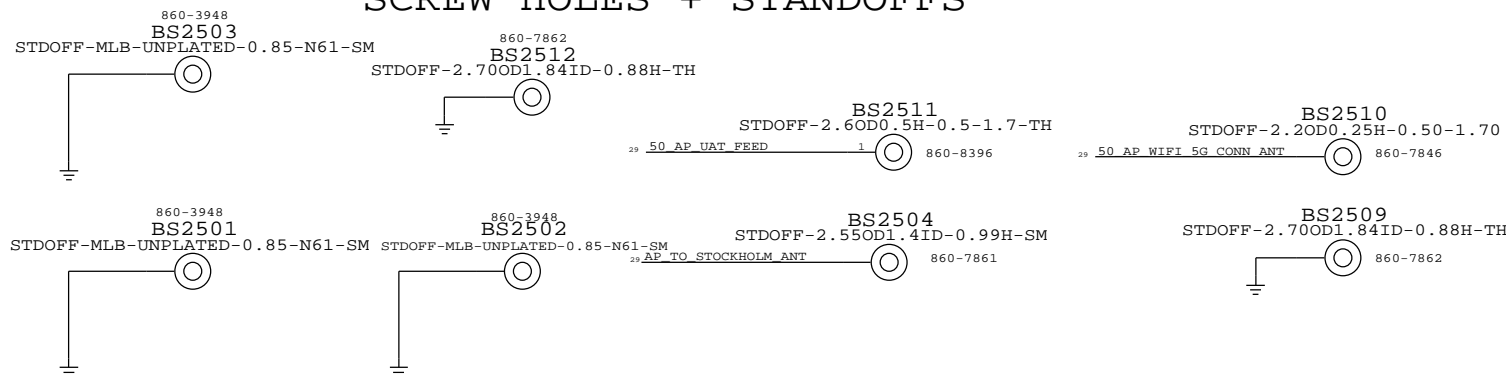
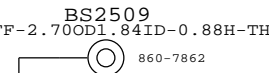


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24 OF 5

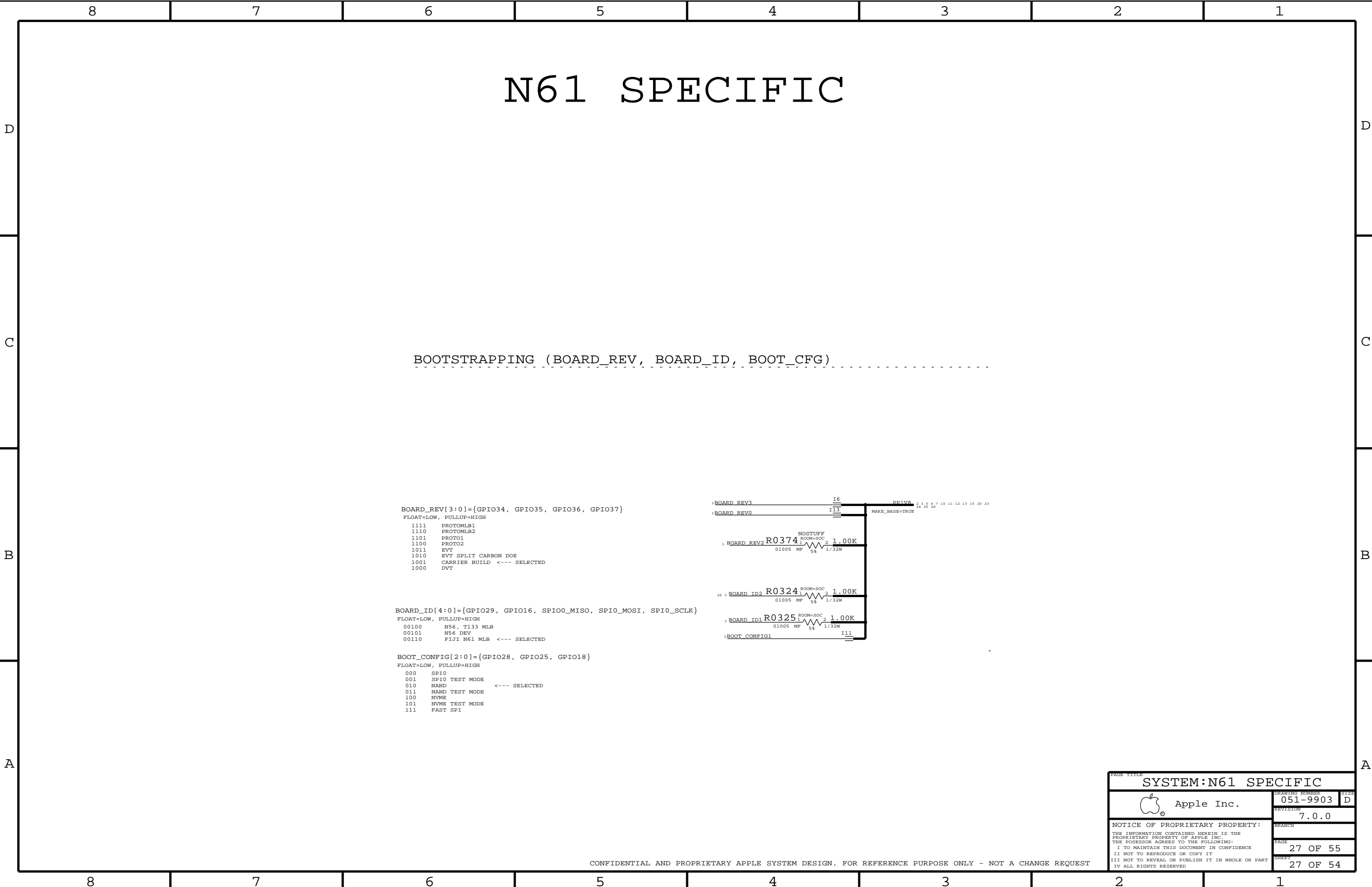
8	7	6	5	4	3	2	1
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A

VOLTAGE PROPERTIES

	VOLTAGE=3.3V	PP3V3 USB	3 12			VOLTAGE=5.0V	PP LED DRV LX	14	
R67	VOLTAGE=1.8V	PP1V8 VA I19 I67	10 12 16			R68	VOLTAGE=5.0V	PP LED BOOST OUT	16
R68	VOLTAGE=3.0V	PP3V0 TRISTAR	12 15 17 29						
R69	VOLTAGE=3.0V	PP3V0 IMU	12 19			R70	VOLTAGE=2.9V	PP2V9 LDO9	12
R70	VOLTAGE=3.0V	PP3V0 NAND	6 12						
R71	VOLTAGE=3.0V	PP3V3 ACC	12 17			R72	VOLTAGE=1.8V	PP CODEC TO MIC1 BIAS CONN	18
R72	VOLTAGE=3.0V	PP1V0 PROX ALS	11 12			R73	VOLTAGE=4.6V	PP E75 TO TRISTAR ACC2	17 18
						R74	VOLTAGE=4.6V	PP E75 TO TRISTAR ACC2 CONN	18 25
R84	VOLTAGE=4.6V	PP VCC MAIN	10 12 14 15 16 17 23 31 39			R100	VOLTAGE=1.8V	PP1V8 LCM CONN	20
R85	VOLTAGE=1.0V	PP1V0	7 12			R101	VOLTAGE=22.0V	PP LCM BL ANODE CONN	20 25
R86	VOLTAGE=3.0V	PP3V0 PROX TRIED	11 12			R102	VOLTAGE=-5.7V	PP5V7 LCM AVDDN CONN	20
R87	VOLTAGE=1.8V	PP1V8 ALWAYS	3 5 12 14			R103	VOLTAGE=5.7V	PP5V7 LCM AVDDH CONN	20
R88	VOLTAGE=3.0V	PP3V0 MESA	12 21						
R89	VOLTAGE=1.1V	PP CPU	4 12						
R90	VOLTAGE=1.1V	PP GPU	4 12			R105	VOLTAGE=1.8V	PP1V8 MESA	21
						R106	VOLTAGE=16.5V	PP16V5 MESA CONN	21
R101	VOLTAGE=1.2V	PP1V2 SDRAM	2 4 12 23			R108	VOLTAGE=5.0V	PP TRISTAR PIN	17
R102	VOLTAGE=1.8V	PP1V8 SDRAM	3 4 10 12 13 14 15 17 29						
R103	VOLTAGE=1.8V	PP1V8	2 3 5 6 7 10 11 12 13 15 20 23						
R104	VOLTAGE=1.8V	PP1V8 GRAPE	12 24						
R105	VOLTAGE=1.8V	PP1V8 OSCAR	12 19 22						
R106	VOLTAGE=1.2V	PP1V2 NAND VDDT	6						
R107	VOLTAGE=1.8V	PP EXTMIC BIAS FILT IN	10			R110	VOLTAGE=1.2V	PP1V2 RCAM CONN	23
R108	VOLTAGE=1.8V	BOARD ID2	3 27			R111	VOLTAGE=1.8V	PP1V8 RCAM CONN	23
R109	VOLTAGE=1.2V	PP1V2	2 4 5 11 12						
R110	VOLTAGE=5.0V	PP E75 TO TRISTAR ACC1 CONN	18 25			R122	VOLTAGE=3.0V	PP2V85 CAM VDD	11 23
R111	VOLTAGE=5.0V	PP E75 TO TRISTAR ACC1	17 18			R123	VOLTAGE=1.8V	PP2V85 RCAM AVDD CONN	23
R112	VOLTAGE=22.0V	PP LCM BL ANODE	15 20			R124	VOLTAGE=1.8V	PP CUMULUS VDDCORE	24
R113	VOLTAGE=0.2V	PP LCM BL CAT2	15 20			R125	VOLTAGE=1.2V	PP CUMULUS VDDANA	24
R114	VOLTAGE=0.2V	PP LCM BL CAT1	15 20			R126	VOLTAGE=13.5V	PP SAGE TO TOUCH VCPH CONN	24
R115	VOLTAGE=0.2V	PP LCM BL CAT2 CONN	20 25			R127	VOLTAGE=-12V	PP SAGE TO TOUCH VCPH CONN	24
R116	VOLTAGE=0.2V	PP LCM BL CAT1 CONN	20 25			R128	VOLTAGE=13.5V	PP SAGE TO TOUCH VCPH	24
						R129	VOLTAGE=-12V	PP SAGE TO TOUCH VCPH	24
R118	VOLTAGE=-5.7V	PP5V7 SAGE AVDDN	15 20 24			R130	VOLTAGE=-12V	PP SAGE VCPH F	24
R119	VOLTAGE=1.2V	PP1V2 OSCAR	12 22			R131	VOLTAGE=5.7V	PP SAGE LX	24
R120	VOLTAGE=3.0V	PP3V0 MESA CONN	21			R132	VOLTAGE=17.0V	PP SAGE LX	24
R121	VOLTAGE=6V	PP6V0 LCM BOOST	16						
R122	VOLTAGE=5.0V	PP STRB_DRIVER TO LED_WARM	9 16						
R123	VOLTAGE=5.0V	PP STRB_DRIVER TO LED_COOL	8 16						
						R135	VOLTAGE=1.8V	PP PMU VREF	13
						R136	VOLTAGE=14V	PP SAGE_VBST_OUTH	24
						R140	VOLTAGE=5.0V	PP TIGRIS_VBUS_DET	14
R137	VOLTAGE=1.8V	PP CODEC TO MIC1 BIAS	10 18						
R138	VOLTAGE=1.8V	PP EXTMIC BIAS IN	10						
R139	VOLTAGE=1.8V	PP EXTMIC BIAS FILT	10						
R140	VOLTAGE=1.8V	PP CODEC TO FRONTMIC3 BIAS	10 11			R145	VOLTAGE=2.5V	PP PMU_VDD_REF	13
R141	VOLTAGE=1.8V	PP CODEC_TO_REARMIC2_BIAS	8 10			R146	VOLTAGE=1.8V	PP EXTMIC BIAS	10
R142	VOLTAGE=1.8V	PP CODEC FILT+	10			R147	VOLTAGE=1.8V	PP1V8 XTAL	2
R143	VOLTAGE=2.2V	PP CODEC SPKR_VO	10			R148	VOLTAGE=1.8V	PP PMU_VDD_RTC	13
R144	VOLTAGE=2.5V	PP CODEC VCPHFLT	10						
R145	VOLTAGE=2.5V	PP CODEC VCPHFLT+	10			R150	VOLTAGE=4.6V	PP BATT_VCC	14 16 25 40 45 46
R146	VOLTAGE=2.5V	PP CODEC VHP_FLVN	10			R151	VOLTAGE=1.8V	PP1V8 MESA CONN	21
R147	VOLTAGE=0.2V	PP CODEC VHP_FLVX	10			R152	VOLTAGE=3.0V	PP3V0 PROX CONN	11
R148	VOLTAGE=2.5V	PP CODEC VHP_FLVY	10						
R149	VOLTAGE=1.8V	PP1V8 RCAM CONN	11						
R150	VOLTAGE=3.0V	PP2V85 RCAM AVDD CONN	11			R160	VOLTAGE=1.0V	PP0V95_FIXED_SOC	4 7 12
R151	VOLTAGE=1.8V	PP CODEC TO FRONTMIC3_BIAS_CONN	11			R161	VOLTAGE=1.0V	PP0V95_FIXED_SOC_RCIE	7
R152	VOLTAGE=3.0V	PP3V0_ALS_CONN	11			R162	VOLTAGE=1.2V	PP1V2_PLL	2
R153	VOLTAGE=1.2V	PP1V2_RCAM_VDDIO_CONN	11			R163	VOLTAGE=1.0V	PP_BUCK5_LX1	12
R154	VOLTAGE=5.0V	PP5V0_USB	12 14 17 18 25			R164	VOLTAGE=1.0V	PP_VAR_SOC	5 12
R155	VOLTAGE=5.0V	PP5V0_USB_TO_PMU	12						
R156	VOLTAGE=4.6V	PP_BUCK5_LX0	12						
R157	VOLTAGE=4.6V	PP_BUCK3_LX	12						
R158	VOLTAGE=4.6V	PP_BUCK4_LX	12						
R159	VOLTAGE=4.6V	PP_BUCK2_LX	12			R169	VOLTAGE=5.0V	PMID_CAP	14
R160	VOLTAGE=4.6V	PP_BUCK1_LX1	12			R170	VOLTAGE=5.0V	CHARGER_LDO	14
R161	VOLTAGE=4.6V	PP_BUCK1_LX2	12			R171	VOLTAGE=4.6V	CHG_BOOT	14
R162	VOLTAGE=4.6V	PP_BUCK0_LX3	12			R172	VOLTAGE=4.6V	CHG_LX	14
R163	VOLTAGE=4.6V	PP_BUCK0_LX2	12						
R164	VOLTAGE=4.6V	PP_BUCK0_LX1	12			R179	VOLTAGE=3.0V	VIBR_DRIVE_P	14 18
R165	VOLTAGE=4.6V	PP_BUCK0_LX0	12			R180	VOLTAGE=3.0V	VIBR_DRIVE_N	14 18
R166	VOLTAGE=6.0V	PP_CHESTNUT_LXP	15						
R167	VOLTAGE=6.0V	PP_CHESTNUT_CP	15						
R168	VOLTAGE=6.0V	PP_CHESTNUT_CN	15			R189	VOLTAGE=1.8V	PP_RCAM_AF_CONN	23
R169	VOLTAGE=5.7V	PP5V7_SAGE_AVDDH	15 24			R190	VOLTAGE=-14.0V	PP_SAGE_VBST_OUTH	24
R170	VOLTAGE=5.7V	PP5V7_LCM_AVDDH	15 20			R191	VOLTAGE=-12.0V	PP_SAGE_TO_TOUCH_VCPH_FILT	24
R171	VOLTAGE=5.1V	PP5V1_GRAPE_VDDH	15 24			R192	VOLTAGE=2.7V	PP_BB_VDD_2V7_CONN	18
R172	VOLTAGE=22.0V	PP_WLED_LX	15						
R173	VOLTAGE=18.0V	PP18V0_MESA_SW	15						
R174	VOLTAGE=17.0V	PP17V0_MOTANK_IDPIN	15						
R175	VOLTAGE=16.5V	PP16V5_MESA	15 21 25						
R176	VOLTAGE=8.0V	PP_SPKAMP_SW	16						
R177	VOLTAGE=8.0V	PP_I19_VBOOST	16						
R178	VOLTAGE=1.8V	PP_SPKAMP_FILT	16						
R179	VOLTAGE=1.8V	PP_SPKAMP_LDO_FILT	16						

PAGE TITLE		SYSTEM:VOLTAGE PROPERTIES	
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RADIO_MLB HIERARCHICAL SYMBOL

POWER

VCC_MAIN, VBAT GOES TO RADIO_MLB DIRECTLY
CHECK ALL PAGES IN RF SIDE!

CELLULAR HOUSE KEEPING

3	AP_TO_RADIO_ON_L	MAKE_BASE+TRUE	I325	RADIO_ON_L	30 32
3	BB_TO_AP_RESET_DET_L	MAKE_BASE+TRUE	I324	BB_RESET_DET_L	30 35
13	PMU_TO_BB_RST_L	MAKE_BASE+TRUE	I326	RF_PMIC_RESET_L	30 32
3	AP_TO_BB_RST_L	MAKE_BASE+TRUE	I327	BB_RST_L	30 32
3	AP_TO_BB_WAKE_MODEM	MAKE_BASE+TRUE	I329	AP_WAKE_MODEM	35
13	BB_TO_PMU_HOST_WAKE_L	MAKE_BASE+TRUE	I328	BB_WAKE_HOST_L	30 35
3	BB_TO_AP_IPC_GPIO	MAKE_BASE+TRUE	I331	BB_IPC_GPIO	35
16	BB_TO_LEDDR_V_GSM_BLANK	MAKE_BASE+TRUE	I330	GSM_TXBURST_IND	35
3	BB_TO_AP_GPS_SYNC	MAKE_BASE+TRUE	I332	BB_GPS_SYNC	30 35

HSIC IPC

2	50_AP_BI_BB_HSIC1_DATA	MAKE_BASE+TRUE	I368	50_BB_HSIC_DATA	30 34
3	50_AP_BI_BB_HSIC1_STB	MAKE_BASE+TRUE	I369	50_BB_HSIC_STROBE	30 34
3	AP_TO_BB_HOST_RDY	MAKE_BASE+TRUE	I371	BB_HOST_RDY	30 35
3	BB_TO_AP_DEVICE_RDY	MAKE_BASE+TRUE	I370	BB_DEVICE_RDY	30 35
3	BB_TO_AP_IPC_GPIO1	MAKE_BASE+TRUE	I372	BB_IPC_GPIO1	35

UART IPC

3	AP_TO_BB_UART2_RTS_L	MAKE_BASE+TRUE	I373	BB_UART_CTS_L	30 35
3	BB_TO_AP_UART2_CTS_L	MAKE_BASE+TRUE	I374	BB_UART_RTS_L	30 35
17 3	AP_TO_BB_UART2_TXD	MAKE_BASE+TRUE	I374	BB_UART_RXD	30 35
17 3	BB_TO_AP_UART2_RXD	MAKE_BASE+TRUE	I375	BB_UART_TXD	30 35

AUDIO I2S

3	45_AP_TO_BB_I2S3_BCLK	MAKE_BASE+TRUE	I377	BB_I2S_CLK	35
3	AP_TO_BB_I2S3_DOUT	MAKE_BASE+TRUE	I378	BB_I2S_RXD	30 35
3	BB_TO_AP_I2S3_DIN	MAKE_BASE+TRUE	I379	BB_I2S_TXD	30 35
3	AP_TO_BB_I2S3_LRCLK	MAKE_BASE+TRUE	I380	BB_I2S_WS	30 35

OSCAR UART

22	OSCAR_TO_BB_UART_TXD	MAKE_BASE+TRUE	I382	BB_OTHER_RXD	30 35
22	BB_TO_OSCAR_UART_RXD	MAKE_BASE+TRUE	I381	BB_OTHER_TXD	30 35

BB DEBUG INTERFACES

3	AP_TO_BB_COREDUMP	MAKE_BASE+TRUE	I384	BB_CORE_DUMP	30 35
13	PMU_TO_BB_VBUS_DET	MAKE_BASE+TRUE	I387	BB_USB_VBUS	30 34
17	90_TRISTAR_BI_BB_USB_N	MAKE_BASE+TRUE	I386	90_BB_USB_N	30 34
17	90_TRISTAR_BI_BB_USB_P	MAKE_BASE+TRUE	I388	90_BB_USB_P	30 34

RADIO ANTENNA CONTROL

18	PP_BB_VDD_2V7	MAKE_BASE+TRUE	I389	PP_LDO14_RFSW	31 41 42
18	BB_GPIO0	MAKE_BASE+TRUE	I390	BB_LAT_GPIO0	35
18	BB_GPIO2	MAKE_BASE+TRUE	I391	BB_LAT_GPIO2	35
18	BB_GPIO3	MAKE_BASE+TRUE	I392	BB_LAT_GPIO3	35
18	BB_GPIO4	MAKE_BASE+TRUE	I394	BB_LAT_GPIO4	35

FCT TESTING

13	RADIO_TO_PMU_ADC_SMPS1	MAKE_BASE+TRUE	I395	ADC_SMPS1	30
13	RADIO_TO_PMU_ADC_PP_LDO11_VDDIO	MAKE_BASE+TRUE	I396	ADC_PP_LDO11	30
13	RADIO_TO_PMU_ADC_PP_LDO5_SIM	MAKE_BASE+TRUE	I398	ADC_PP_LDO5	30
13	RADIO_TO_PMU_ADC_SMPS4	MAKE_BASE+TRUE	I397	ADC_SMPS4	30

UPPER RADIO ANTENNA CONTROL

25	50_AP_WIFI_5G_CONN_ANT	MAKE_BASE+TRUE	I410	50_WIFI_5G_CONN_ANT	50
25	50_AP_UAT_FEED	MAKE_BASE+TRUE	I409	50_UPPER_ANT_FEED	50
	UAT_ANT_GND	MAKE_BASE+TRUE	I411	ANT_GND	50
29 26 17 15 12	PP3V0_TRISTAR	MAKE_BASE+TRUE	I404	PAC_VDD_3V0	53
25 8	NORTH_AC_GND_SCREW	MAKE_BASE+TRUE	I412	NORTH_ANT_GND	50

POWER

26 17 15 14 13 12 10 4 3	PP1V8_SDRAM	MAKE_BASE+TRUE	I314	PP_WL_BT_VDDIO_AP	51
		MAKE_BASE+TRUE	I315	PP_STOCKHOLM_IVR_S2P	52 54
		MAKE_BASE+TRUE	I407	REFE_VIO_S2P	53

WLAN/BT HOUSE KEEPING

13	45_PMU_TO_WLAN_CLK32K	MAKE_BASE+TRUE	I316	CLK32K_AP	30 51
13	PMU_TO_WLAN_REG_ON	MAKE_BASE+TRUE	I317	WLAN_REG_ON	30 51
13	WLAN_TO_PMU_HOST_WAKE	MAKE_BASE+TRUE	I318	HOST_WAKE_WLAN	30 51
13	PMU_TO_BT_REG_ON	MAKE_BASE+TRUE	I319	BT_REG_ON	30 51
3	AP_TO_BT_WAKE	MAKE_BASE+TRUE	I320	WAKE_BT	30 51
13	BT_TO_PMU_HOST_WAKE	MAKE_BASE+TRUE	I321	HOST_WAKE_BT	51

3	AP_TO_WLAN_JTAG_SWCLK	MAKE_BASE+TRUE	I333	WLAN_JTAG_SWCLK	30 51
3	AP_TO_WLAN_JTAG_SWDIO	MAKE_BASE+TRUE	I334	WLAN_JTAG_SWDIO	30 51
13	WLAN_TO_PMU_PCIE_WAKE_L	MAKE_BASE+TRUE	I335	WLAN_PCIE_WAKE_L	30 51
3	AP_TO_WLAN_DEVICE_WAKE	MAKE_BASE+TRUE	I336	PCIE_DEV_WAKE	30 51
3	90_WLAN_TO_AP_PCIE1_RXDP_P	MAKE_BASE+TRUE	I337	90_WLAN_PCIE_TDP	30 51
3	90_WLAN_TO_AP_PCIE1_RXDP_N	MAKE_BASE+TRUE	I338	90_WLAN_PCIE_TDN	30 51
3	90_AP_TO_WLAN_PCIE1_TXDP_P	MAKE_BASE+TRUE	I338	90_WLAN_PCIE_RDP	30 51
3	90_AP_TO_WLAN_PCIE1_TXDP_N	MAKE_BASE+TRUE	I339	90_WLAN_PCIE_RDN	30 51
3	90_AP_TO_WLAN_PCIE1_REFCLK1_P	MAKE_BASE+TRUE	I342	90_WLAN_PCIE_REFCLK_P	51
3	90_AP_TO_WLAN_PCIE1_REFCLK1_N	MAKE_BASE+TRUE	I341	90_WLAN_PCIE_REFCLK_N	51
3	WLAN_TO_AP_PCIE1_CLKREQ_L	MAKE_BASE+TRUE	I344	WLAN_PCIE_CLKREQ_L	30 51
3	AP_TO_WLAN_PCIE1_RST_L	MAKE_BASE+TRUE	I343	WLAN_PCIE_PERST_L	30 51

WLAN HSIC IPC

3	WLAN_TO_AP_UART4_RXD	MAKE_BASE+TRUE	I345	WLAN_UART_TXD	30 51
3	AP_TO_WLAN_UART4_TXD	MAKE_BASE+TRUE	I348	WLAN_UART_RXD	30 51
3	WLAN_TO_AP_UART4_CTS_L	MAKE_BASE+TRUE	I347	WLAN_UART_RTS_L	30 51
3	AP_TO_WLAN_UART4_RTS_L	MAKE_BASE+TRUE	I346	WLAN_UART_CTS_L	30 51

BT UART IPC

3	AP_TO_BT_UART1_RTS_L	MAKE_BASE+TRUE	I349	BT_UART_CTS_L	51
3	BT_TO_AP_UART1_CTS_L	MAKE_BASE+TRUE	I352	BT_UART_RTS_L	51
3	AP_TO_BT_UART1_TXD	MAKE_BASE+TRUE	I351	BT_UART_RXD	30 51
3	BT_TO_AP_UART1_RXD	MAKE_BASE+TRUE	I350	BT_UART_TXD	30 51

BT AUDIO PCM


3	45_AP_TO_BT_I2S1_BCLK	MAKE_BASE+TRUE	I354	BT_PCM_CLK	51
3	AP_TO_BT_I2S1_DOUT	MAKE_BASE+TRUE	I353	BT_PCM_IN	51
3	BT_TO_AP_I2S1_DIN	MAKE_BASE+TRUE	I355	BT_PCM_OUT	51
3	AP_TO_BT_I2S1_LRCLK	MAKE_BASE+TRUE	I356	BT_PCM_SYNC	51

OSCAR STATES

22	OSCAR_TO_RADIO_CONTEXT_A	MAKE_BASE+TRUE	I358	OSCAR_CONTEXT_A	51
22	OSCAR_TO_RADIO_CONTEXT_B	MAKE_BASE+TRUE	I357	OSCAR_CONTEXT_B	51

STOCKHOLM

3	STOCKHOLM_TO_AP_UART3_CTS_L	MAKE_BASE+TRUE	I359	STOCKHOLM_RTS_L	30 52
3	AP_TO_STOCKHOLM_UART3_RTS_L	MAKE_BASE+TRUE	I360	STOCKHOLM_CTS_L	30 52
3	STOCKHOLM_TO_AP_UART3_RXD	MAKE_BASE+TRUE	I361	STOCKHOLM_UART_TXD	30 52
3	AP_TO_STOCKHOLM_UART3_TXD	MAKE_BASE+TRUE	I362	STOCKHOLM_UART_RXD	30 52
3	AP_TO_STOCKHOLM_DWLD_REQ	MAKE_BASE+TRUE	I362	STOCKHOLM_FW_DWLD_REQ	52
13	STOCKHOLM_TO_PMU_HOST_WAKE	MAKE_BASE+TRUE	I364	STOCKHOLM_HOST_WAKE	30 52
3	AP_TO_STOCKHOLM_EN	MAKE_BASE+TRUE	I365	STOCKHOLM_ENABLE	52
29 26 17 15 12	PP3V0_TRISTAR	MAKE_BASE+TRUE	I366	STOCKHOLM_VDD_MUX_3V0	54
3	AP_TO_STOCKHOLM_SIM_SEL	MAKE_BASE+TRUE	I367	STOCKHOLM_SIM_SEL	54
25	AP_TO_STOCKHOLM_ANT	MAKE_BASE+TRUE	I406	STOCKHOLM_ANT	52

PAGE TITLE	
CELL:ALIASES	
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AP INTERFACE & DEBUG CONNECTORS

PROBE POINTS

PP3105_RF	P2MM	SM 1	CLK32K_AP	29 51
PP3113_RF	P4MM	SM 1	BB_COEX_UART_RXD	35 51
PP3114_RF	P4MM	SM 1	BB_COEX_UART_TXD	35 51
PP3119_RF	P2MM	SM 1	BT_UART_TXD	29 51
PP3120_RF	P2MM	SM 1	BT_UART_RXD	29 51
PP3152_RF	P2MM	SM 1	WAKE_BT	29 51
PP3153_RF	P4MM	SM 1	WLAN_REG_ON	29 51
PP3154_RF	P4MM	SM 1	BT_REG_ON	29 51
PP3155_RF	P2MM	SM 1	HOST_WAKE_WLAN	29 51
PP3156_RF	P2MM	SM 1	WLAN_PCIE_WAKE_L	29 51
PP3157_RF	P2MM	SM 1	WLAN_PCIE_PERST_L	29 51
PP3158_RF	P4MM	SM 1	WLAN_PCIE_CLKREQ_L	29 51
PP3159_RF	P4MM	SM 1	PCIE_DEV_WAKE	29 51
PP3160_RF	P2MM	SM 1	WLAN_UART_RTS_L	29 51
PP3161_RF	P2MM	SM 1	WLAN_UART_CTS_L	29 51
PP3162_RF	P2MM	SM 1	WLAN_UART_RXD	29 51
PP3163_RF	P2MM	SM 1	WLAN_UART_TXD	29 51
PP3190_RF	P2MM	SM 1	WLAN_JTAG_SWDCLK	29 51
PP3191_RF	P2MM	SM 1	WLAN_JTAG_SWDIO	29 51

PP3121_RF	P2MM	SM 1	STOCKHOLM_HOST_WAKE	29 52
PP3122_RF	P4MM	SM 1	BB_REQUEST_XO_CLK	32 52
PP3123_RF	P2MM	SM 1	STOCKHOLM_UART_RXD	29 52
PP3124_RF	P2MM	SM 1	STOCKHOLM_UART_TXD	29 52
PP3125_RF	P2MM	SM 1	STOCKHOLM_CTS_L	29 52
PP3126_RF	P2MM	SM 1	STOCKHOLM_RTS_L	29 52
PP3128_RF	P4MM	SM 1	PP_PN65_VCC_SIM	52
PP3174_RF	P4MM	SM 1	STOCKHOLM_SIM_SWP	52 54
PP3179_RF	P4MM	SM 1	REF_CLK_FROM_BB	32 52
PP3183_RF	P4MM	SM 1	DSDS_SIM_CLK	34 54
PP3184_RF	P4MM	SM 1	DSDS_SIM_RESET	34 54
PP3186_RF	P4MM	SM 1	DSDS_SIM_DETECT	34
PP3187_RF	P4MM	SM 1	PP_LDO6	31 33 54
PP3188_RF	P4MM	SM 1	DSDS_SIM_SWP	54
PP3189_RF	P4MM	SM 1	DSDS_SIM_DATA_R	54
PP 3178_RF	P2MM	SM 1	BB_SIM_RESET	30 35
PP 3179_RF	P2MM	SM 1	BB_SIM_CLK	30 35
PP 3180_RF	P2MM	SM 1	BB_SIM_DATA	30 35
PP 3183_RF	P2MM	SM 1	BB_SIM_DETECT	30 35
PP 3184_RF	P2MM	SM 1	PP_LDO5	30 31 33 54

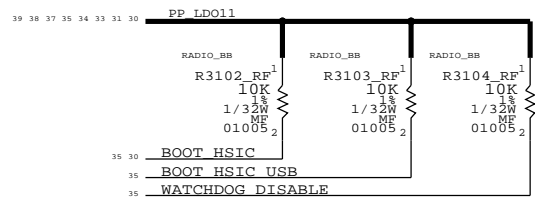
PP3115_RF	P4MM	SM 1	50_BB_HSIC_STROBE	29 34
PP3116_RF	P4MM	SM 1	50_BB_HSIC_DATA	29 34
PP3101_RF	P4MM	SM 1	BB_DEBUG_ERROR	35
PP3102_RF	P4MM	SM 1	RF_PMIC_RESET_L	29 32
PP3103_RF	P4MM	SM 1	PS_HOLD_PMIC	32
PP3127_RF	P4MM	SM 1	PMIC_RESOUT_L	32 34
PP3104_RF	P4MM	SM 1	MDM_CLK	32 34
PP3109_RF	P4MM	SM 1	PP_LDO11	30 31 33 34 35 37 38
PP3110_RF	P4MM	SM 1	RADIO_ON_L	29 32
PP3111_RF	P4MM	SM 1	SPMI_DATA	32 34
PP3112_RF	P4MM	SM 1	SPMI_CLK	32 34

PP3130_RF	P4MM	SM 1	BB_JTAG_RST_L	34
PP3131_RF	P4MM	SM 1	BB_JTAG_TCK	34
PP3132_RF	P4MM	SM 1	BB_JTAG_TMS	34
PP3133_RF	P4MM	SM 1	BB_JTAG_TDO	34
PP3134_RF	P4MM	SM 1	BB_JTAG_TDI	34
PP3135_RF	P4MM	SM 1	BB_JTAG_TEST_L	34
PP3136_RF	P4MM	SM 1	BB_DEBUG_STATUS	35
PP3137_RF	P4MM	SM 1	BB_CORE_DUMP	29 35
PP3138_RF	P4MM	SM 1	BB_USB_VBUS	29 34
PP3139_RF	P4MM	SM 1	90_BB_USB_N	29 34
PP3140_RF	P4MM	SM 1	90_BB_USB_P	29 34

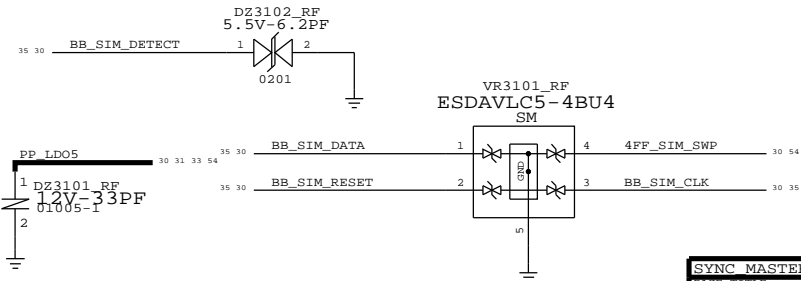
PP3141_RF	P4MM	SM 1	BB_UART_TXD	29 35
PP3142_RF	P4MM	SM 1	BB_UART_RXD	29 35
PP3143_RF	P4MM	SM 1	BB_UART_RTS_L	29 35
PP3144_RF	P4MM	SM 1	BB_UART_CTS_L	29 35
PP3145_RF	P4MM	SM 1	BB_HOST_RDY	29 35
PP3146_RF	P4MM	SM 1	BB_DEVICE_RDY	29 35
PP3147_RF	P4MM	SM 1	BB_GPS_SYNC	29 35
PP3148_RF	P4MM	SM 1	BB_WAKE_HOST_L	29 35
PP3149_RF	P4MM	SM 1	BB_RESET_DET_L	29 35
PP3150_RF	P4MM	SM 1	BB_RST_L	29 32
PP3151_RF	P4MM	SM 1	BOOT_HSIC	30 35

PP3170_RF	P4MM	SM 1	RFFE1_CLK	35 39 40 41 42 43 44
PP3171_RF	P4MM	SM 1	RFFE1_DATA	35 39 40 41 42 43 44
PP3172_RF	P4MM	SM 1	RFFE2_CLK	35 45 46 48
PP3173_RF	P4MM	SM 1	RFFE2_DATA	35 45 46 48
PP3175_RF	P4MM	SM 1	BB_I2S_WS	29 35
PP3176_RF	P4MM	SM 1	BB_I2S_RXD	29 35
PP3177_RF	P4MM	SM 1	BB_I2S_TXD	29 35
PP3178_RF	P4MM	SM 1	BB_OTHER_TXD	29 35
PP3179_RF	P4MM	SM 1	BB_OTHER_RXD	29 35

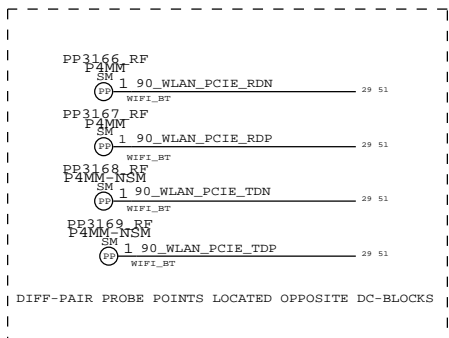
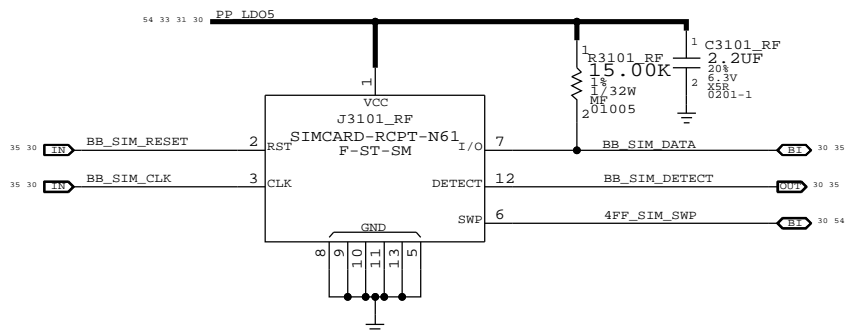
PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
197S0565	197S0593	ALTERNATE	Y3301_RF	KDS 19.2MHZ XTAL
197S0598	197S0593	ALTERNATE	Y3301_RF	AVX 19.2MHZ XTAL
138S00005	138S00003	ALTERNATE	C3216_RF	150F CAPACITOR
138S0739	138S0706	ALTERNATE	C4207_RF	1.00UF CAPACITOR
138S0945	138S0706	ALTERNATE	C4207_RF	1.00UF CAPACITOR
138S1103	138S0719	ALTERNATE	C4007_RF	4.7UF CAPACITOR
339S0231	339S0228	ALTERNATE	U5201_RF	CORONA MODULE USI
339S0242	339S0228	ALTERNATE	U5201_RF	CORONA MODULE TDK
155S00024	155S0950	ALTERNATE	F_TRI_RF	TRIPLEXER BIN2



SIM CARD ESD PROTECTION



SIM CARD CONNECTOR



33 31	VREG_SMPS1_0V90	1 0 2	ADC_SMPS1	29
38 37 35 34 33 31 30	PP_LDO11	1 0 2	ADC_PP_LDO11	29
54 33 31 30	PP_LDO5	1 0 2	ADC_PP_LDO5	29
31	VREG_SMPS4_2V075	1 0 2	ADC_SMPS4	29

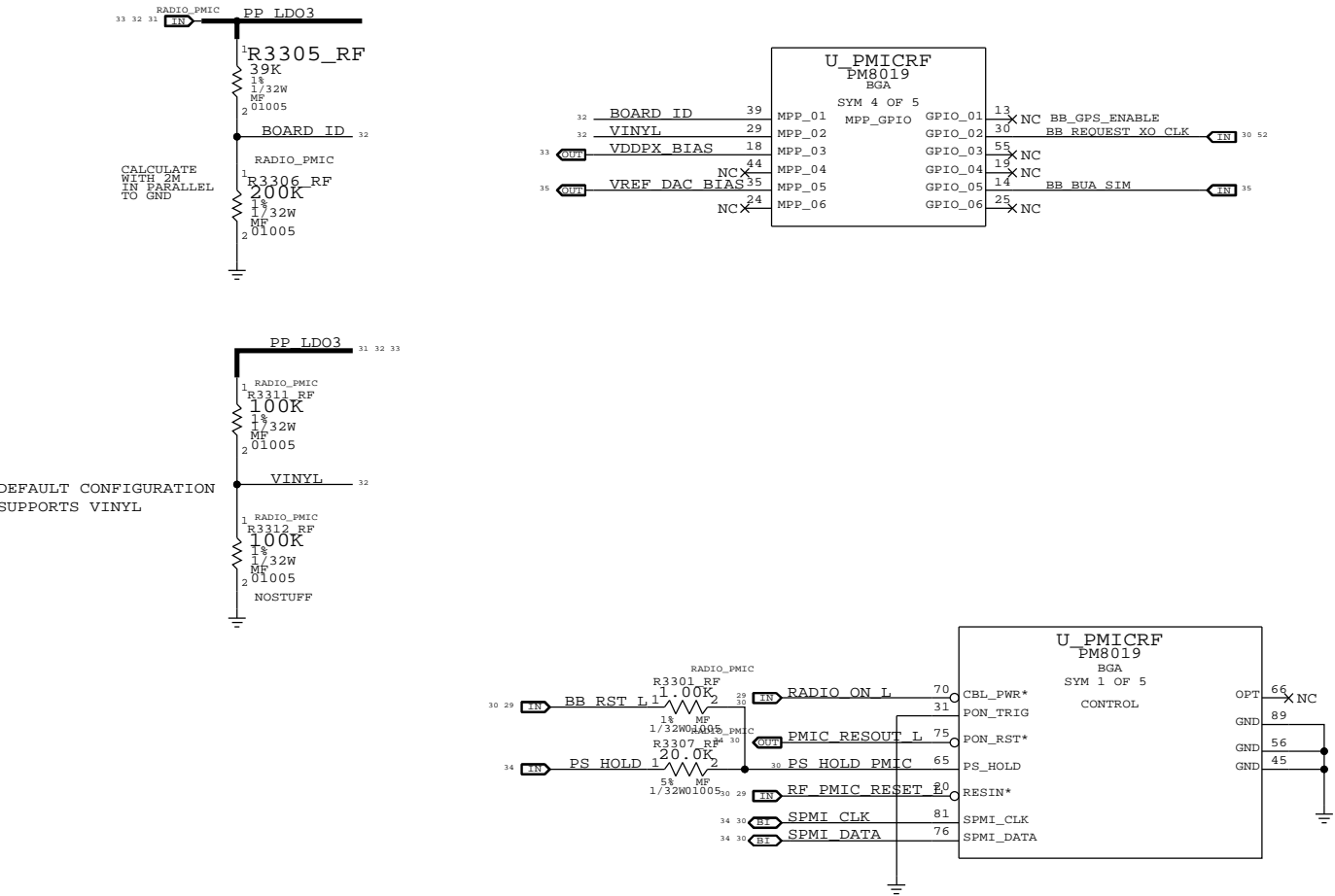
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AP INTERFACE & DEBUG CONNECTORS		DRAWING NUMBER	051-9903
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BASEBAND PMU (2 OF 2)

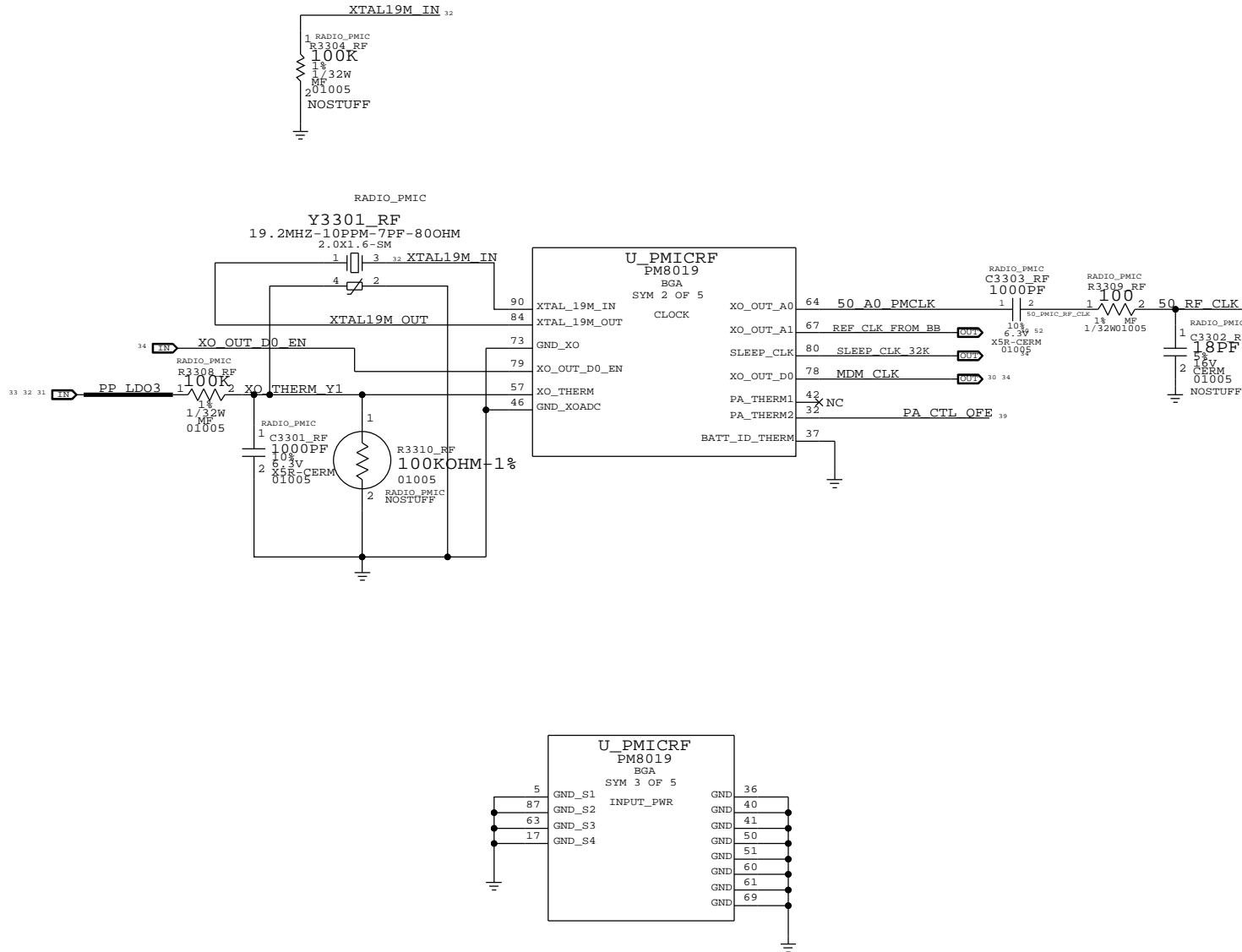
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C401
R411
L400
U404

BOARD ID	REVISION
0.00V	N61 PROTO_MLB1
0.50V	N61 DEV3
0.70V	N61 DEV4
0.90V	N61 PROTO_MLB2
1.10V	N61/N56 PROTO1
1.30V	N61/N56 PROTO2
1.40V	N61/N56 EVT1
1.50V	N61/N56 EVT2 (CARRIER)
1.60V	N61/N56 DVT
1.70V	N61/N56 PVT



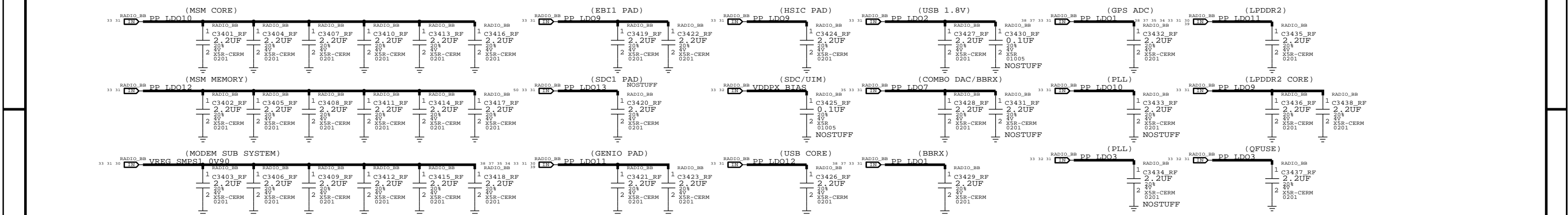
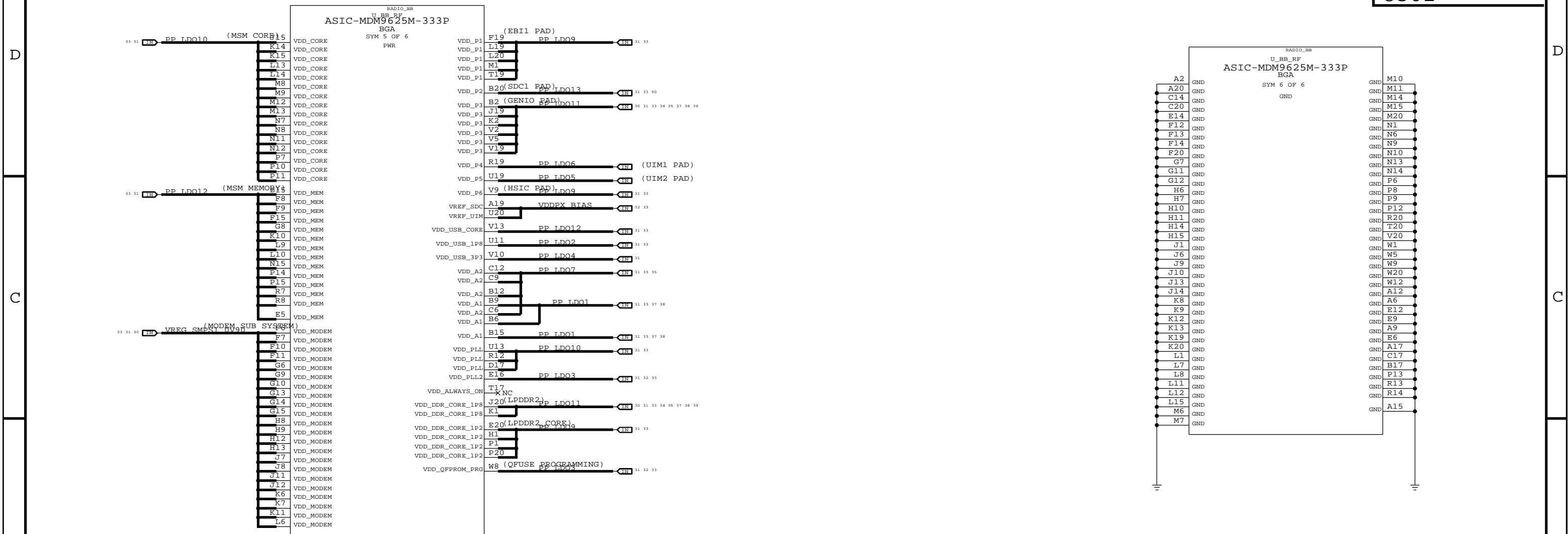
DEFAULT CONFIGURATION
SUPPORTS VINYL



BASEBAND PMU (2 OF 2)		
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		33 OF 55
		SHEET
		32 OF 54

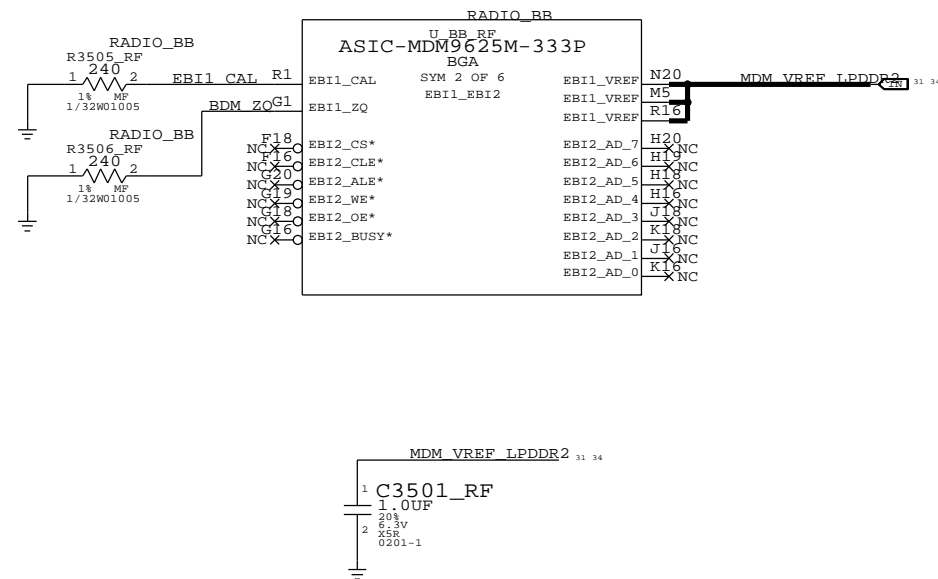
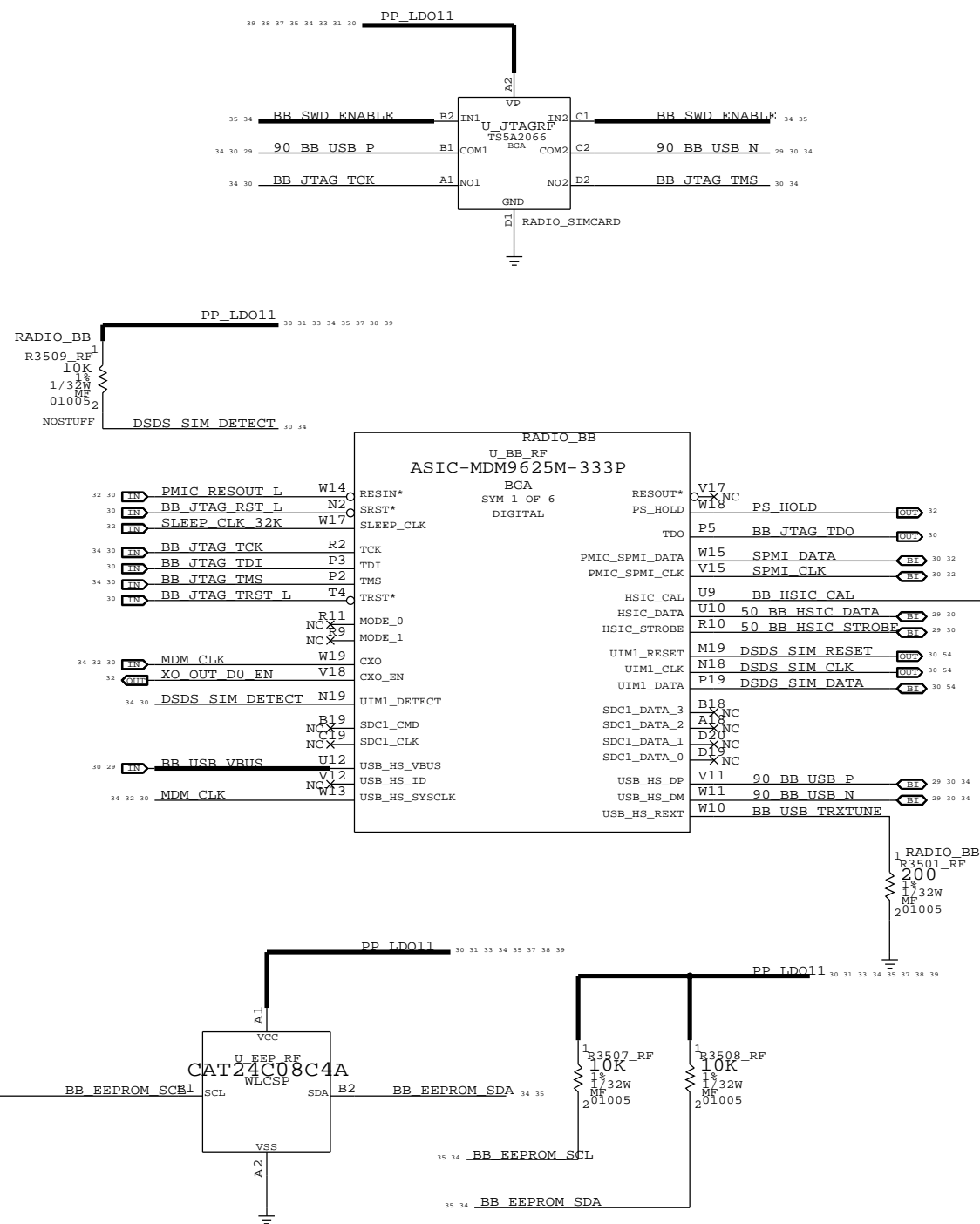
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C538
R500
L500
U502



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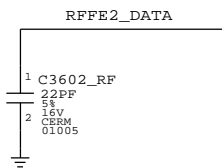
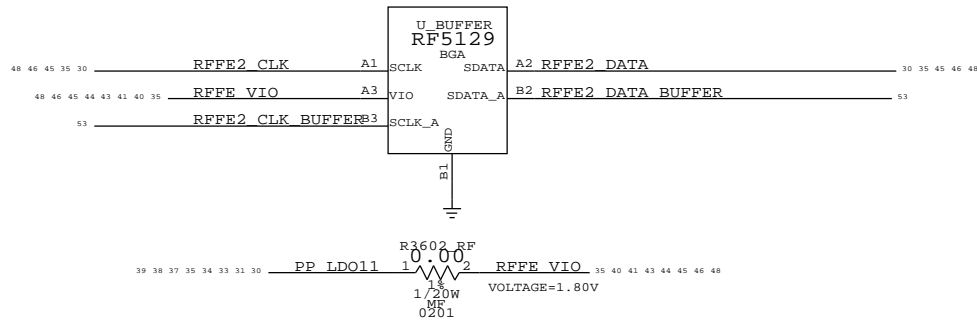
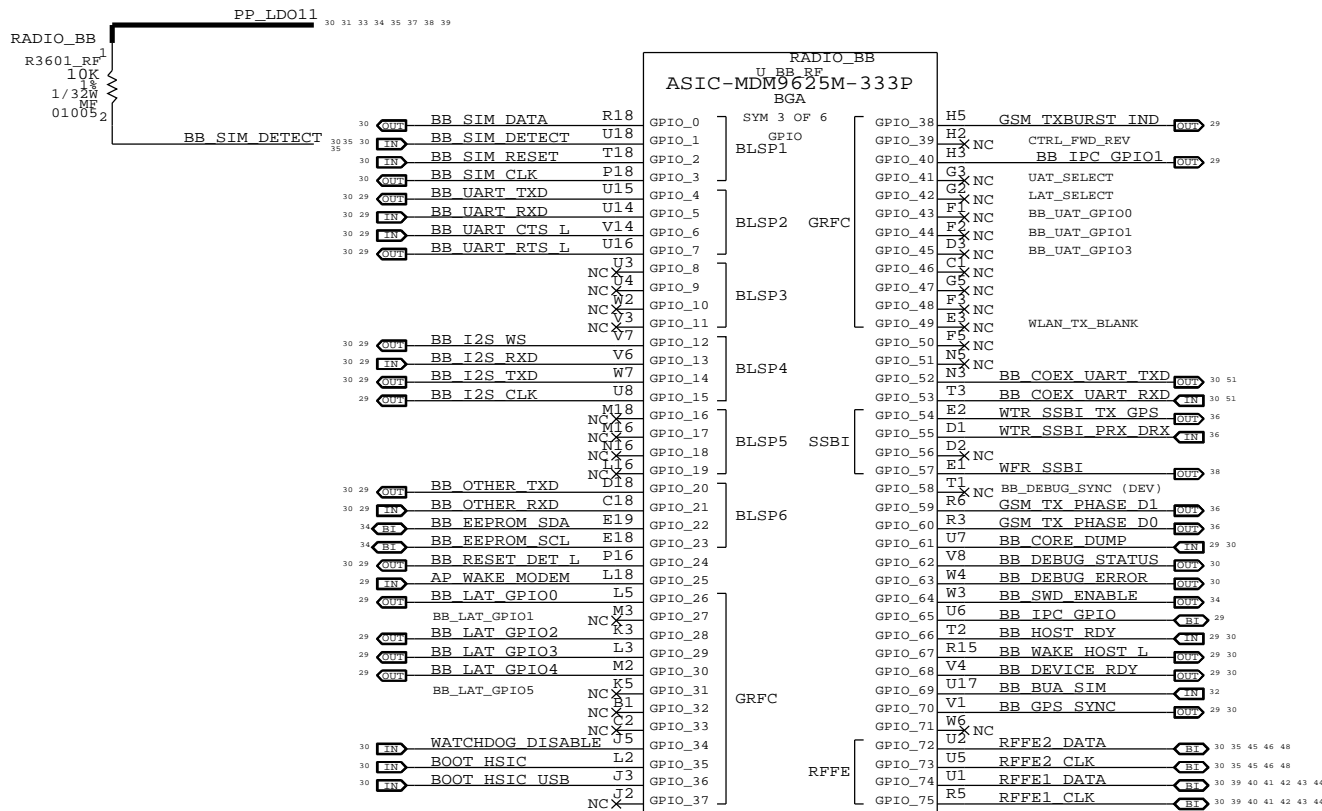
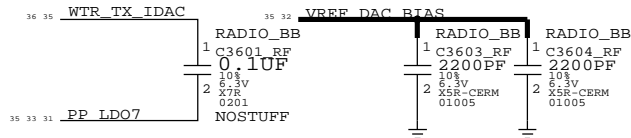
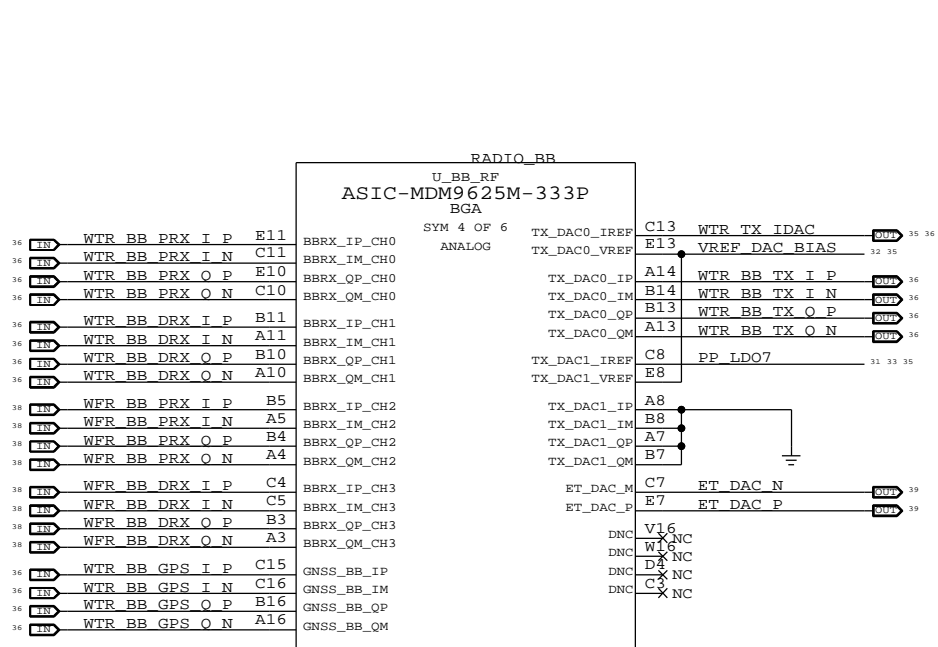
C600
R606
L600
U602



BASEBAND (3 OF 3)

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C704
R700
L700
U702



MOBILE DATA MODEM (2 OF 2)

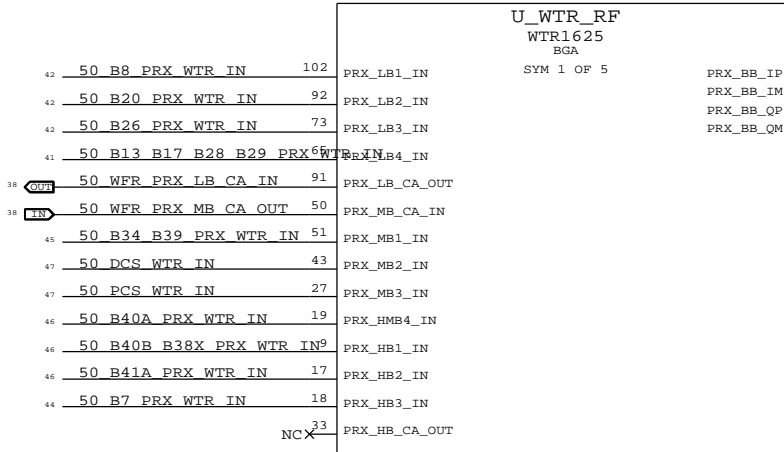
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		REVISION	7.0.0
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		SHEET	35 OF 54

WTR TRANSCEIVER (1 OF 2)

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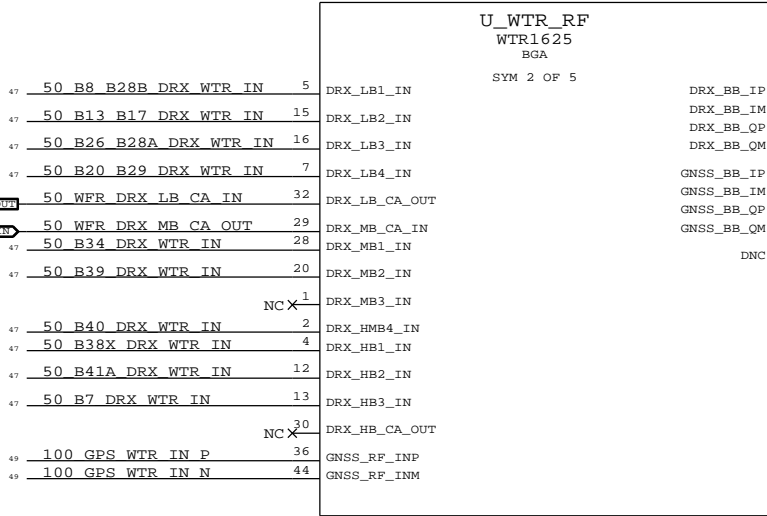
C802
R802
L800
U803

LB1	DC
LB2	DC
LB3	DC
LB4	DC
MB1	NO DC
MB2	DC
MB3	DC
HB1	NO DC
HB2	DC
HB3	DC
HMB4	NO DC

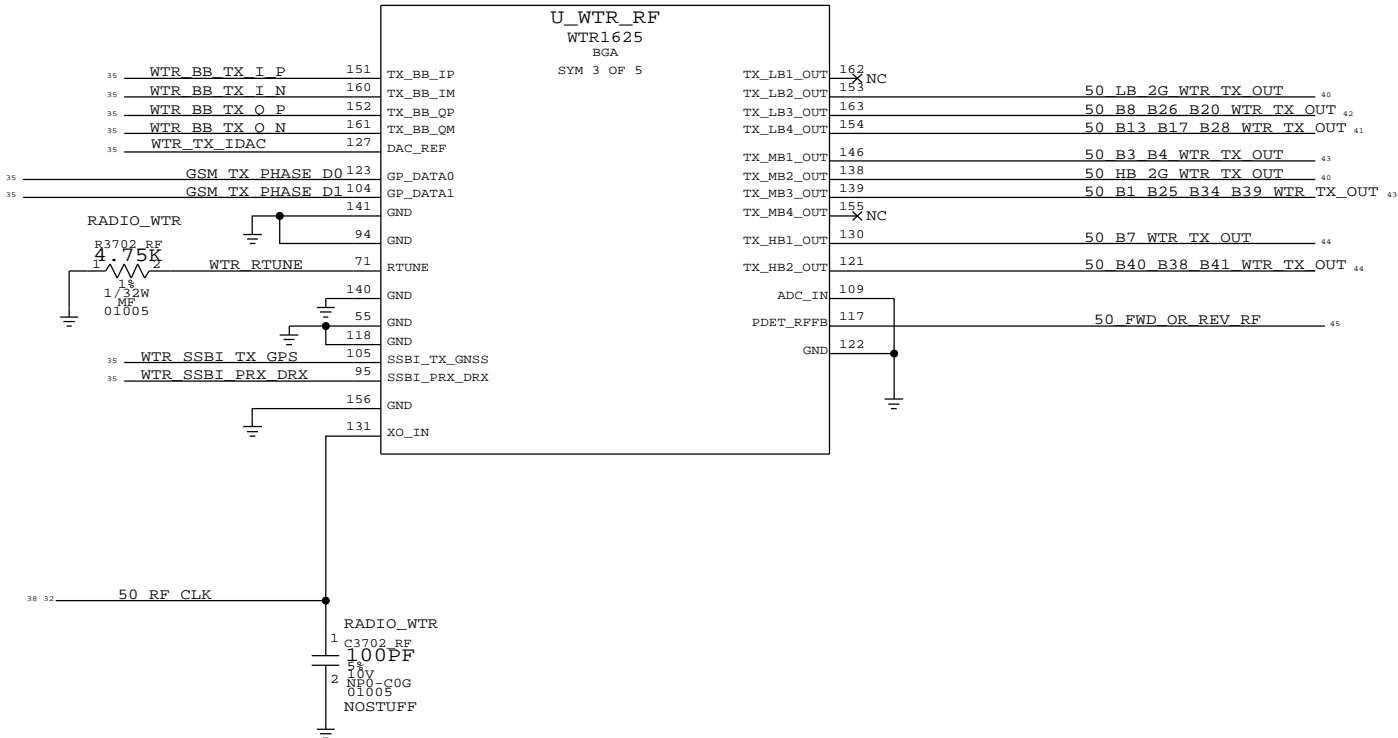


99	WTR_BB_PRX_I_P	35
108	WTR_BB_PRX_I_N	35
107	WTR_BB_PRX_O_P	35
97	WTR_BB_PRX_O_N	35

LB1	DC
LB2	DC
LB3	DC
LB4	DC
MB1	NO DC
MB2	DC
MB3	DC
HB1	NO DC
HB2	DC
HB3	DC
HMB4	NO DC




76	WTR_BB_DRX_I_P	35	RADIO_WTR
86	WTR_BB_DRX_I_N	35	RADIO_WTR
61	WTR_BB_DRX_O_P	35	RADIO_WTR
68	WTR_BB_DRX_O_N	35	RADIO_WTR
60	WTR_BB_GPS_I_P	35	RADIO_WTR
53	WTR_BB_GPS_I_N	35	RADIO_WTR
67	WTR_BB_GPS_O_P	35	RADIO_WTR
85	WTR_BB_GPS_O_N	35	RADIO_WTR



RF_CLK IS SHARED BETWEEN WTR AND WFR. LENGTH DIFFERENCE BETWEEN THE TWO SHOULD BE < 5MM.

RF TRANSCEIVER (1 OF 3)

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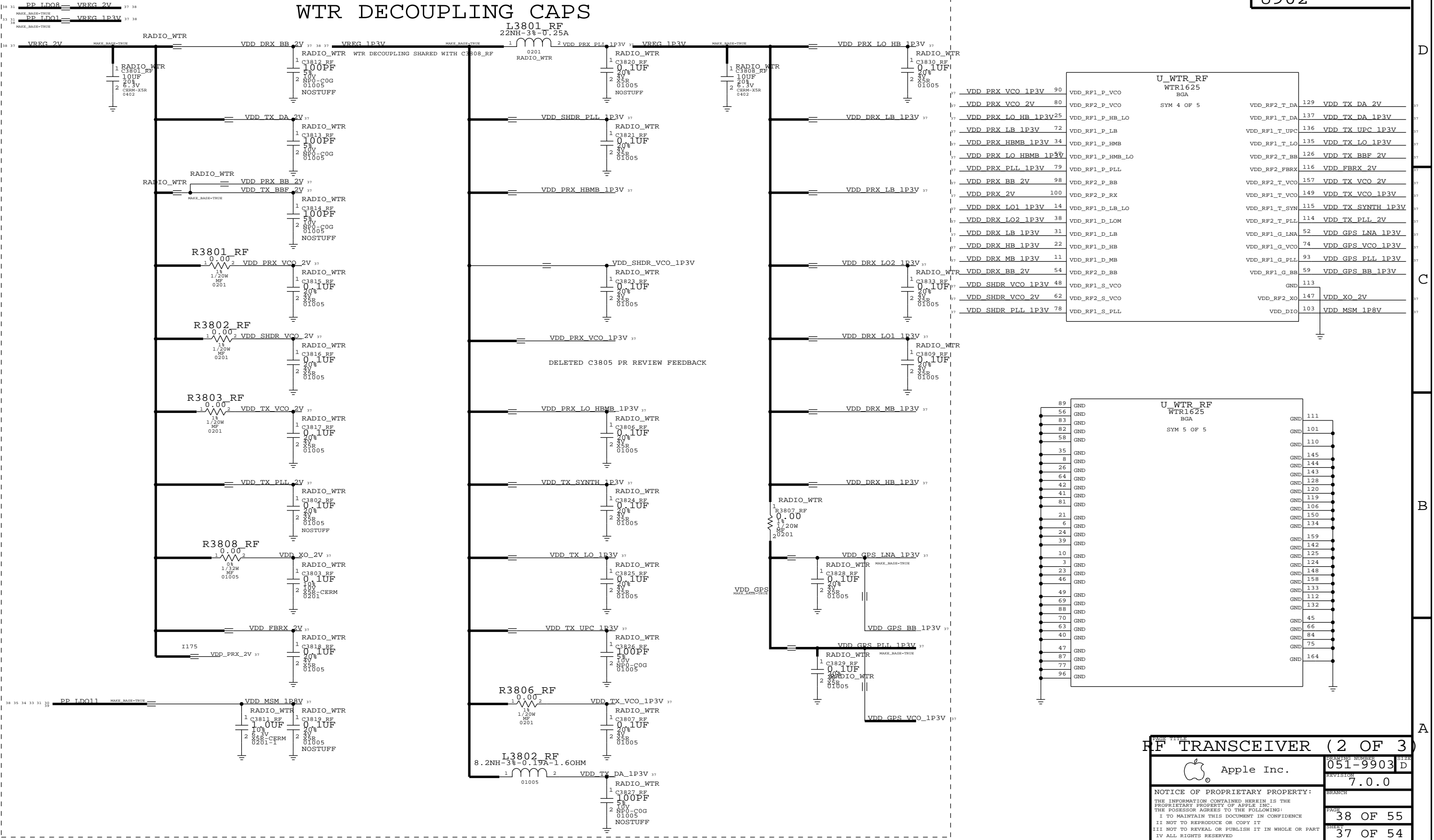
WTR TRANSCIEIVER (2 OF 2)

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C934
R926
L3802_RF
U902

WTR DECOUPLING CAPS

L3801 RF
22NH-3%-0.25A



RF TRANSCEIVER (2 OF 3)



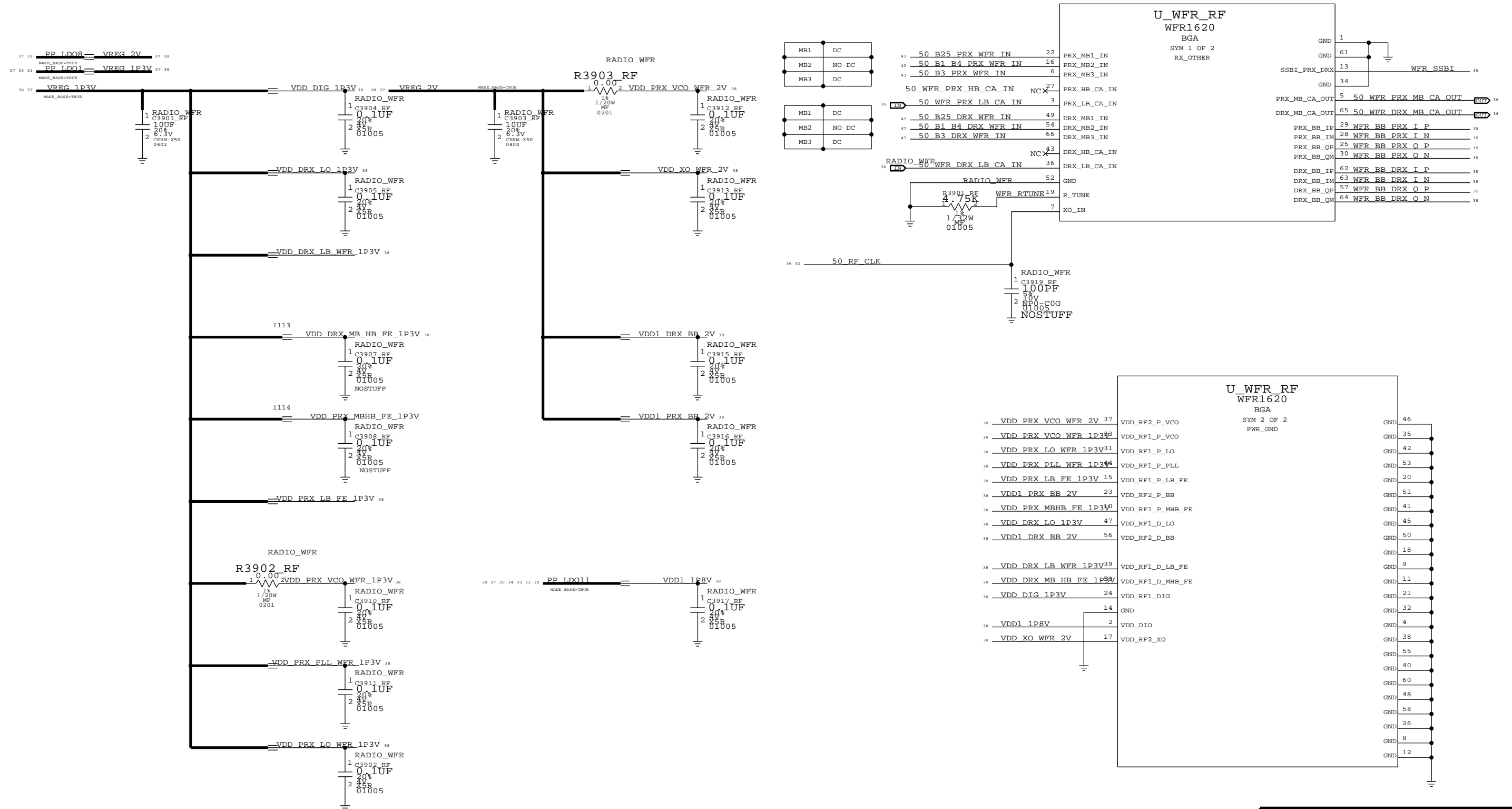
Apple Inc.


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REVISION		
7.0.0		
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PAGE		
38 OF 55		
SHEET		
37 OF 54		

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C1019
R1016
L1000
U1002

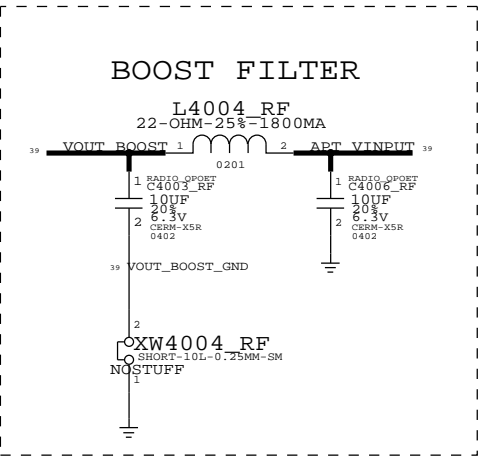
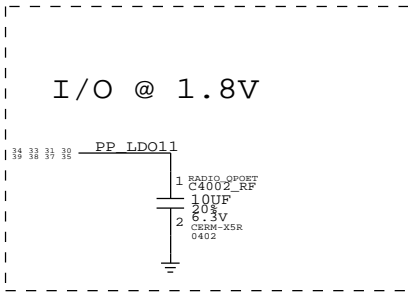
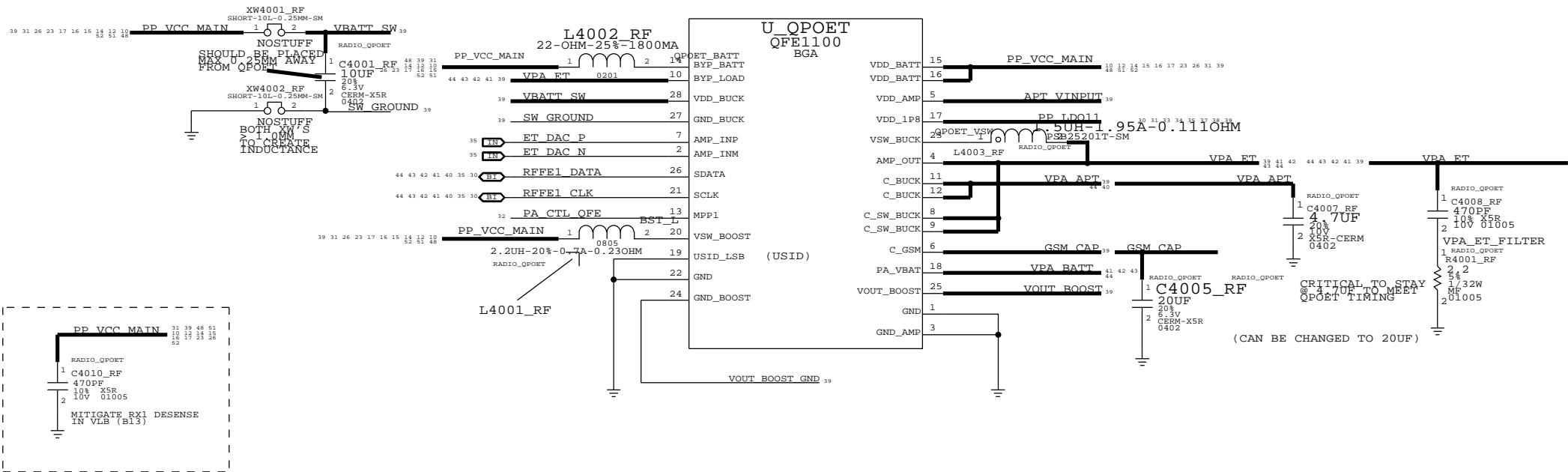



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QFE DCDC

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C1110
R1102
L1104
U1101

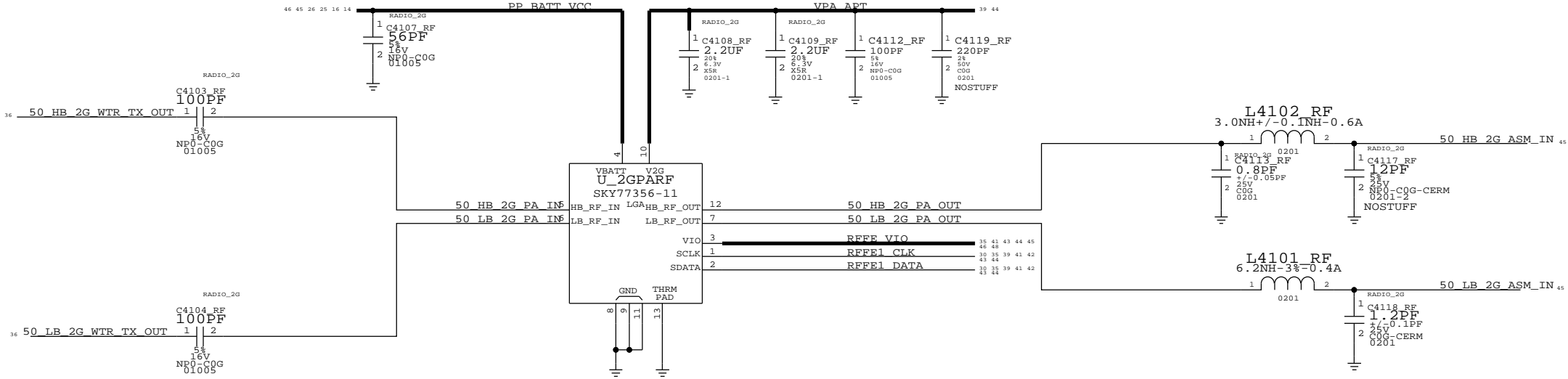



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2G PA

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C1208
R1200
L1204
U1201



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2G PA		
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VERY LOW BAND PAD (B13, B17, B28)

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C1332
R1300
L4215_RF
U1304

D

C

B

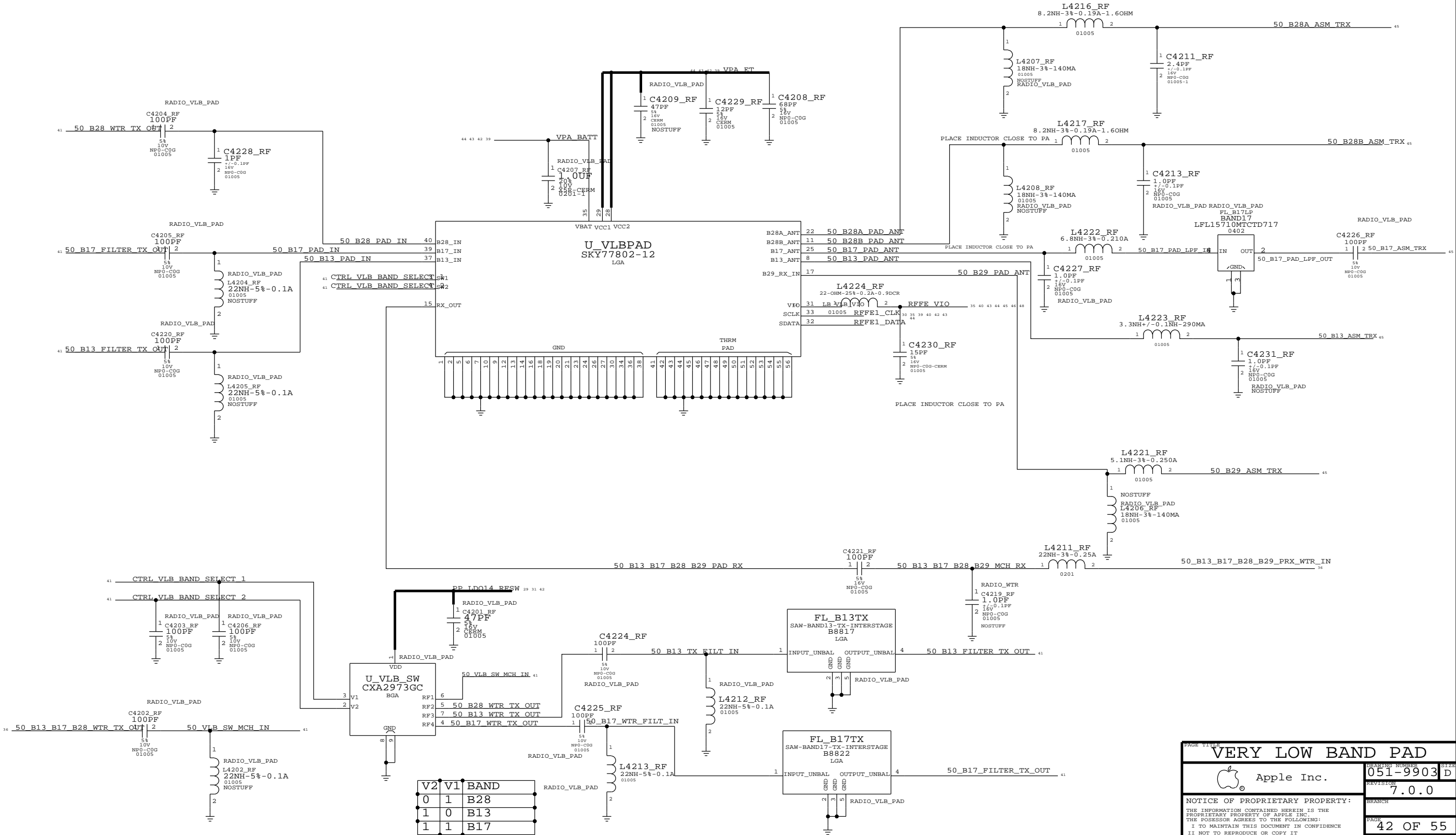
A

D

C

B

A



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MID BAND PAD (B1, B25, B3, B4, B34, B39)

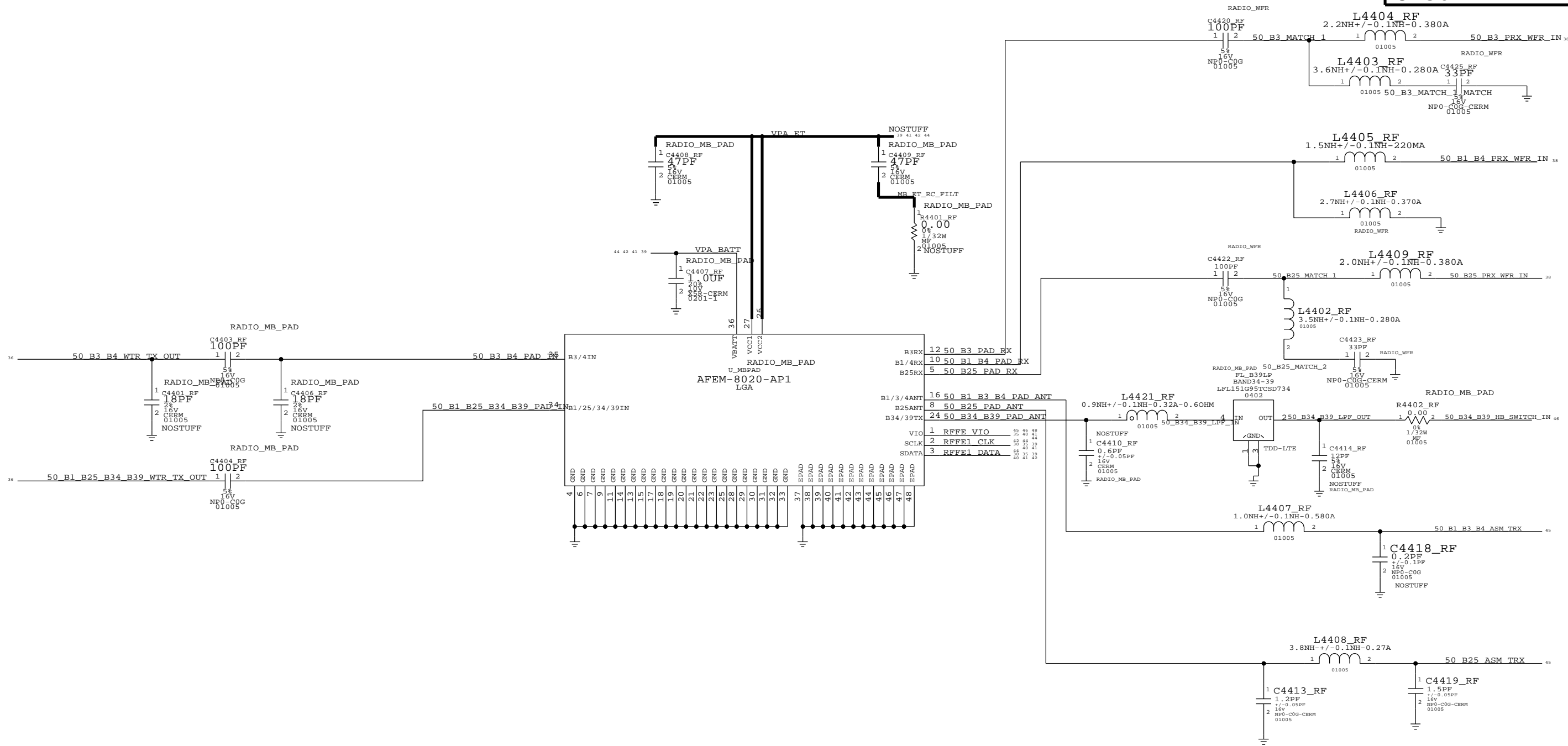
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C4426 RF

R1500

L4409 RF

U1501



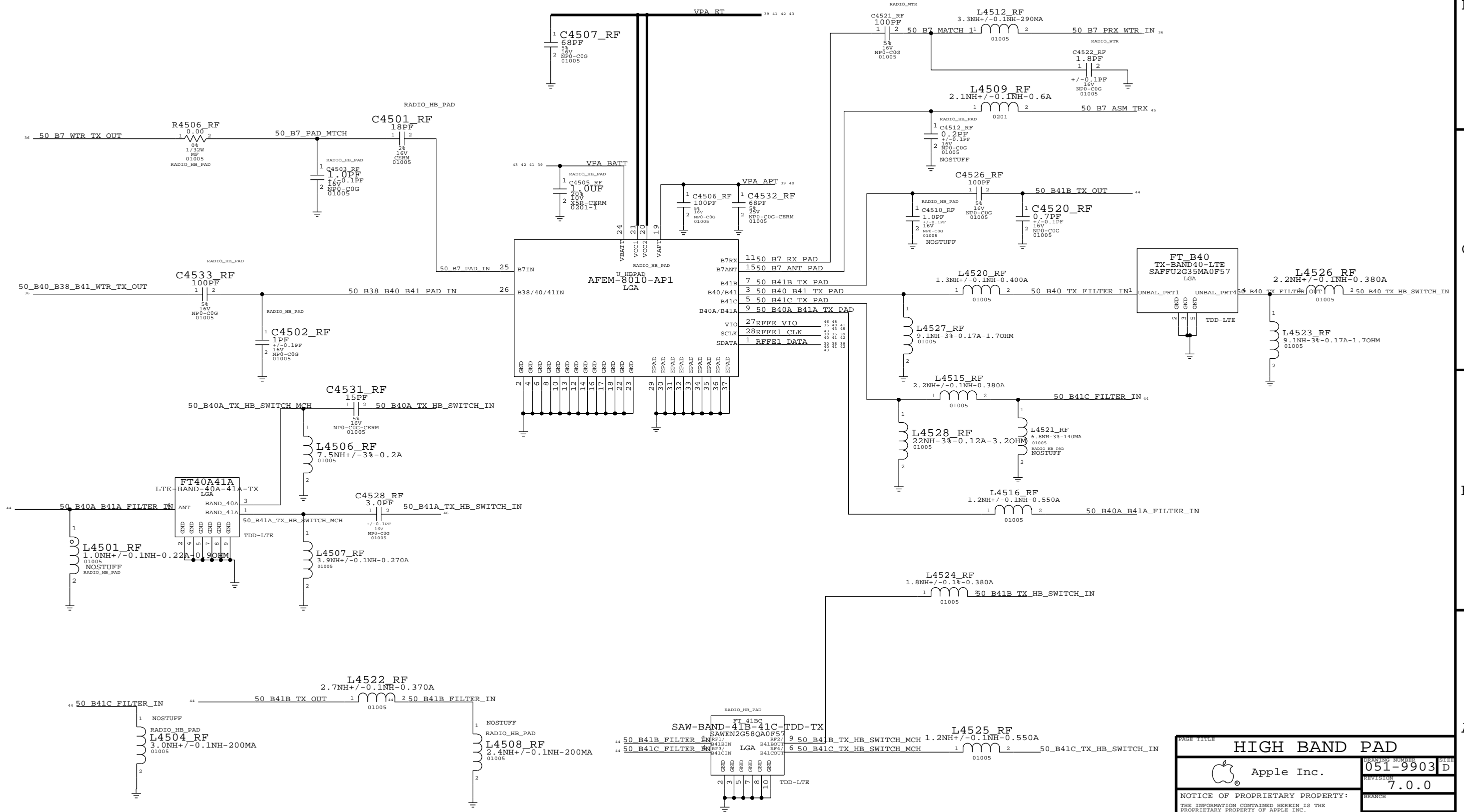
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HIGH BAND PAD (B7, B38, B40, B41, XGP)

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C4533_RF
R1600
L1616
U1601



HIGH BAND PAD



Apple Inc.

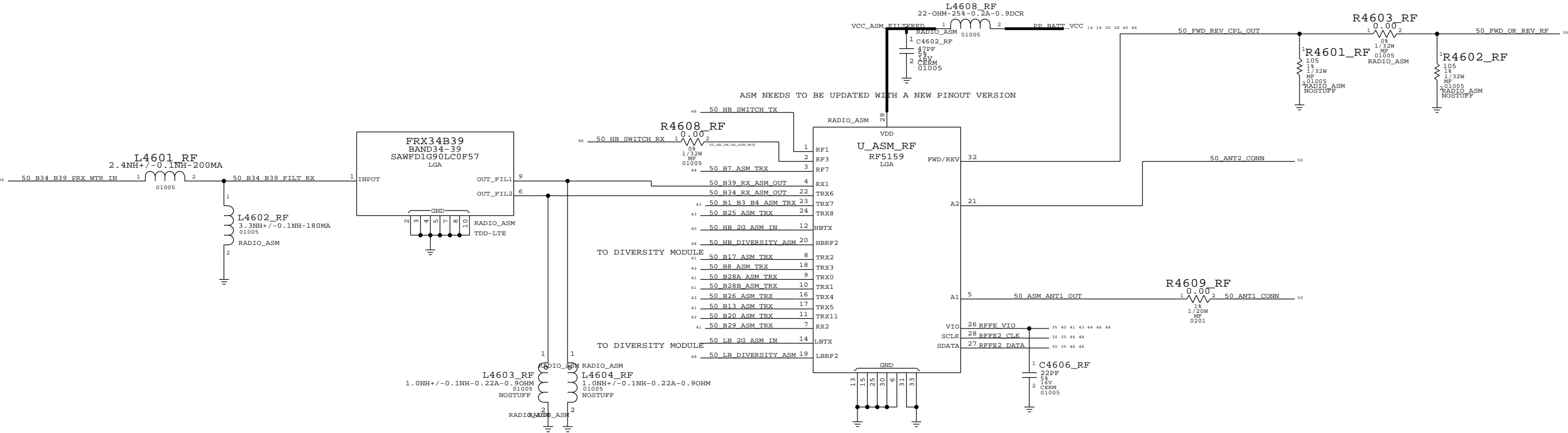
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ANTENNA SWITCH

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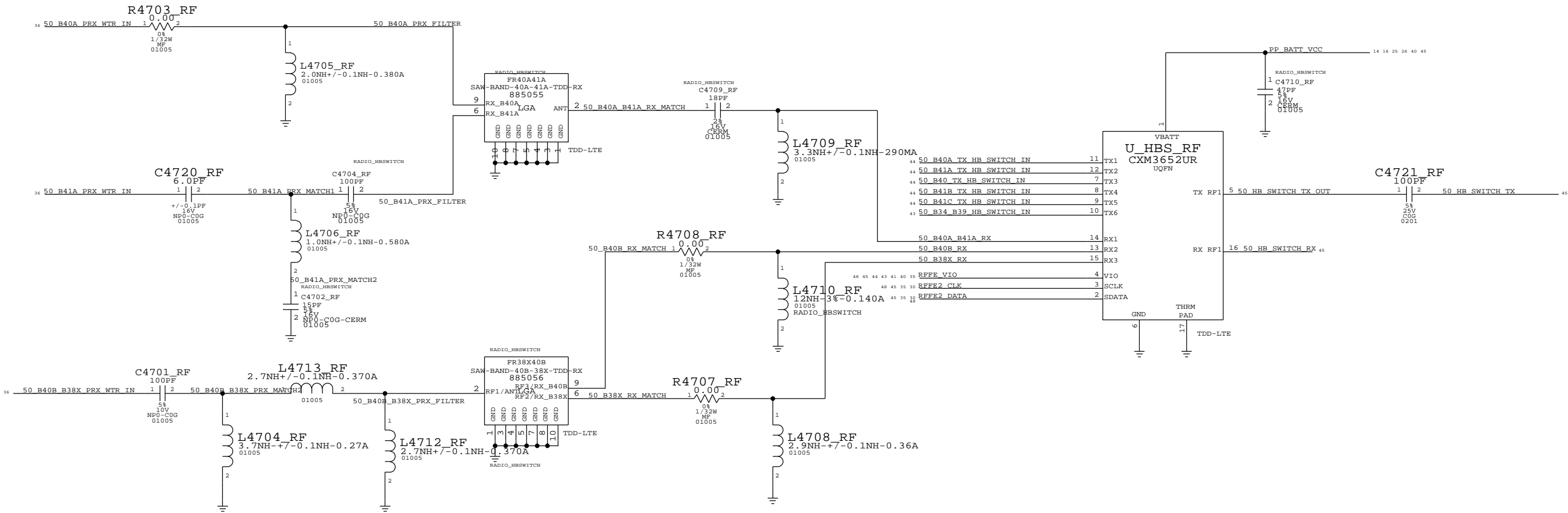
C1702
R1700
L4608_RF
U1702




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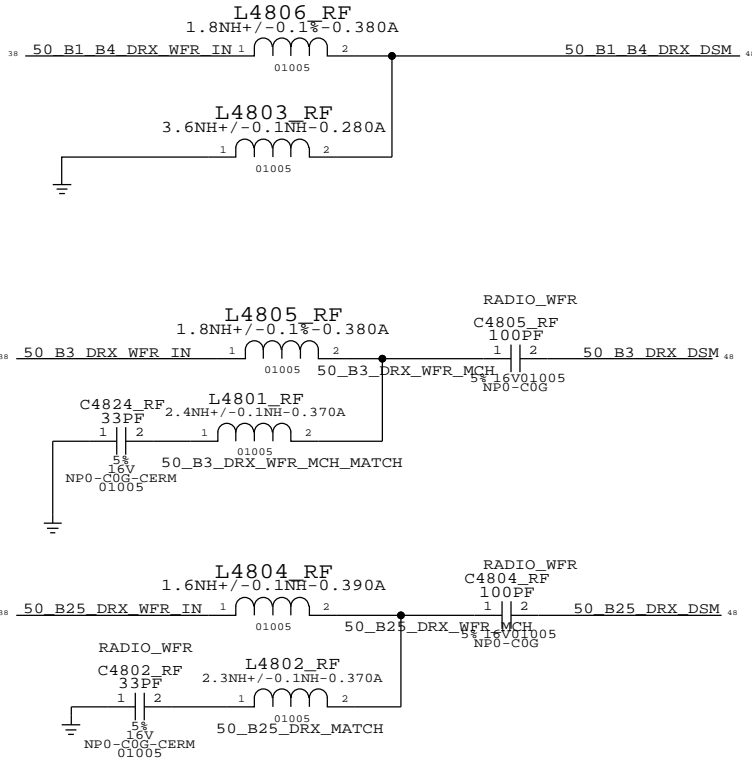
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	SHEET	46 OF 54

RX DIVERSITY (1)

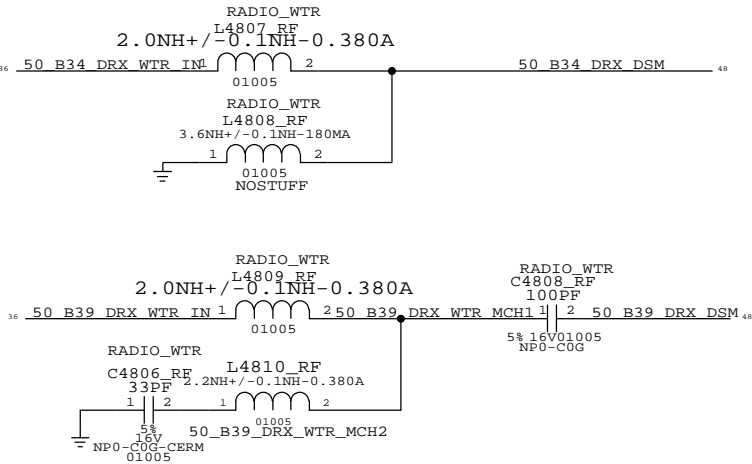
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C4826_RF
R1800
L1829
U1801

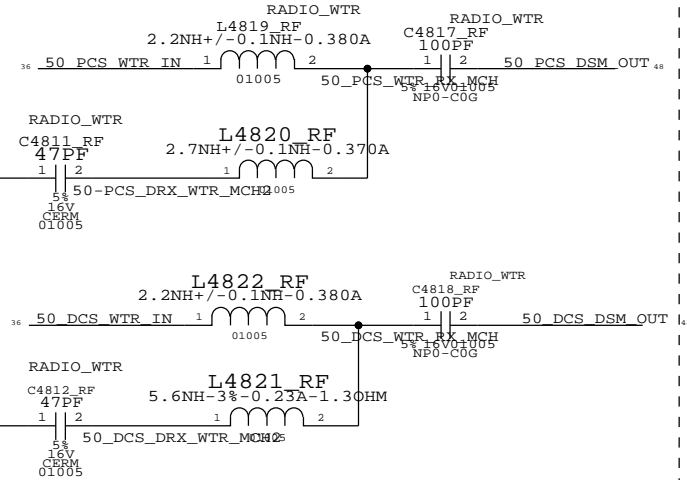
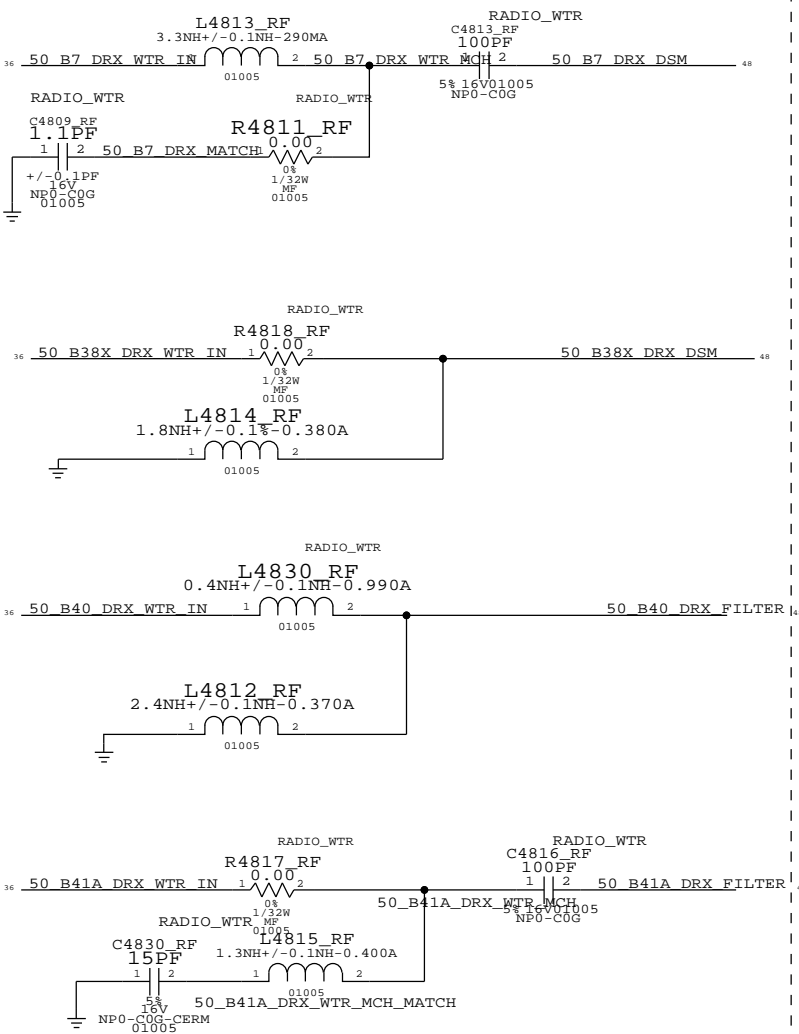
MIDBAND MIDBAND DIVERSITY - WFR



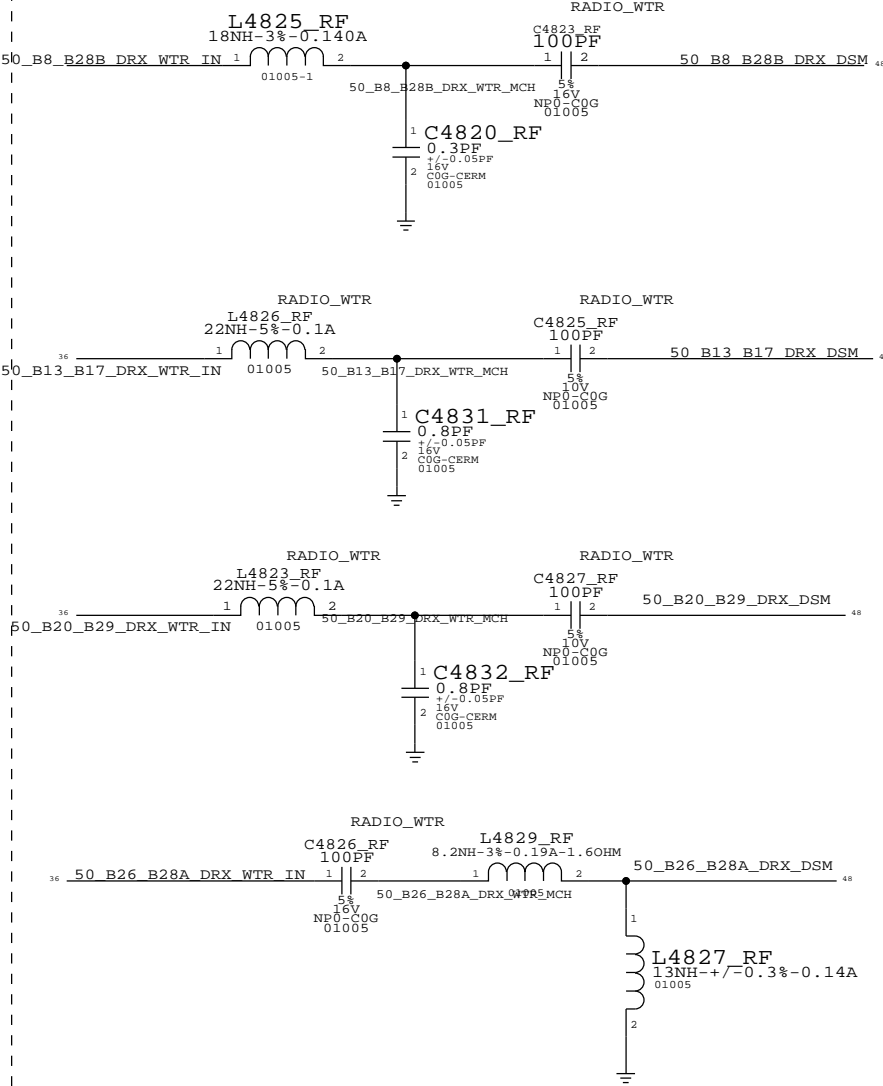
MIDBAND DIVERSITY - WTR



HIGHBAND DIVERSITY - WTR



LOWBAND DIVERSITY - WTR



RX DIVERSITY

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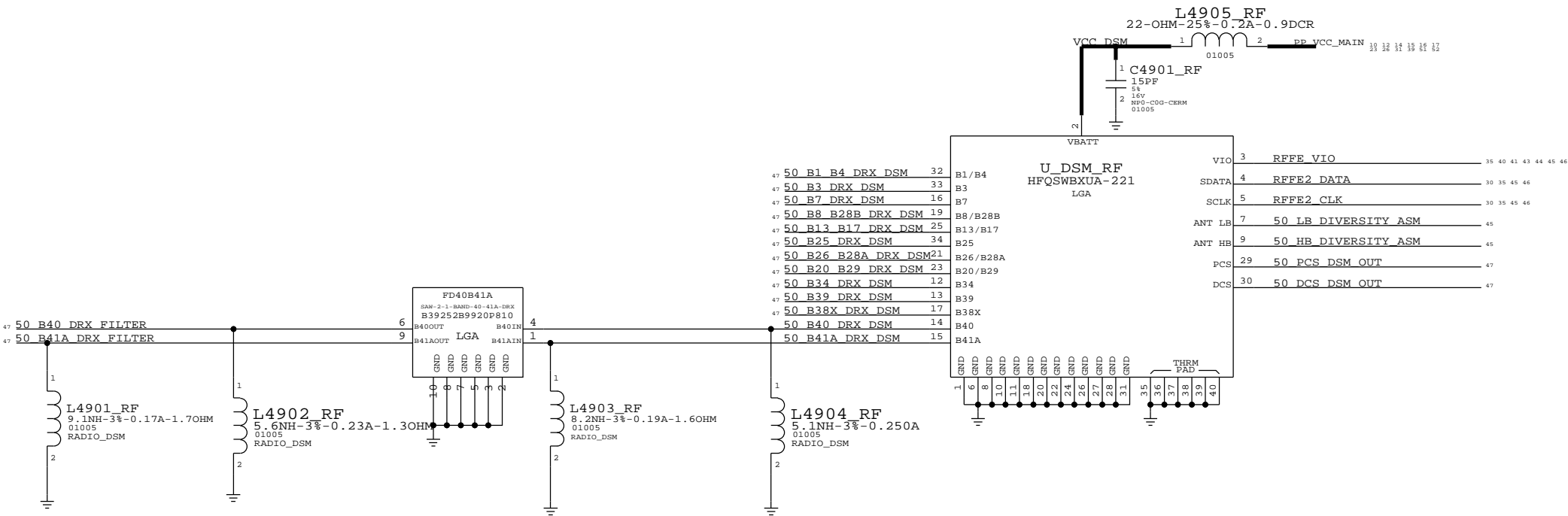
PAGE 48 OF 55

SHEET 47 OF 54

RX DIVERSITY (2)

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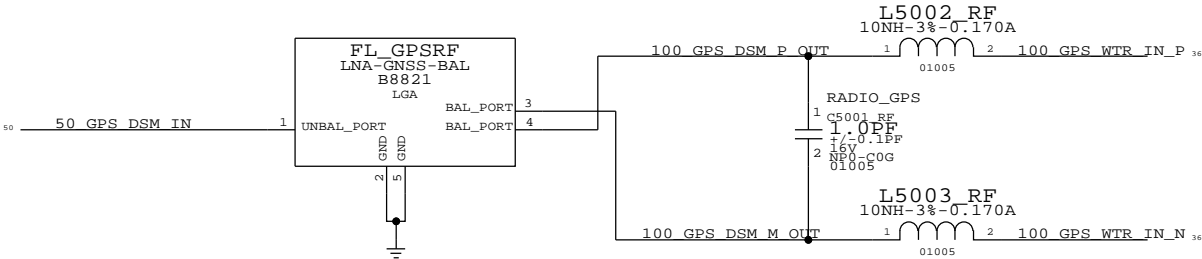
C1900
R1900
L1900
U1901




GPS

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C1900
R1900
L1900
U1901

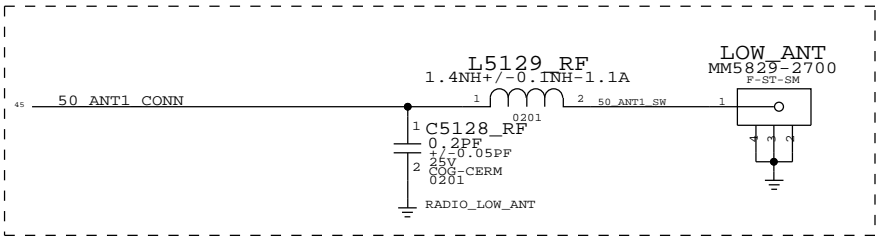
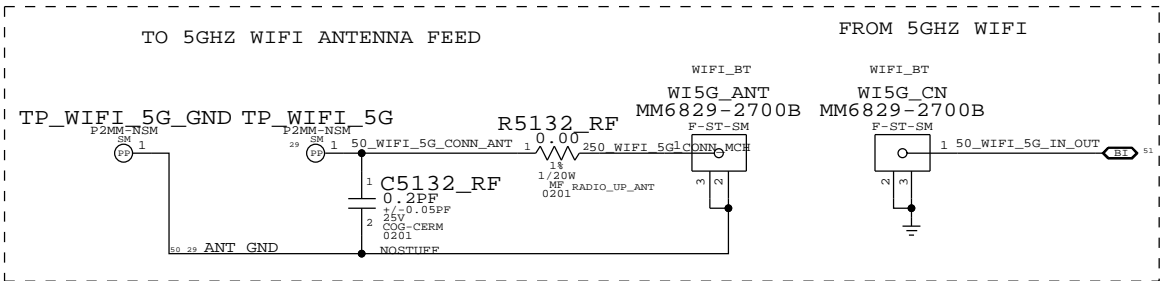
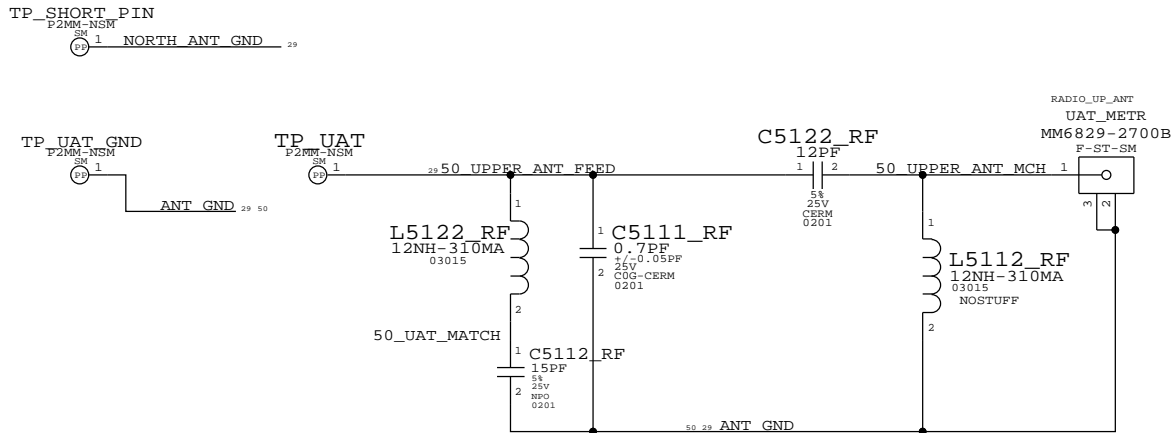
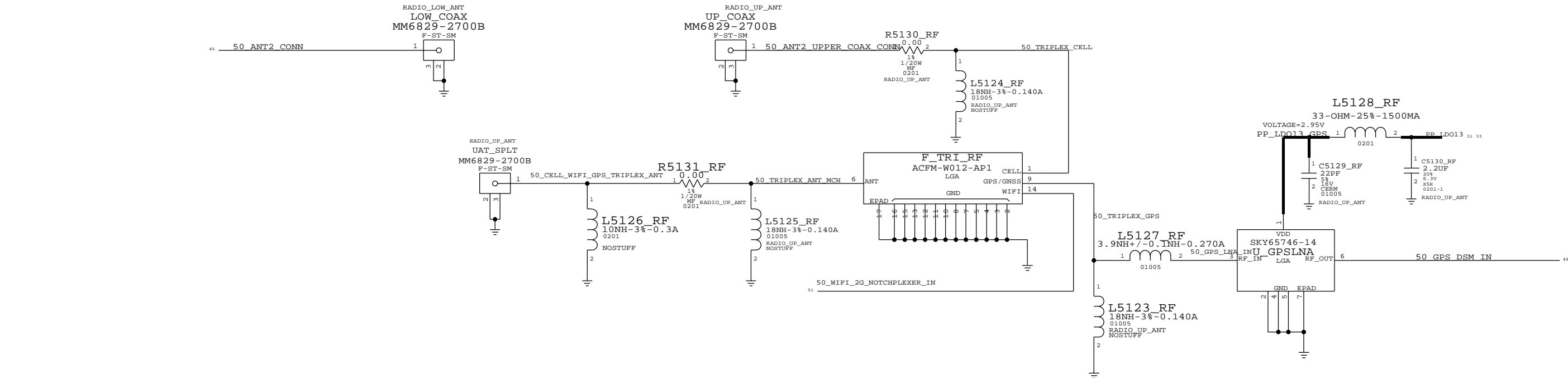



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ANTENNA FEED'S

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TEST & COAX CONNECTOR FOR LOWER SECTION OF MLB



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	PAGE	51 OF 55
	SHEET	50 OF 54

[illegible][illegible]

STOCKHOLM

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C2101
R2100
L2102
U2100

REMOVING BULK CAP 4.7UF 0402 -->
BECAUSE OF OTHER BULK CAPS IN LAYOUT

PP VCC MAIN

PP STOCKHOLM_V1R_S2R

RADIO_STOCKHOLM

VDD/RF_IF_VDD C6
VDD/RF_IF_VDD C7
VDD/RF_IF_VDD D3
VDD/RF_IF_VDD G2
TVDD_IN G1
SVDD_B7
ESE_VDD C5

U5301_RF
PN65V
UFLGA

STOCKHOLM_HOST_WAKE D1
ALWAYS ON PULL-UP --> NC B3
STOCKHOLM_FW_DWLD_REQ A1
BB_REQUEST_XO_CLK A2
REF_CLK_FROM_BB A3
STOCKHOLM_UART_RXD C1
STOCKHOLM_UART_TXD D2
STOCKHOLM_CTS_L B1
STOCKHOLM_RTS_L B2
STOCKHOLM_ENABLE E1

IRQ SVDD_REQ DWL CLK_REQ RX TX CTS RTS VEN SMX_RST* SMX_CLK ESE_IO1 ESE_IO2 ESE_IO3 ESE_IO4 XTAL2

SIM_SWIO SIM_VCC SIM_PMU_VCC TX_PWR_REQ ESE_DWPM_DBG ESE_DWPS_DBG ANT1 RXN/RP_CLK_RX TX1 TX2 RXN/RP_CLK_RX ANT2 VMID RF_CLK_TX RF_DATA_IO

STOCKHOLM_SIM_SWAP PP_PN65_VCC_SIM PP_PN65_SIM_PMU

STOCKHOLM_RF_CLK_RX STOCKHOLM_RF_CLK_TX STOCKHOLM_RF_DATA_IO

TP5301_RF STOCKHOLM_TIO TP-P55

TP5302_RF TP-P55

NOSTUFF

RADIO_STOCKHOLM R5301_RF 100K 1/32W MF 01005

STOCKHOLM_ENABLE

STOCKHOLM FW DWLD REQ

RADIO_STOCKHOLM R5302_RF 100K 1/32W MF 01005

U5302_RF AS3923-B0-BWLT WLCSP

CDMP1 B3 CDMP2 B4 RFO1 C4 RFO2 C3 RFI1 D1 RFI2 C1 VSP_RF D3 VSP C2

L5301_RF 78NH-5%-0.97A-0.130HM 0402

L5302_RF 78NH-5%-0.97A-0.130HM 0402

T5301_RF ATE201388B-20011

C5310_RF 560PF 10% X7R-CERM 0201 RADIO_STOCKHOLM

C5311_RF 560PF 10% X7R-CERM 0201 RADIO_STOCKHOLM

C5312_RF 220PF 20% NPO-COG 0402

C5313_RF 33PF 20% NPO-COG 0201

C5314_RF 390PF 20% NPO-COG 0402

C5315_RF 220PF 20% NPO-COG 0402

C5316_RF 330PF 20% NPO-COG 0201

TP5303_RF TP-P55

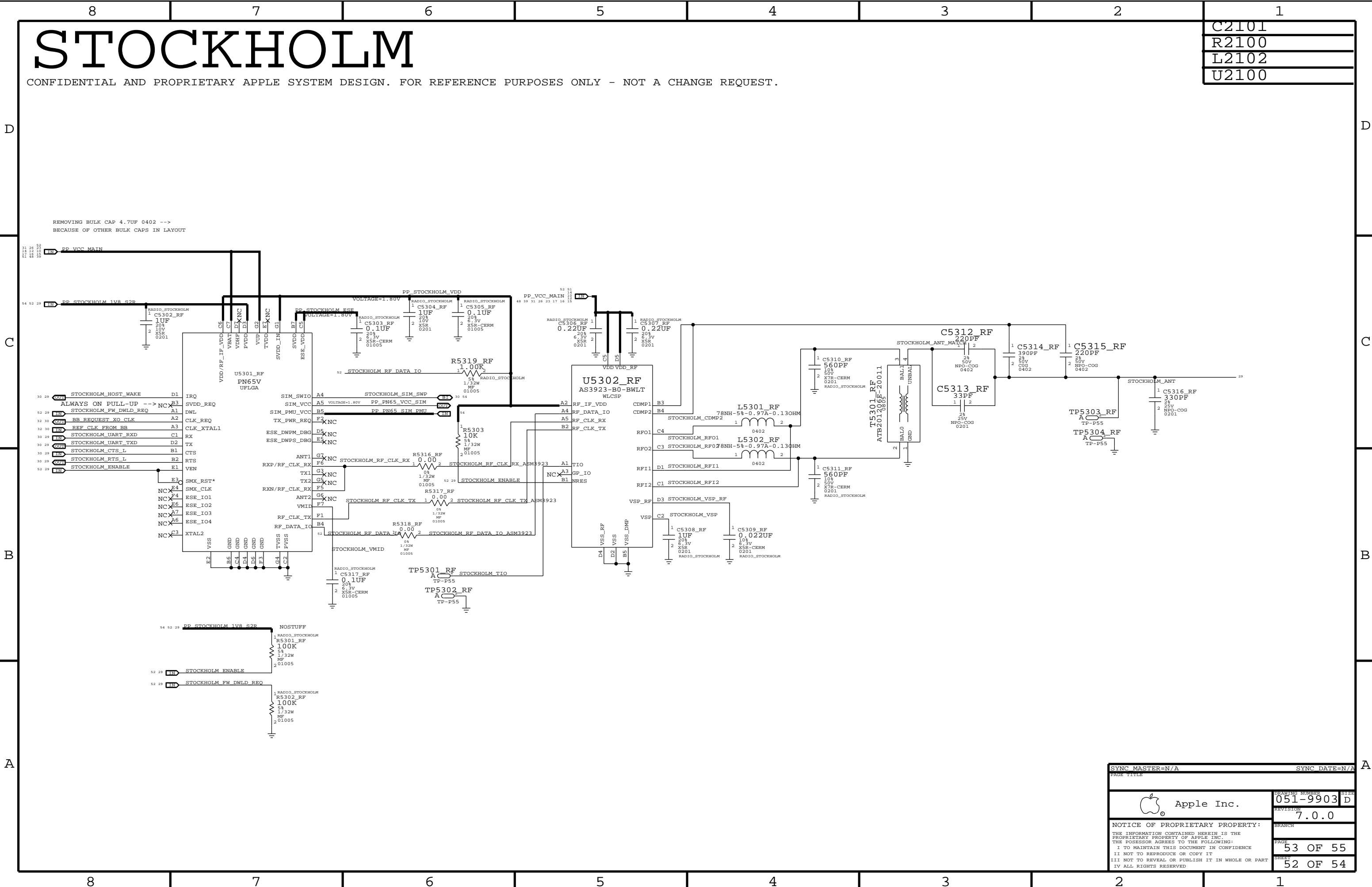
TP5304_RF TP-P55

STOCKHOLM_ANT_MATCH

STOCKHOLM_ANT

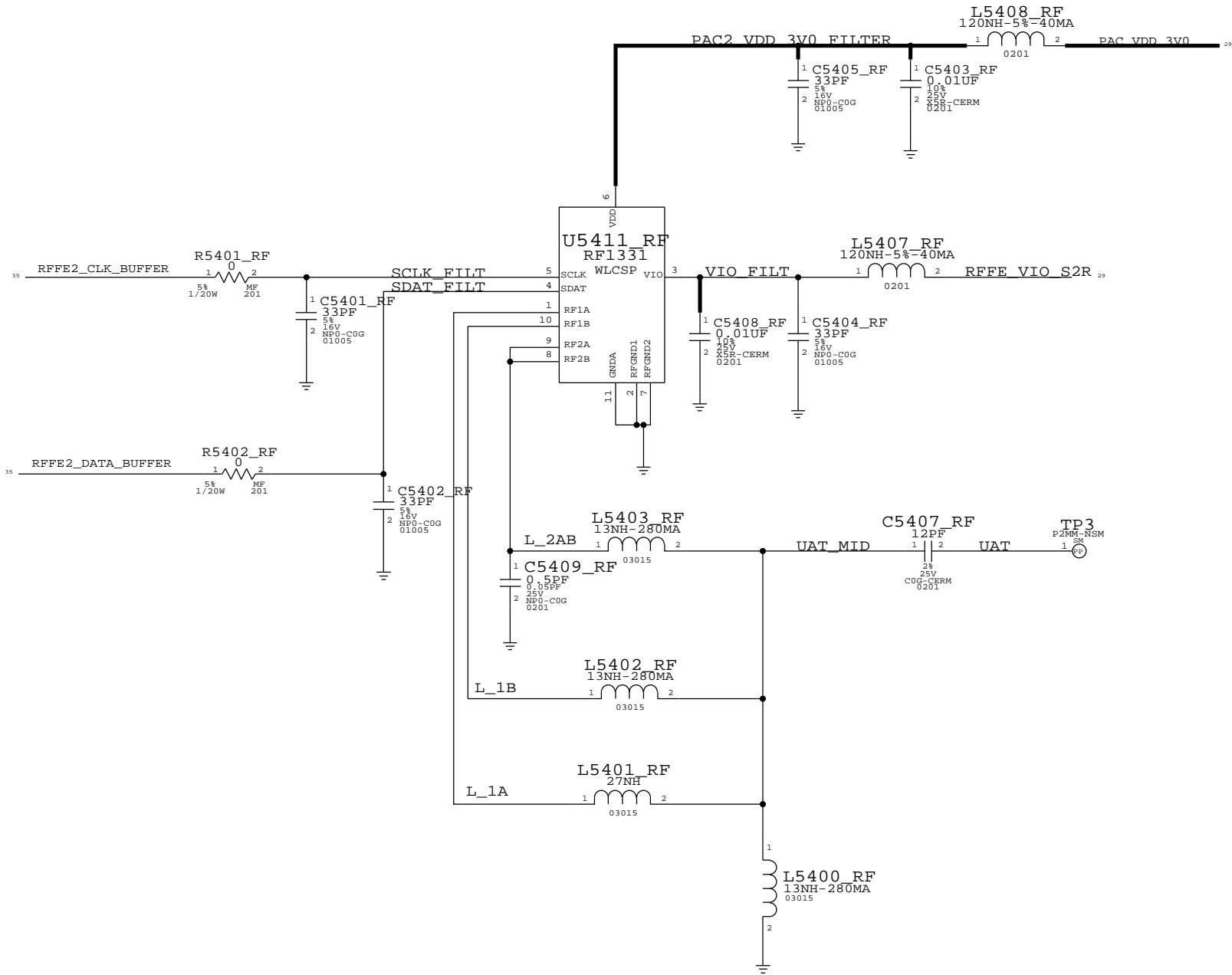
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PAGE 53 OF 55
SHEET 52 OF 54


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ON-BOARD JUMPER FLEX

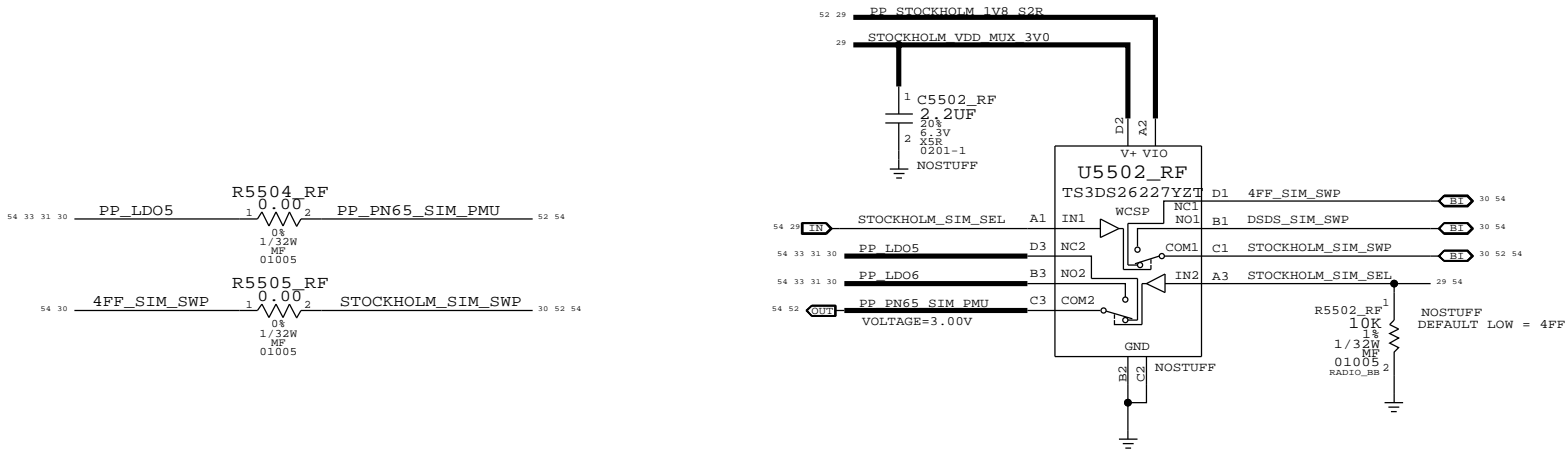
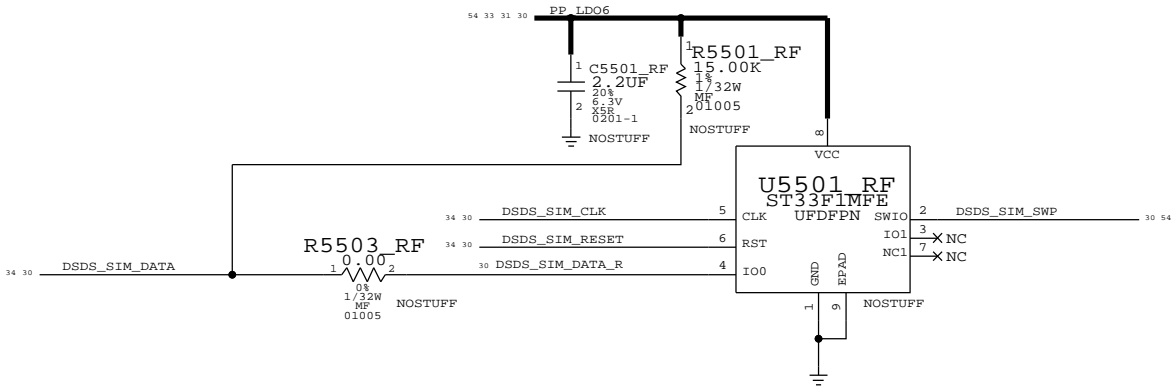
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	REVISION	7.0.0
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	PAGE	54 OF 55
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		54 OF 54	