

8

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1

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.

2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.

3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

SCH AND BOARD PART NUMBERS

| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | BOM OPTION |
|----------|-----|------------------|-------------------------|------------|
| 051-0886 | 1 | SCH_MLB-C1,X200 | SCH1 | |
| 820-4124 | 1 | PCBF_MLB-C1,X200 | PCB1 | |

X200

MLB-C1

LAST_MODIFIED=

Tue Oct 29 15:52:27 2013

REV

ECN

DESCRIPTION OF REVISION

CK APPD
DATE

A

0002535199

PRODUCTION RELEASED

2014-01-13

PDF CSA CONTENTS

SYNC MASTER

DATE

21

27

SENSOR: ACCEL, COMPASS, GYRO

N/A

N/A

22

28

SENSOR: PROX

J85_MLB_I2

05/12

23

29

CAMERA: REAR CONN & FILTERS

N/A

N/A

24

30

CELL:AP INTERFACE & DEBUG CONNECTIONS

MLB_I1

08/29/2013

25

32

CELL: BASEBAND PMU (1 OF 2)

RADIO_MLB_I1

08/29/2013

26

33

CELL: BASEBAND PMU (2 OF 2)

RADIO_MLB_I1

08/29/2013

27

34

CELL: BASEBAND (1 OF 2)

RADIO_MLB_I1

08/29/2013

28

35

CELL: BASEBAND (2 OF 2)

RADIO_MLB_I1

08/29/2013

29

36

CELL: RF TRANSCEIVER (1 OF 2)

RADIO_MLB_I1

08/29/2013

30

37

CELL: RF TRANSCEIVER (2 OF 2)

RADIO_MLB_I1

08/29/2013

31

38

CELL: RX MATCHING

RADIO_MLB_I1

08/29/2013

32

39

CELL: RF TRANSCEIVER (3 OF 4)

RADIO_MLB_I1

08/29/2013

33

40

CELL: PENTABAND PA

RADIO_MLB_I1

08/29/2013

34

41

CELL: BAND 2/3 PAD

RADIO_MLB_I1

08/29/2013

35

42

CELL: BAND 7/20 PAD

RADIO_MLB_I1

08/29/2013

36

43

CELL: BAND 5/8 PAD

RADIO_MLB_I1

08/29/2013

37

44

CELL: 2G PA

RADIO_MLB_I1

08/29/2013

38

45

CELL: PA DCDC CONVERTER

RADIO_MLB_I1

08/29/2013

39

46

CELL: ASM AND HB LTE FRONT-END

RADIO_MLB_I1

08/29/2013

40

47

CELL: RX DIVERSITY

RADIO_MLB_I1

08/29/2013

41

48

CELL: GPS

RADIO_MLB_I1

08/29/2013

42

49

CELL: ANTENNA FEEDS

RADIO_MLB_I1

08/29/2013

43

57

IO: FILTERS & HOTBAR CONN

N/A

04/18/2011

44

58

WIFI/BT: MODULE

WIFI_DEV05

20/2013

45

75

POWER: BATTERY CONNECTOR

N/A

N/A

46

81

PMU: ANYA PAGE 1

J72_MLB_I1

26/2012

47

82

PMU: ANYA PAGE 2

J85_MLB_I2

03/2012

48

83

PMU: ANYA PAGE 3

J72_MLB_I1

26/2012

49

84

PMU: ANYA PAGE 4

J72_MLB_I1

26/2012

50

85

POWER: PP1V8_SW

J85_MLB_I1

26/2012

51

90

SEP: EEPROM & SOC DEBUG

J72_MLB_I1

26/2012

52

93

TEST: TP/HOLES/FIDUCIALS

J85_MLB_I2

03/12

53

94

TEST: EE TP/PP

J72_MLB_I1

26/2012

54

121

POWER: ALIASES

J72_MLB_I1

26/2012

PDF CSA CONTENTS

SYNC MASTER

DATE

1

1

TABLE OF CONTENTS

N/A

N/A

2

2

BLOCK DIAGRAM: SYSTEM

J85_MLB_I1

04/02/2013

3

4

BOM TABLES

J72_MLB_I1

26/2012

4

6

SOC: MAIN

N/A

04/18/2011

5

7

SOC: I/OS

N/A

05/05/2011

6

8

SOC: NAND

N/A

04/18/2011

7

9

SOC: DP,MIPI

MLB

05/04/2012

8

10

SOC: SRAM, IO PWRS

N/A

04/18/2011

9

11

SOC: VDD, SRAM, CPU, GPU PWRS

N/A

04/18/2011

10

12

SOC: MISC & ALIASES

N/A

04/11/2011

11

13

IO: TRISTAR

N/A

N/A

12

14

NAND STORAGE

MLB

05/04/2012

13

17

TOUCH: SUPPORT CKT & CONN

N/A

06/21/2010

14

18

AUDIO: HP FLEX CONN

N/A

03/31/2011

15

19

AUDIO: L81 CODEC

KAVITHA

01/18/2012

16

20

AUDIO: CS35L19A AMPS

KAVITHA

01/18/2012

17

21

BUTTON: CONN

N/A

N/A

18

22

VIDEO: EDP SUPPORT & CONN

J85_MLB_I2

05/2012

19

24

SENSOR: OSCAR

J72_MLB_I1

26/2012

20

26

CAMERA: FF-ALS CONN & FILTERS

J85_MLB_I2

03/2012

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DRAWING NUMBER

051-0886

SIZE

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REVISION

A.0.0

BRANCH

PAGE

1 OF 121

SHEET

1 OF 54

8

7

6

5

4

3

2

1

8

7

6

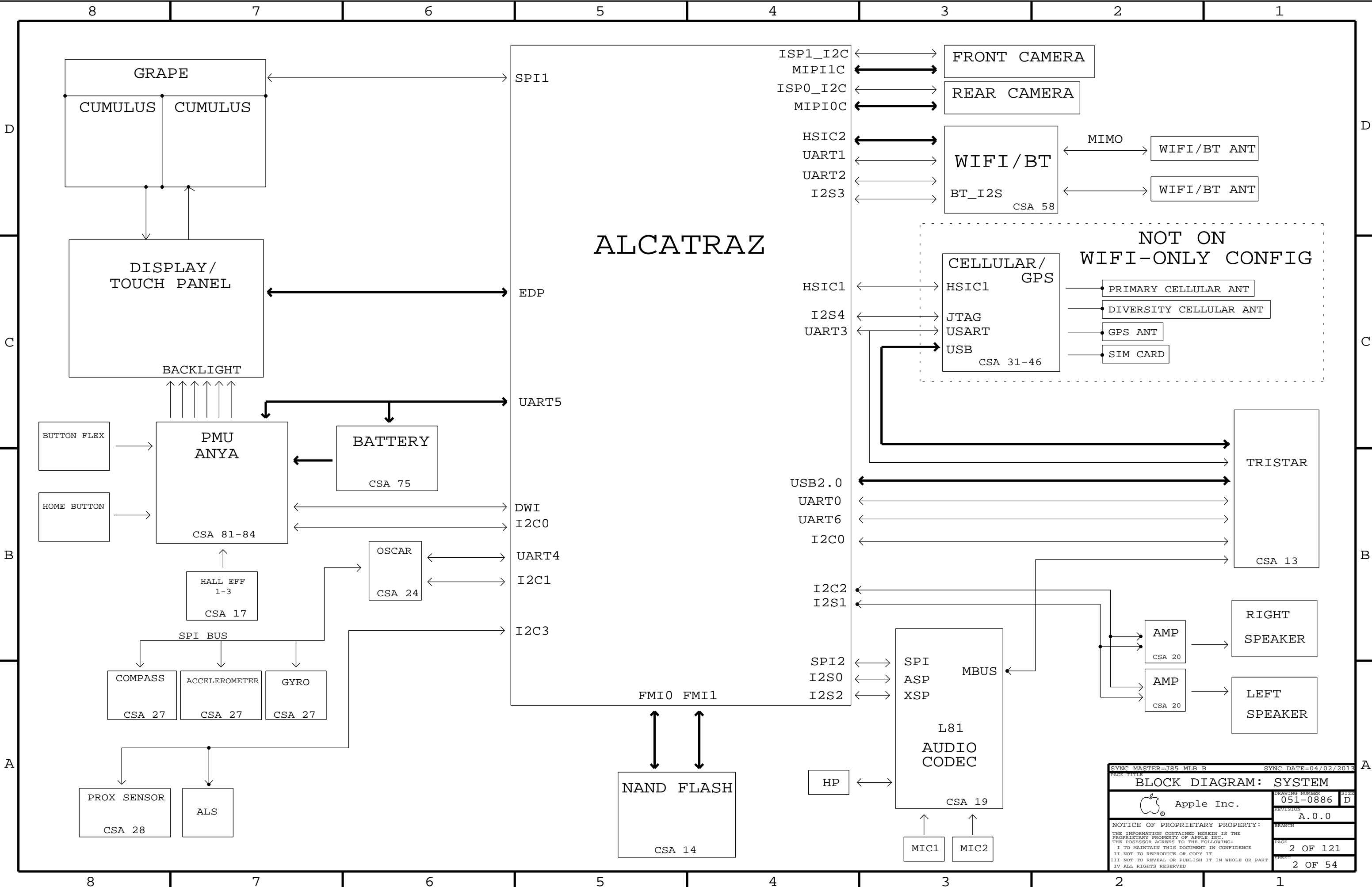
5

4

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1



Power aliases required by this page:
(NONE)

Signal aliases required by this page:
(NONE)

BOM options provided by this page:

```
COMMON
ALTERNATE
16GB_PROD
32GB_PROD
64GB_PROD
128GB_PROD

DEVELOPMENT_JTAG_TAP
JTAG_DAP
MLB (WDOG TO PMU)
```

ANDGATE_TI
FERRITE_TY
FERRITE_TDK

| | |
|-----------|-------------------|
| BOM GROUP | BOM OPTIONS |
| BASIC | COMMON, ALTERNATE |

| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|----------|-----|----------------------------|-------------------------|----------|------------|
| 806-6207 | 1 | FENCE,TALL,MLB,X221 | PD_FENCE_MLB | CRITICAL | |
| 806-7613 | 1 | FENCE,RADIO,MLB,C BRD,X221 | PD_CAN_RADIO | CRITICAL | |

| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|----------|-----|---|-------------------------|----------|-------------------------|
| 825-7639 | 1 | EEEE FOR 639-5393 (X200C1 GOOD) | FNJ D | CRITICAL | EEEE_X200C_GOOD |
| 825-7639 | 1 | EEEE FOR 639-5394 (X200C1 BETTER) | FNJ 5 | CRITICAL | EEEE_X200C_BETTER |
| 825-7639 | 1 | EEEE FOR 639-5385 (X200C1 BEST) | FNJ 9 | CRITICAL | EEEE_X200C_BEST |
| 825-7639 | 1 | EEEE FOR 639-5386 (X200C1 BEST*) | FNJ H | CRITICAL | EEEE_X200C_BEST* |
| 825-7639 | 1 | EEEE FOR 639-5387 (X200C1 ULTIMATE) | FNJ 6 | CRITICAL | EEEE_X200C_ULTIMATE |
| 825-7639 | 1 | EEEE FOR 639-5388 (X200C1 GOOD IVS) | FNJ 8 | CRITICAL | EEEE_X200C_GOOD_IVS |
| 825-7639 | 1 | EEEE FOR 639-5389 (X200C1 BETTER IVS) | FNJ F | CRITICAL | EEEE_X200C_BETTER_IVS |
| 825-7639 | 1 | EEEE FOR 639-5390 (X200C1 BEST IVS) | FNJ C | CRITICAL | EEEE_X200C_BEST_IVS |
| 825-7639 | 1 | EEEE FOR 639-5391 (X200C1 BEST* IVS) | FNJ 7 | CRITICAL | EEEE_X200C_BEST*_IVS |
| 825-7639 | 1 | EEEE FOR 639-5392 (X200C1 ULTIMATE IVS) | FNJ G | CRITICAL | EEEE_X200C_ULTIMATE_IVS |

| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|----------|-----|---------------------|-------------------------|----------|-----------------|
| 338S1192 | 1 | GYRO, ST MICRO | U2720 | CRITICAL | GYRO_STMICRO |
| 338S1218 | 1 | GYRO, INVENSENSE | U2720 | CRITICAL | GYRO_INVENSENSE |
| 132S0391 | 1 | CAP 0.01UF 25V 0201 | C2726 | CRITICAL | GYRO_STMICRO |
| 132S0288 | 1 | CAP 0.1UF 16V 0201 | C2726 | CRITICAL | GYRO_INVENSENSE |

| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|-------------|---------------------------|----------------------------------|-------------------------|-----------|------------|
| 338S1163 | 1 | IC,ACCEL,3-AXIS,DIG,BMA282,LGA14 | U2700 | CRITICAL | |
| PART NUMBER | ALTERNATE FOR PART NUMBER | BOM OPTION | REF DES | COMMENTS: | |

338S1233 ST MICRO - DISQUAL'ED
338S1114 OLD ACCEL - ST MICRO
338S1191 OLD ACCEL - ST MICRO

| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|----------|-----|------------------|-------------------------|----------|------------|
| 339S0207 | 1 | H6P + 1GB ELPIDA | U0652 | CRITICAL | |

| PART NUMBER | ALTERNATE FOR PART NUMBER | BOM OPTION | REF DES | COMMENTS: |
|-------------|---------------------------|------------|---------|-----------|
| 339S0208 | 339S0207 | | U0652 | HYNIX DDR |

| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | CRITICAL | BOM OPTION |
|----------|-----|------------------------------------|-------------------------|----------|------------|
| 343S0656 | 1 | IC,PMU,ANYA,D2089A1,OTPKX,FCCSP342 | U8100 | CRITICAL | |


| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | BOM OPTION |
|----------|-----|------------------------------|-------------------------|------------|
| 335S0921 | 1 | TOS,19NM,FPN1.5,C,DDP,16GB | U1400 | 16GB |
| 335S0922 | 1 | TOS,19NM,FPN1.5,C,QDP,32GB | U1400 | 32GB |
| 335S0923 | 1 | TOS,19NM,FPN1.5,C,ODP,64GB | U1400 | 64GB |
| 335S0929 | 1 | TOS,19NM,FPN1.5,C,12DP,64GB | U1400 | 96GB |
| 335S0924 | 1 | TOS,19NM,FPN1.5,C,16DP,128GB | U1400 | 128GB |

| PART NUMBER | ALTERNATE FOR PART NUMBER | BOM OPTION | REF DES | COMMENTS: |
|-------------|---------------------------|------------|---------|-----------------------|
| 335S0930 | 335S0921 | 16GB | U1400 | HYNIX 20NM PPN1.5 16G |
| 335S0931 | 335S0922 | 32GB | U1400 | HYNIX 20NM PPN1.5 32G |
| 335S0932 | 335S0923 | 64GB | U1400 | HYNIX 20NM PPN1.5 64G |

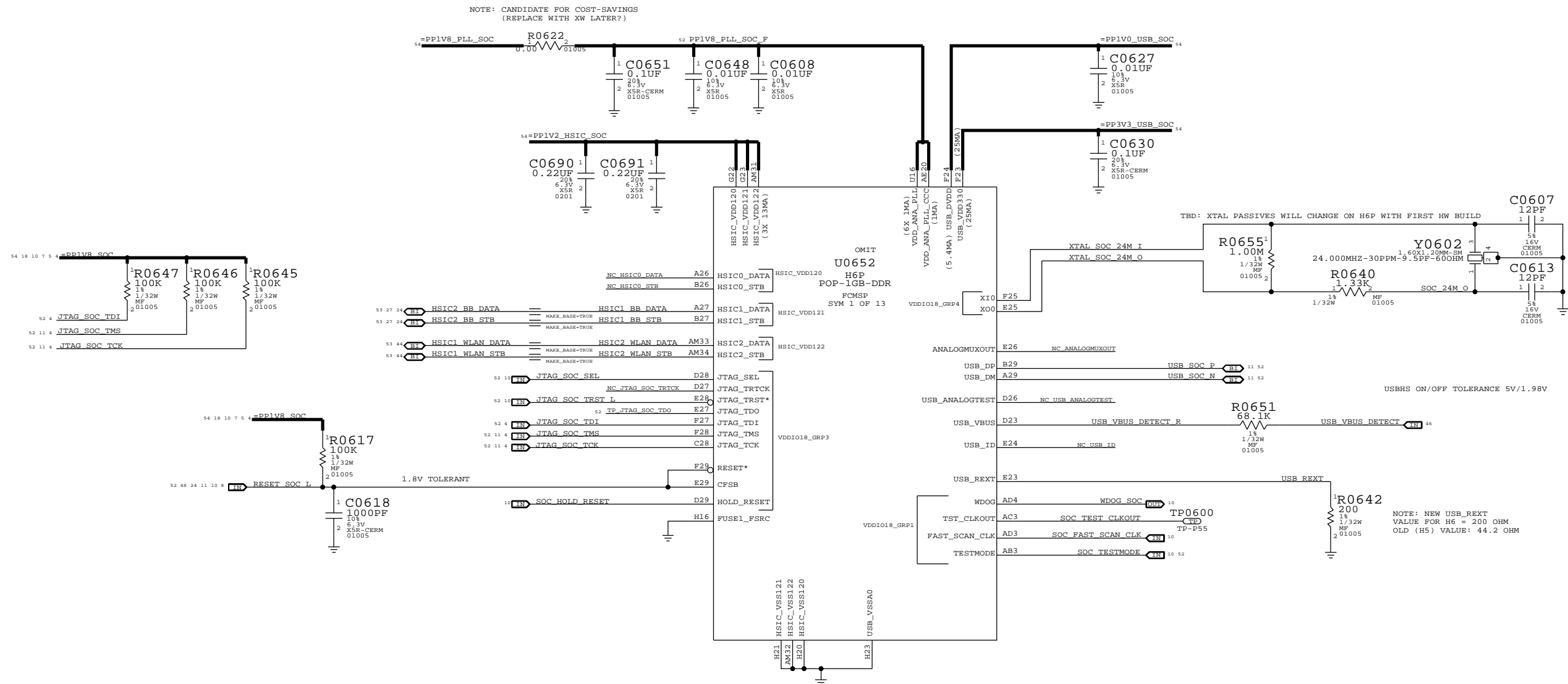
| PART# | QTY | DESCRIPTION | REFERENCE DESIGNATOR(S) | BOM OPTION |
|----------|-----|---|-------------------------|------------|
| 353S4272 | 1 | IC, SLO5AP1423V, PWR SW, GREENPET3, 4A, TDPHS | U2200 | |


NOTE: FOLLOWING J72, U2200 USES 353S3672 FOOTPRINT (353S4272 HAS SMALLER PADS DUE TO NEW DFM RULES)

| PART NUMBER | ALTERNATE FOR PART NUMBER | BOM OPTION | REF DES | COMMENTS: |
|-------------|---------------------------|------------|----------------|----------------|
| 138S0702 | 138S0657 | | C1009,C1015... | RDAR #13988471 |

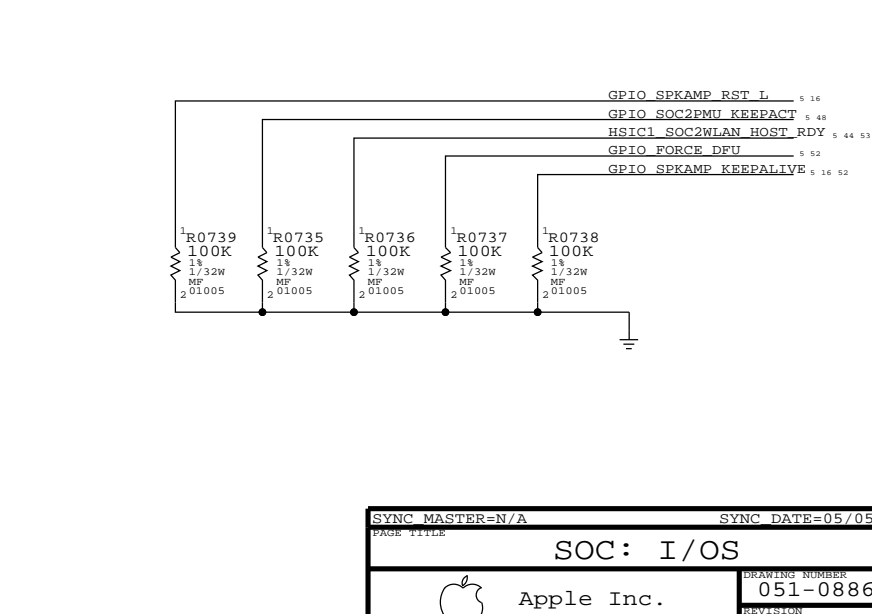
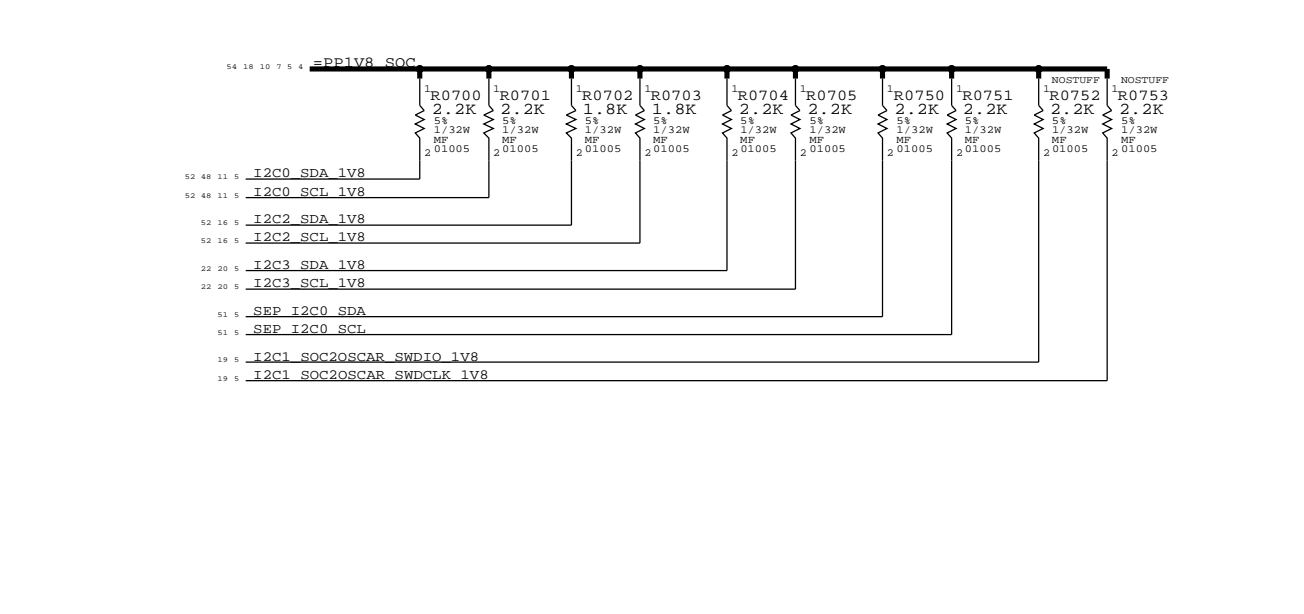
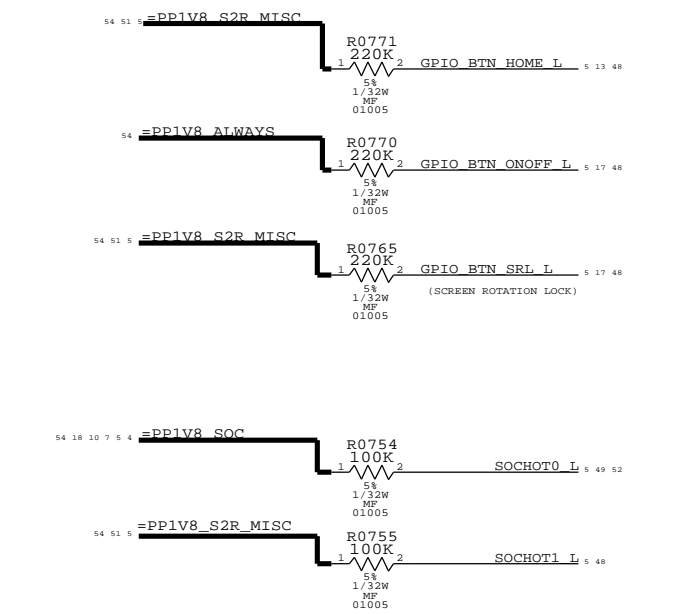
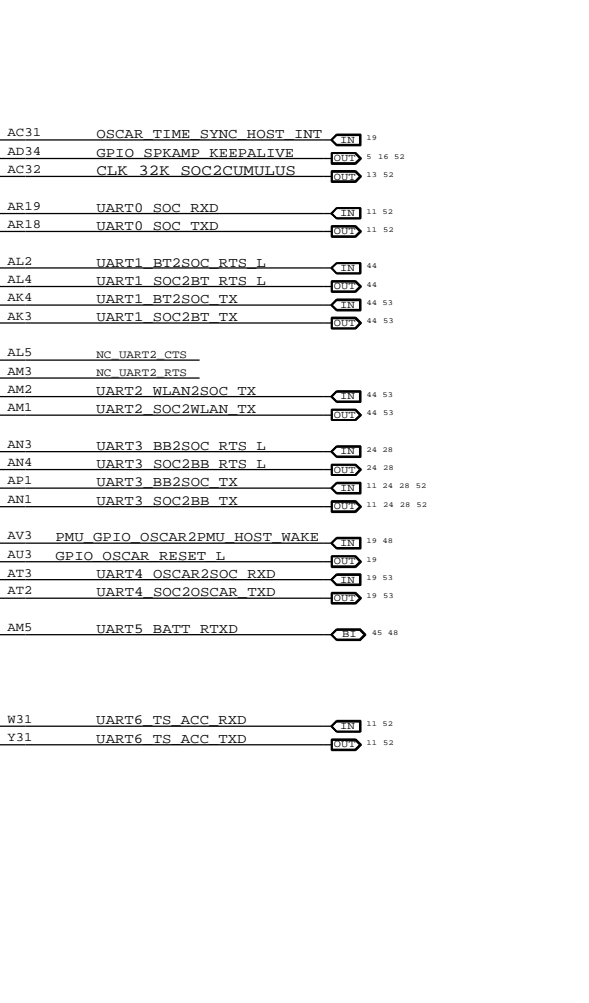
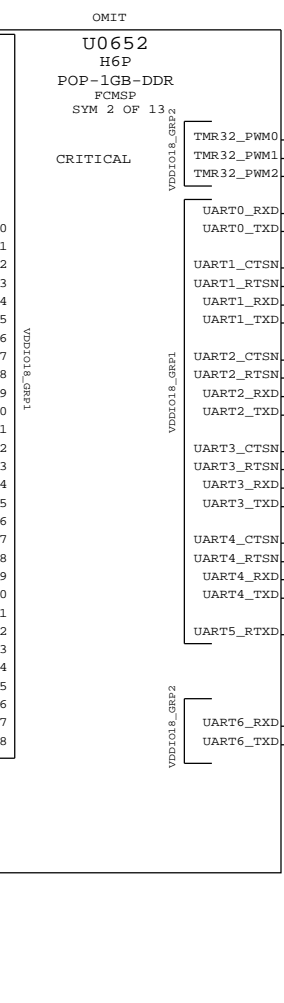
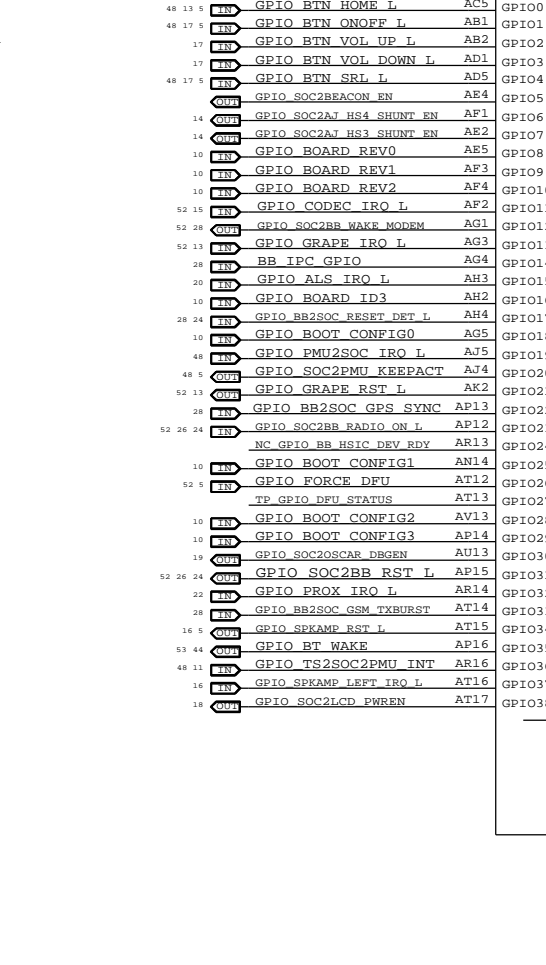
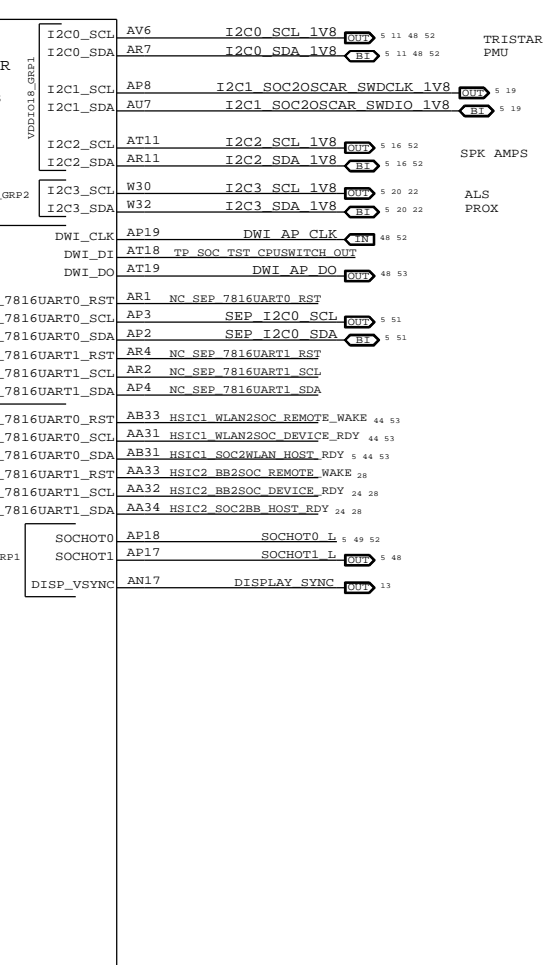
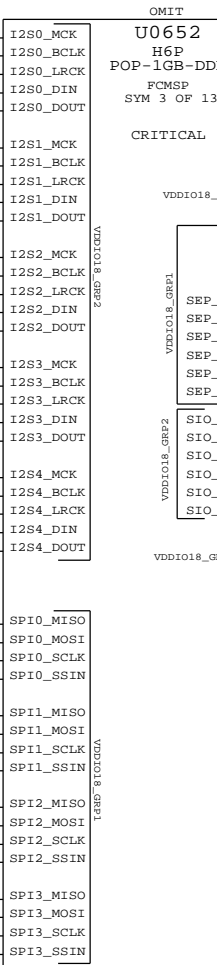
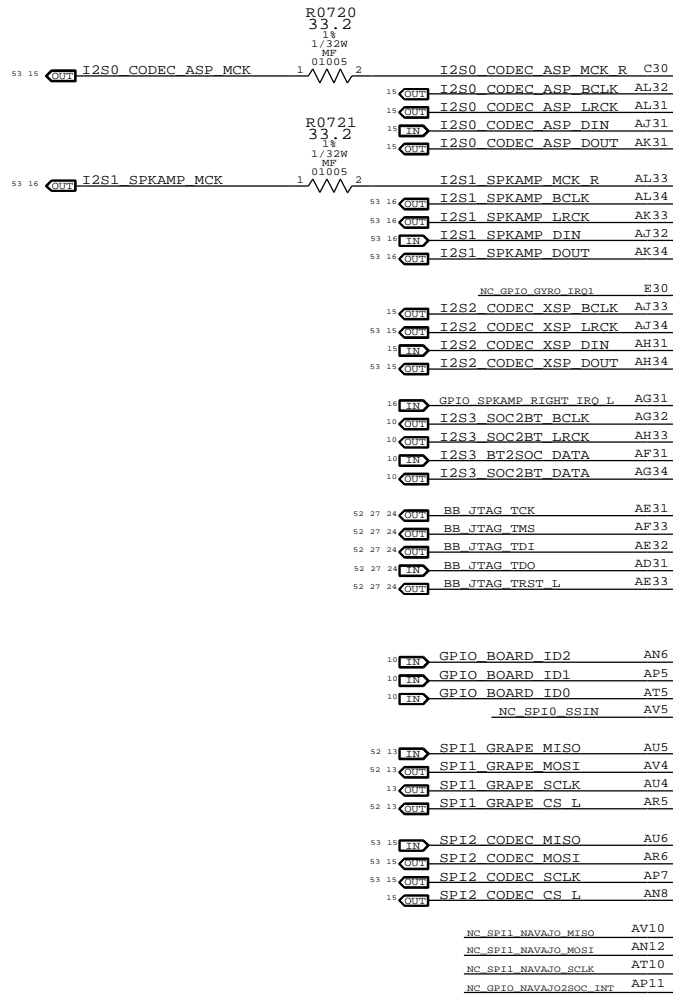
| | | | |
|--|--|-----------------------------------|--|
| SYNC MASTER=J72 MLB C | | SYNC DATE=11/26/2012 | |
| PAGE TITLE | | | |
| BOM TABLES | | | |
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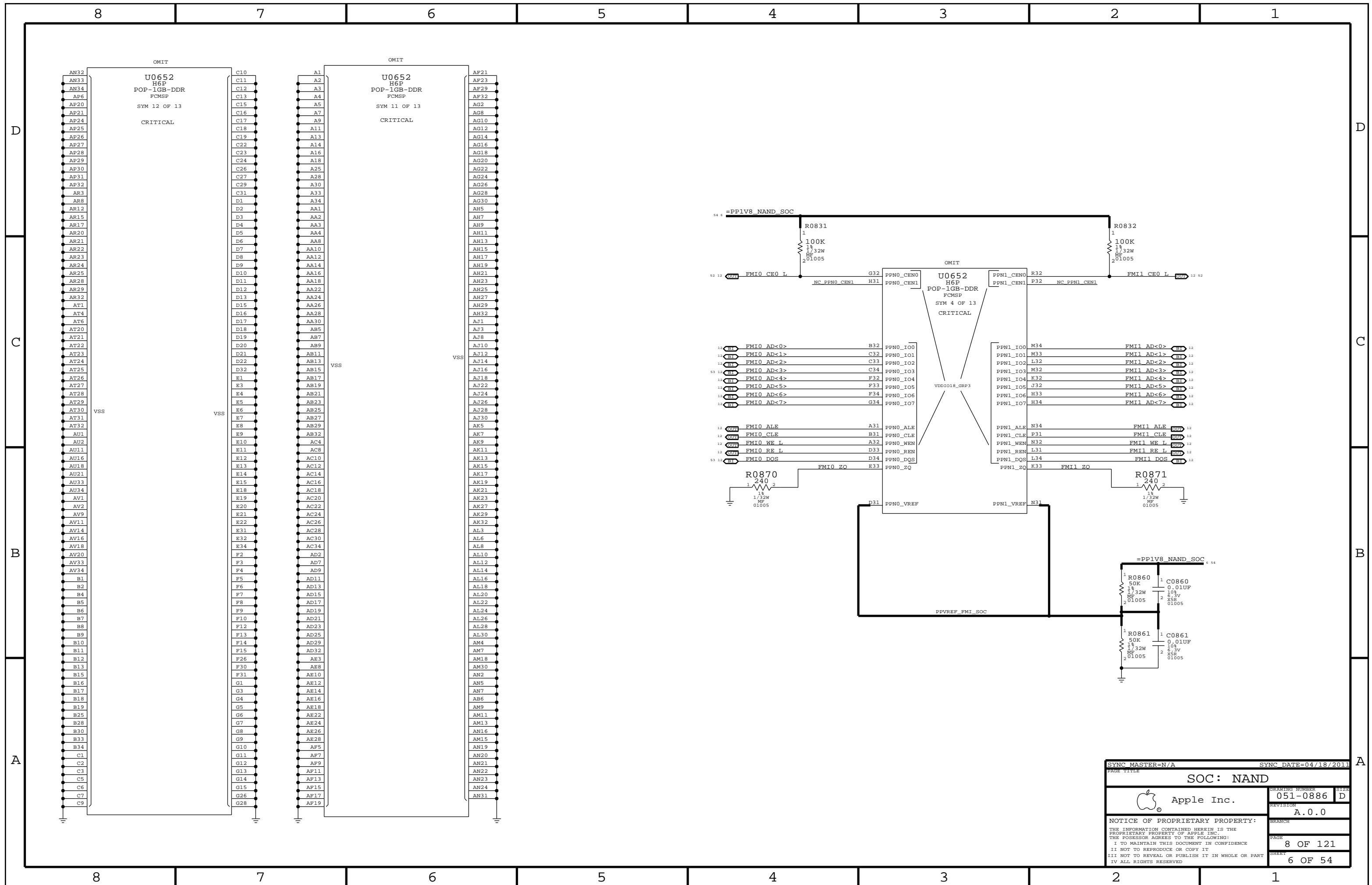
H6P: JTAG, USB, PLL, HSIC, XTAL

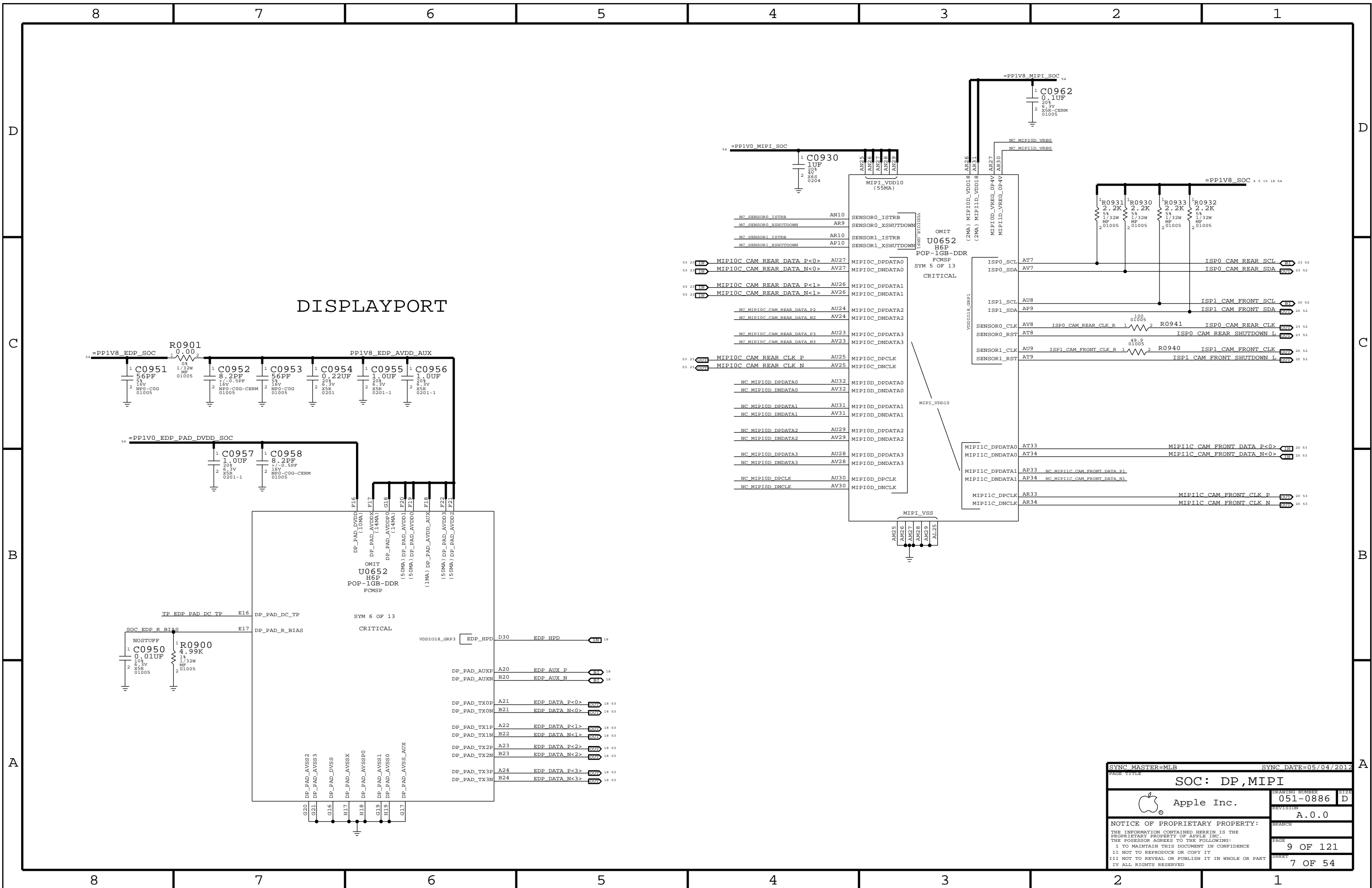


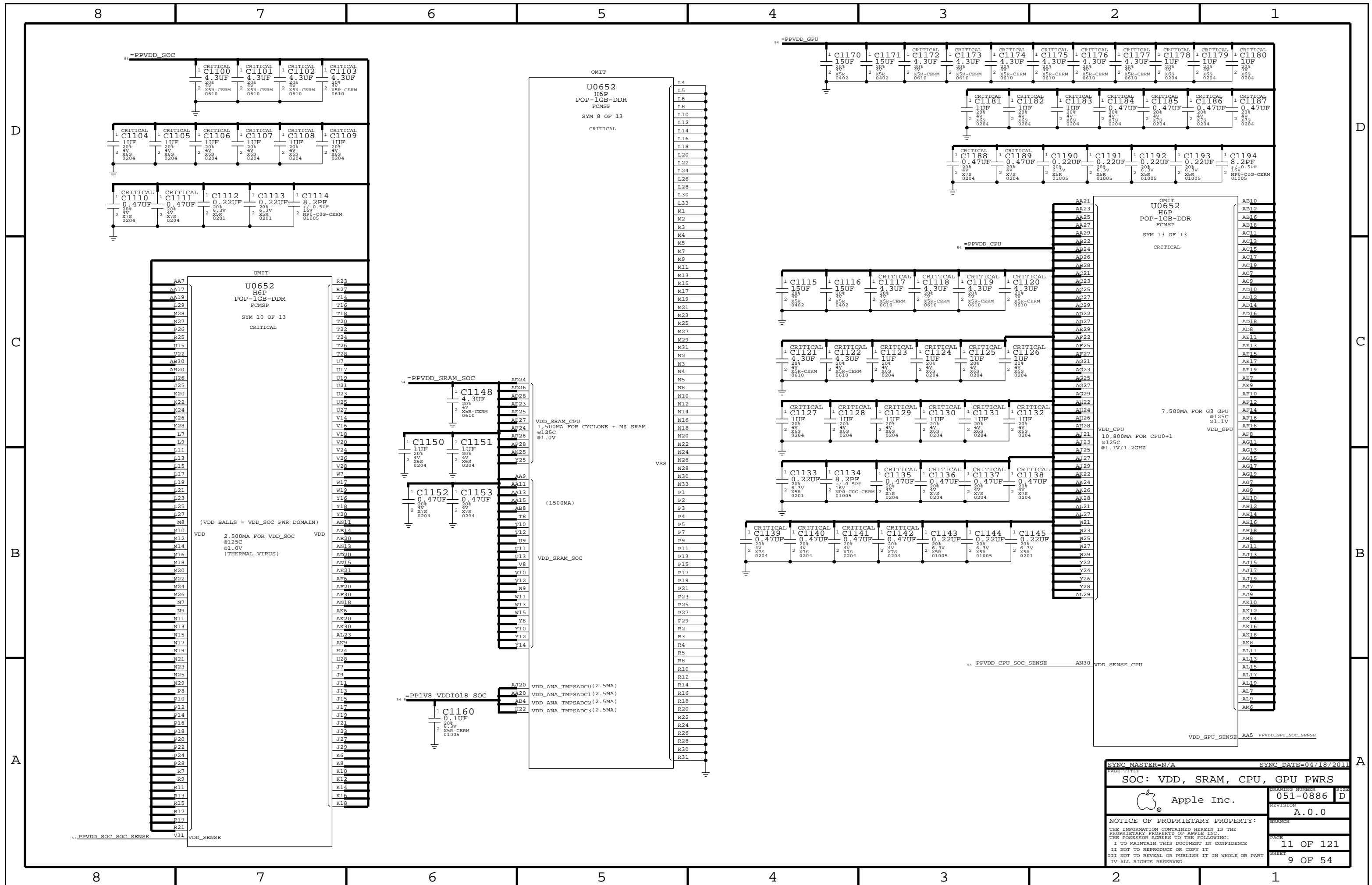
| | | | |
|---|--|----------------------|----------|
| SYNC MASTER=N/A | | SYNC DATE=04/18/2011 | |
| PAGE TITLE | | | |
| SOC: MAIN | | | |
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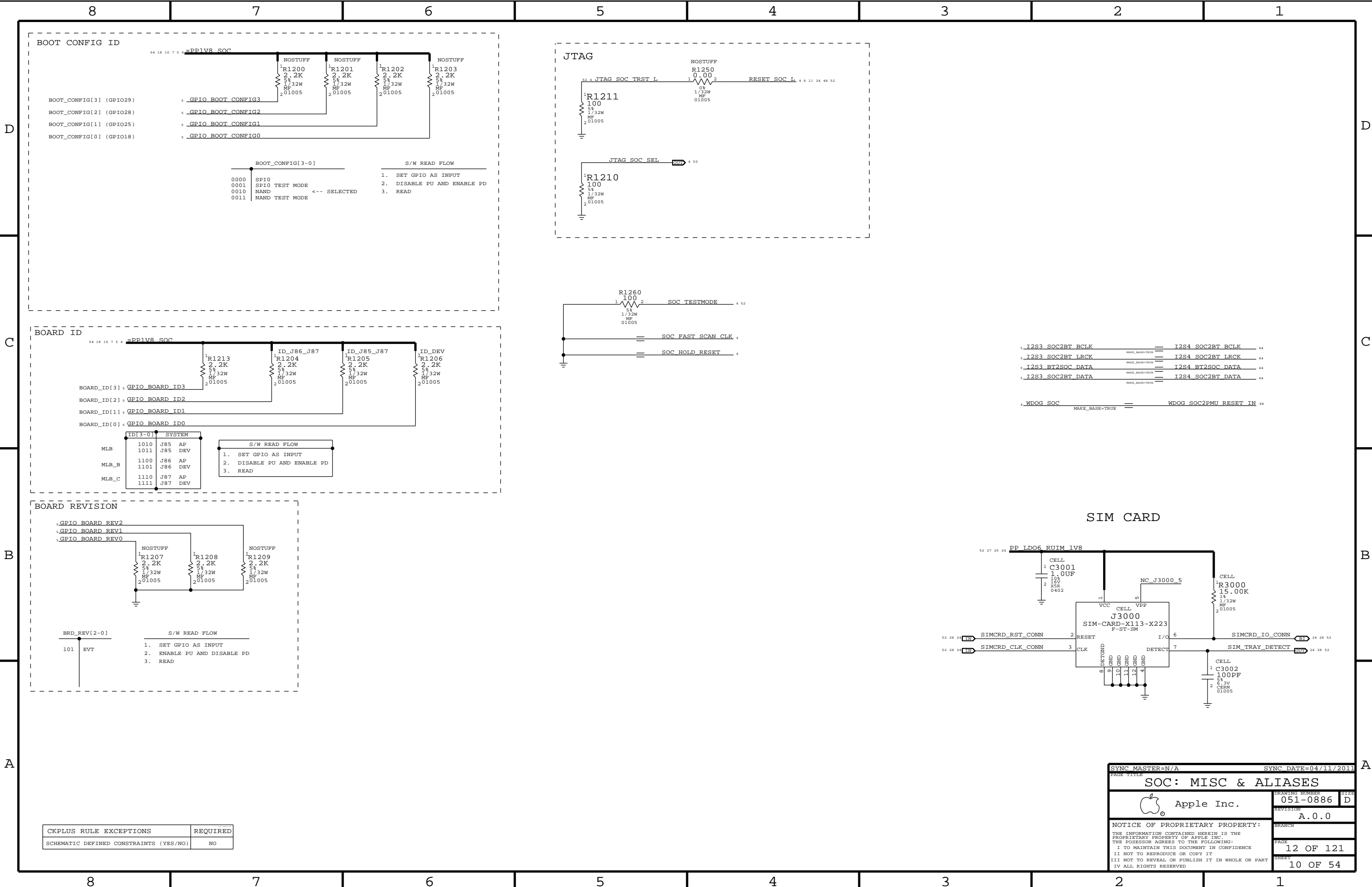
SOC I/OS





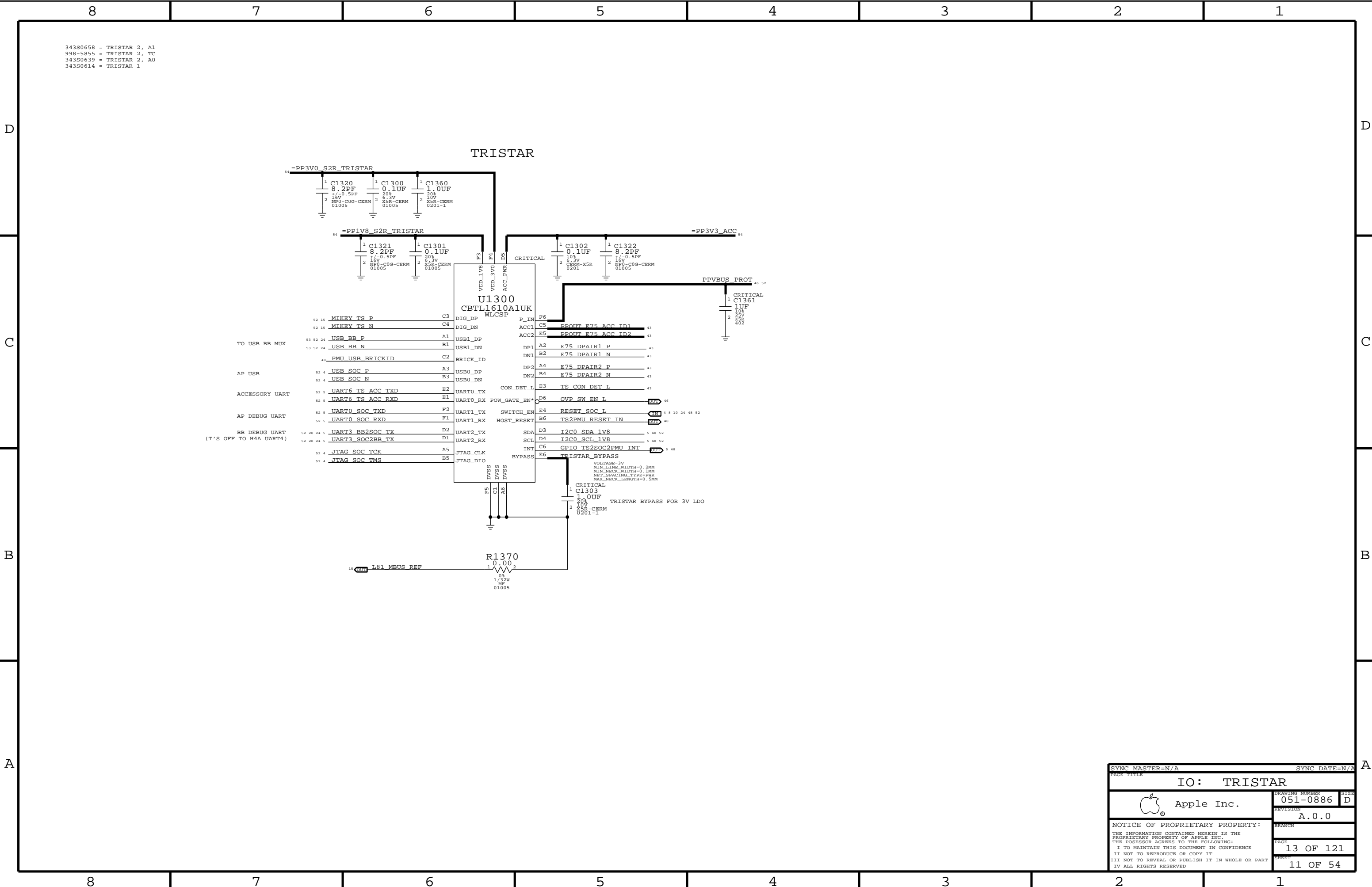


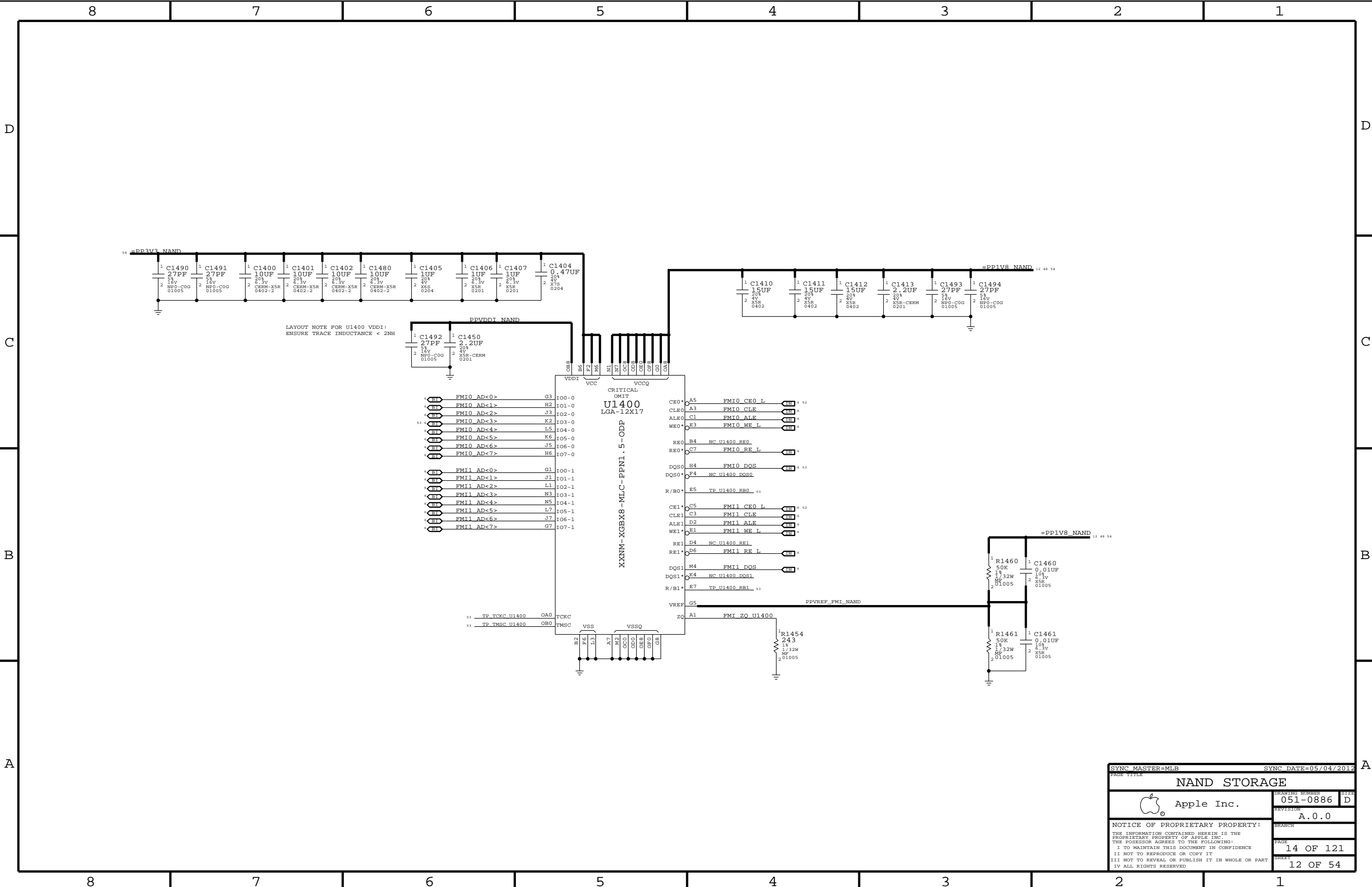




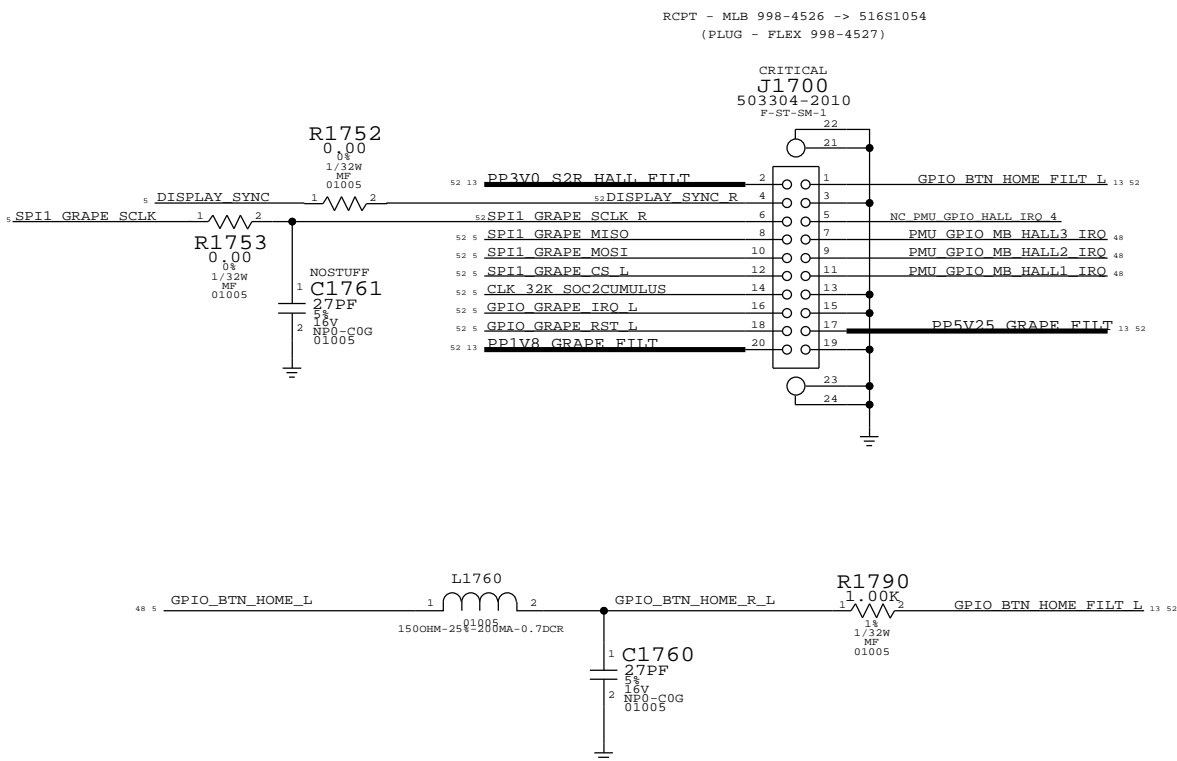
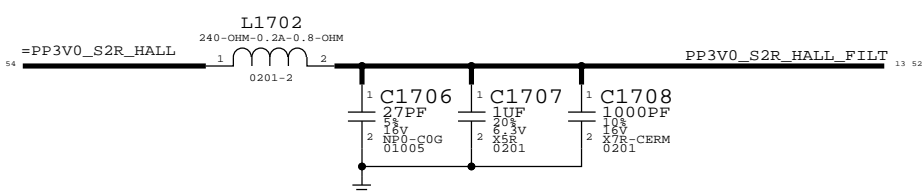
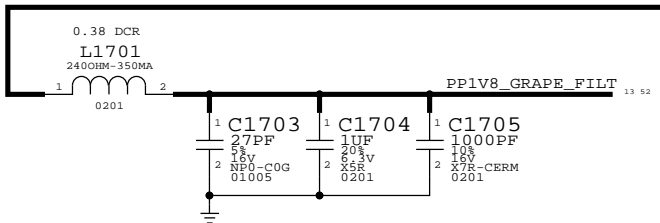
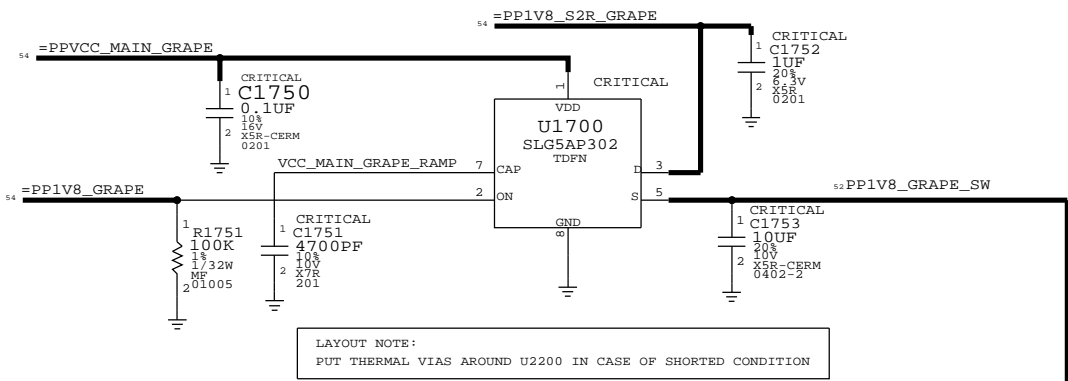
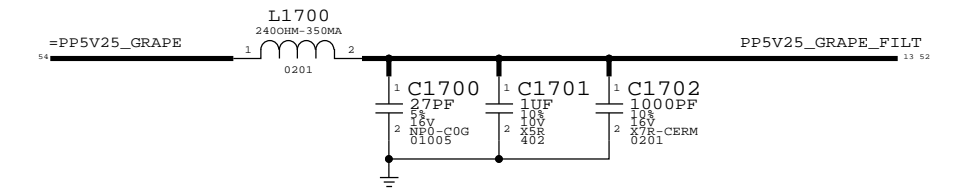
| CKPLUS RULE EXCEPTIONS | REQUIRED |
|--|----------|
| SCHEMATIC DEFINED CONSTRAINTS (YES/NO) | NO |


| | | | | | |
|---|--|-----------------|--|----------------------|--|
| PAGE TITLE | | SYNC MASTER=N/A | | SYNC DATE=04/11/2011 | |
| SOC: MISC & ALIASES | | DRAWING NUMBER | | SIZE | |
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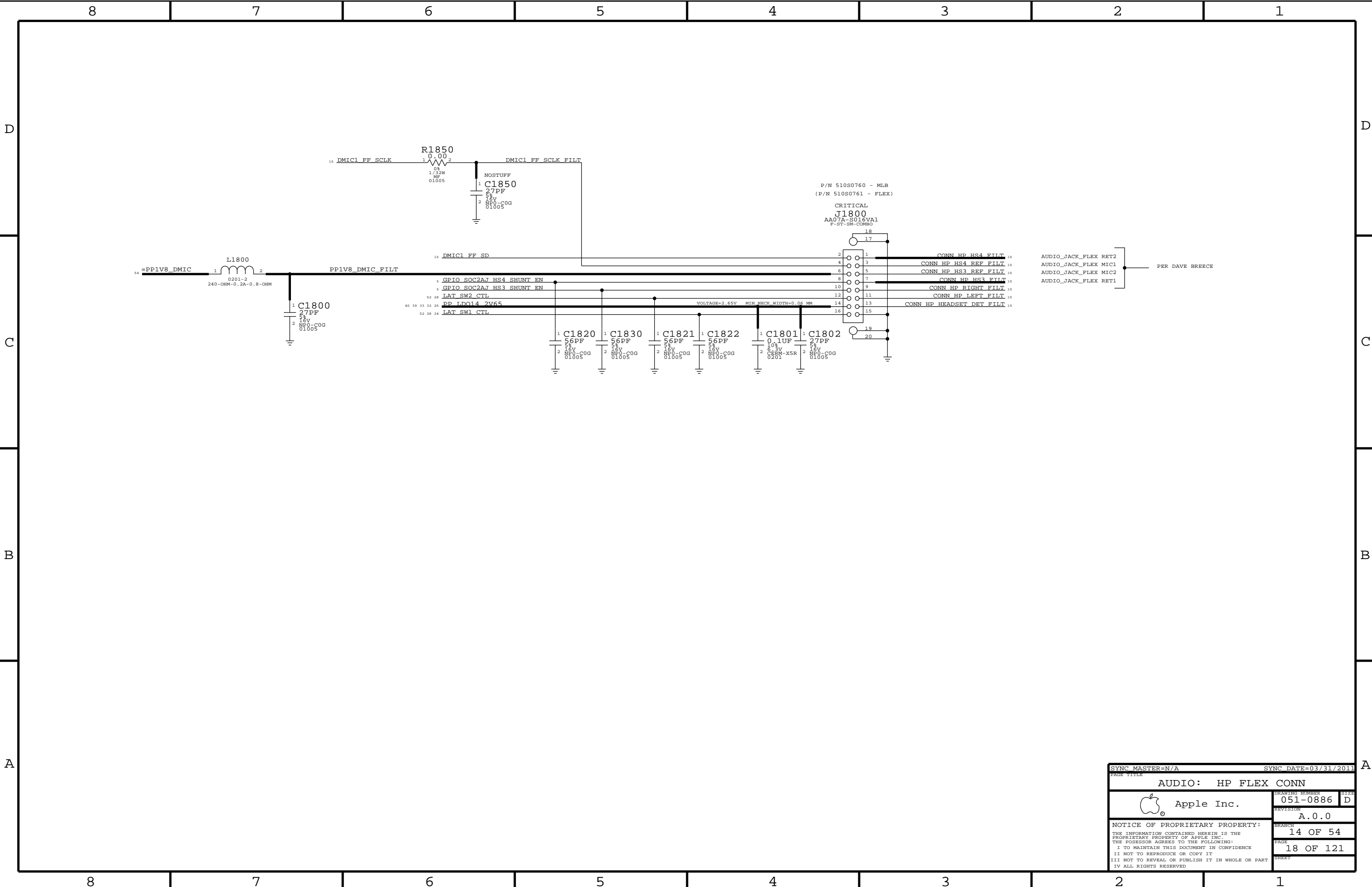




TOUCH SUBSYSTEM

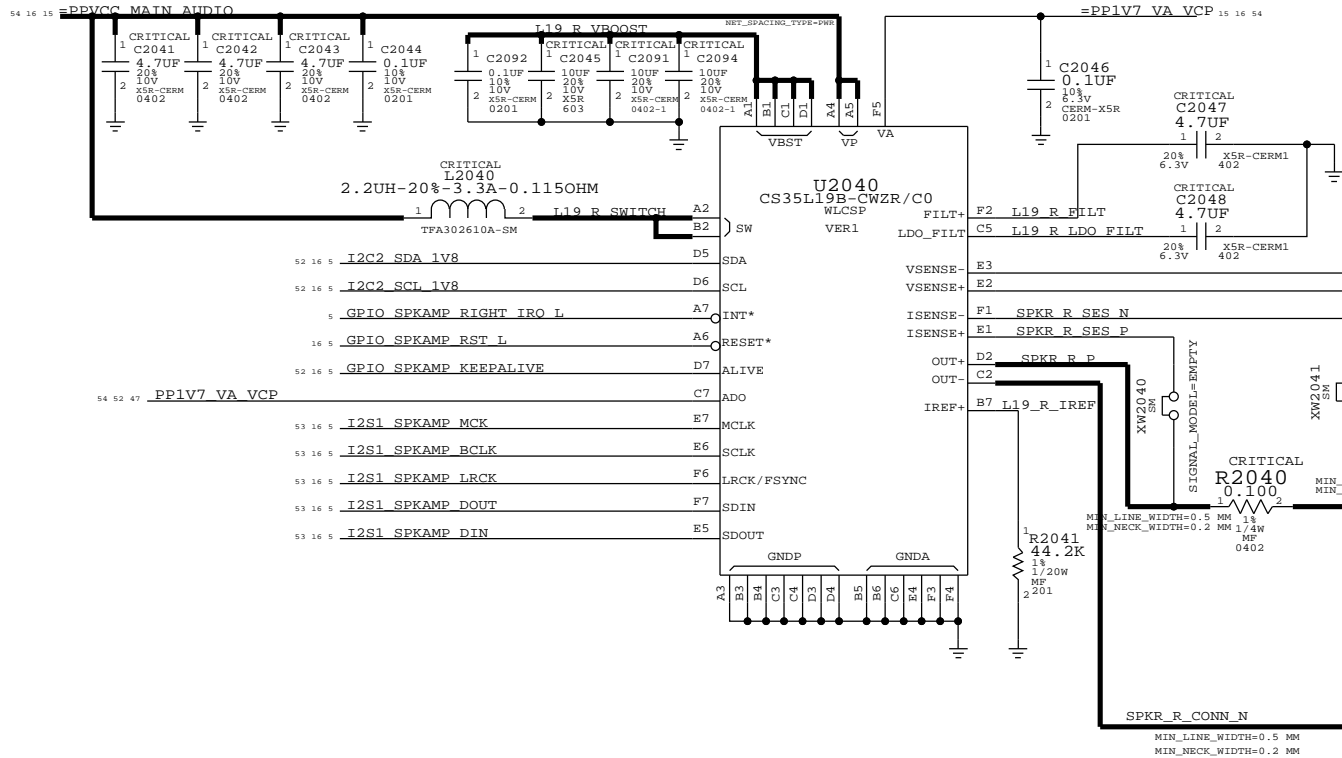


| | | | |
|---|----------------|----------------------|-----------|
| SYNC MASTER=N/A | | SYNC DATE=06/21/2010 | |
| PAGE TITLE | | | |
| TOUCH: SUPPORT CKT & CONN | | | |
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RIGHT SPEAKER AMP

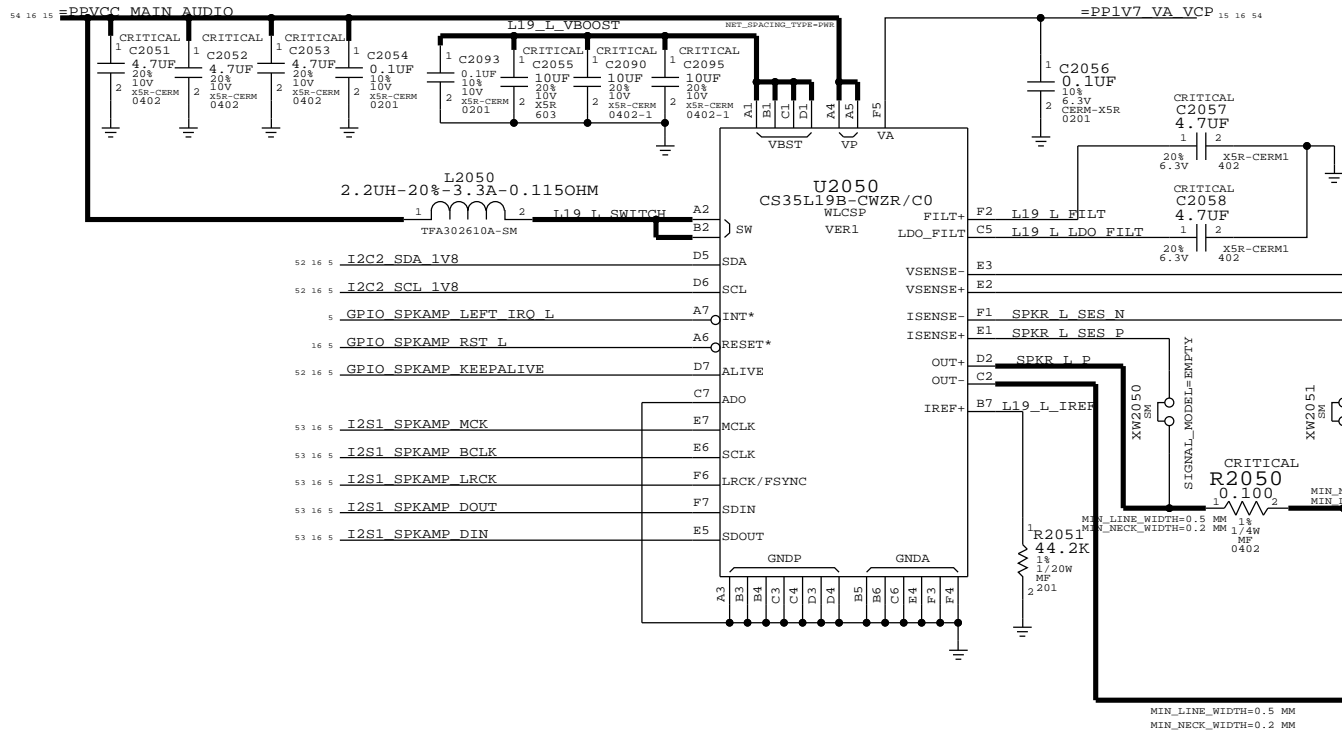
I2C ADDRESS: 1000001X



SPEAKER CONNECTOR


LEFT SPEAKER AMP

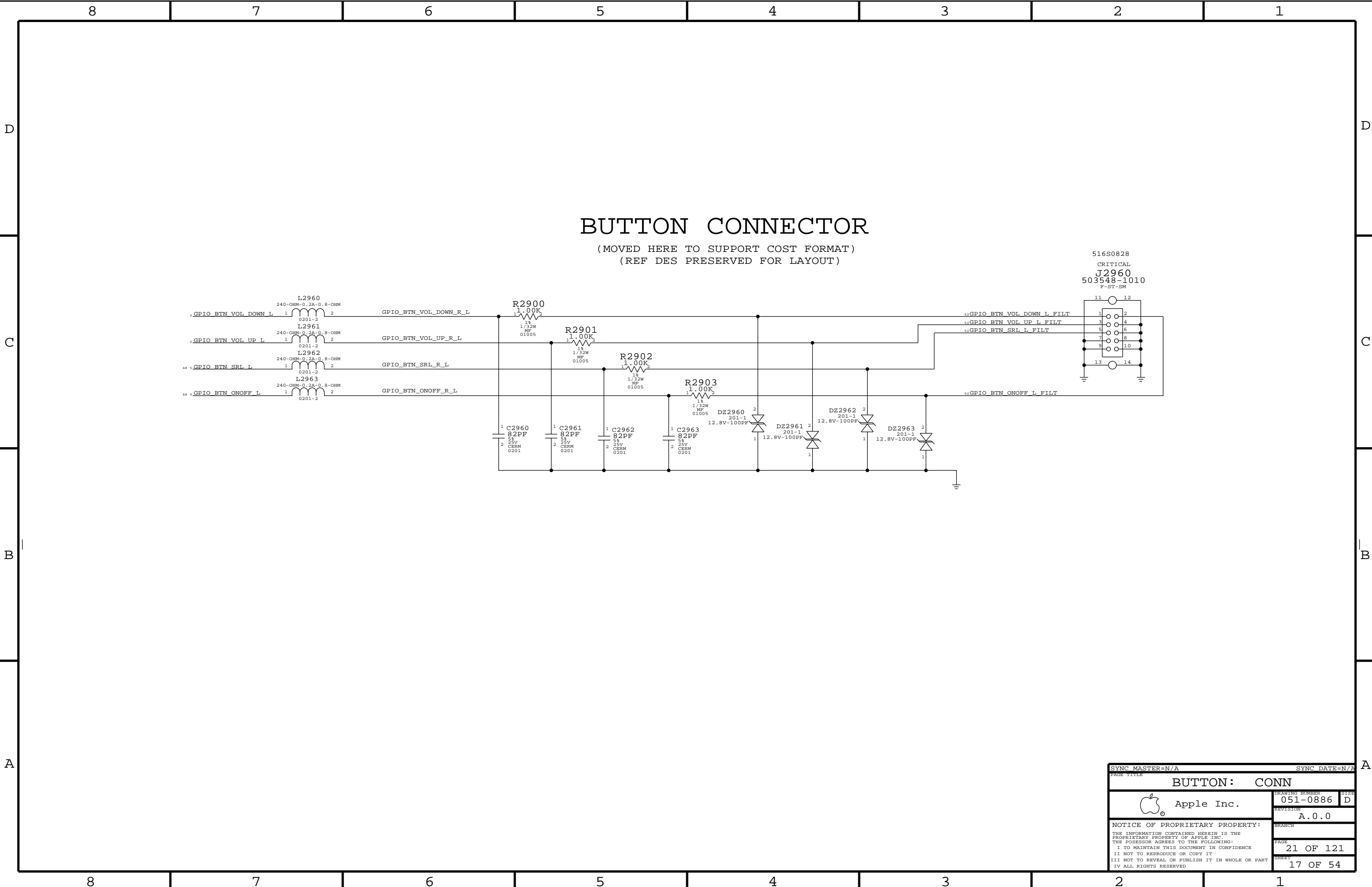
I2C ADDRESS: 1000000X



UPDATED: DEC 13

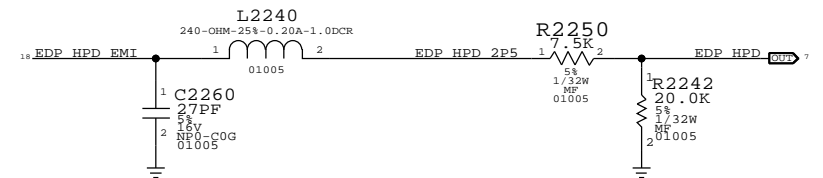
1. ALL THE EMI/DESENSE FILTER COMPONENTS HAVE BEEN REMOVED BASED ON PERFORMANCE ON J65
2. THE CURRENT VERSION OF L19 IS B0 AND WILL CHANGE TO C0 BY MARCH 2013. C0 FIXES PROCESS ISSUES.

| | | | |
|---|--|----------------------|----------|
| SYNC MASTER=KAVITHA | | SYNC DATE=01/18/2012 | |
| PAGE TITLE | | | |
| AUDIO: CS35L19A AMPS | | | |
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| PAGE | | 20 OF 121 | |
| SHEET | | 16 OF 34 | |

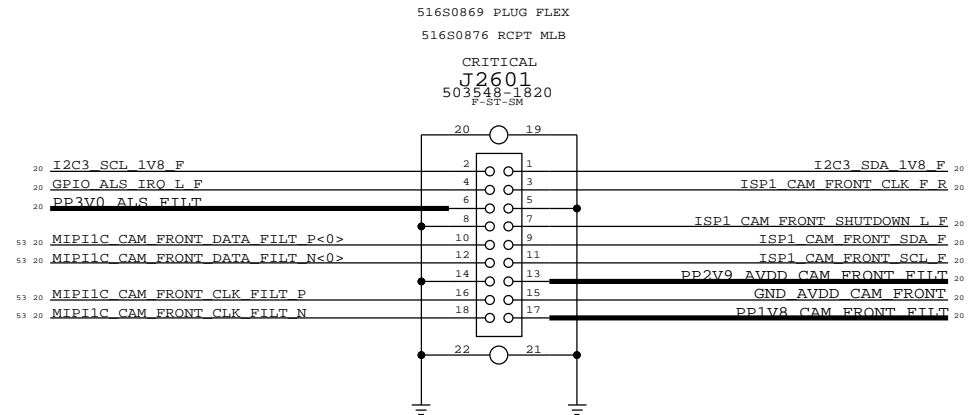
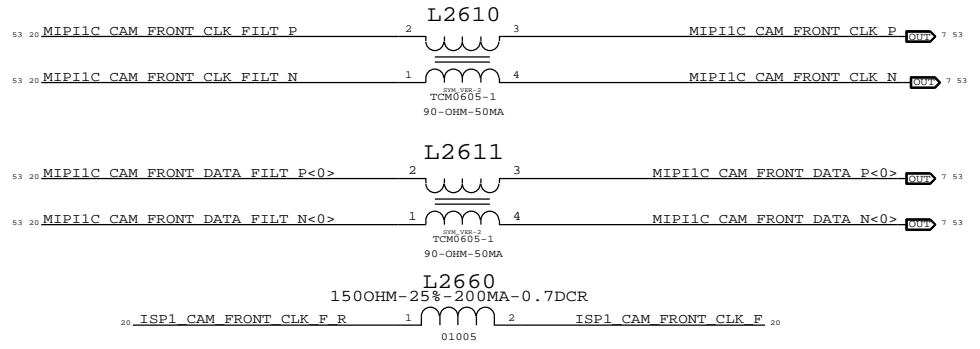
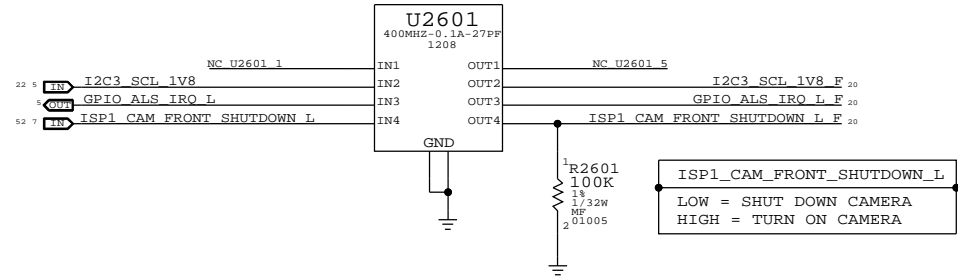
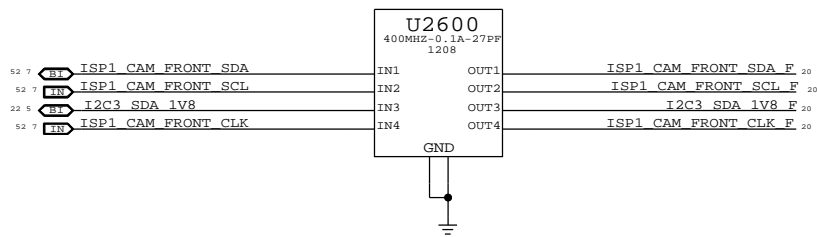
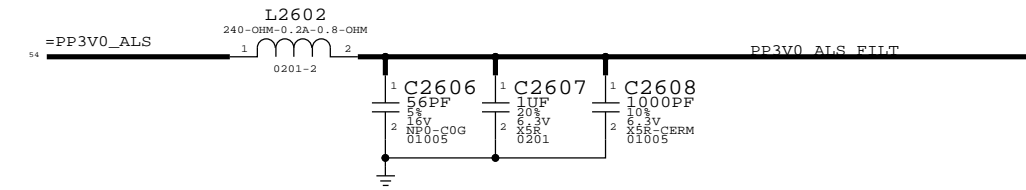
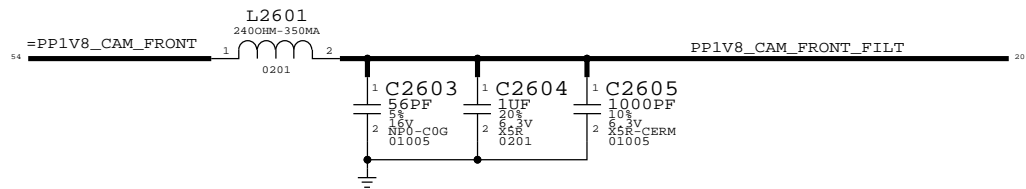
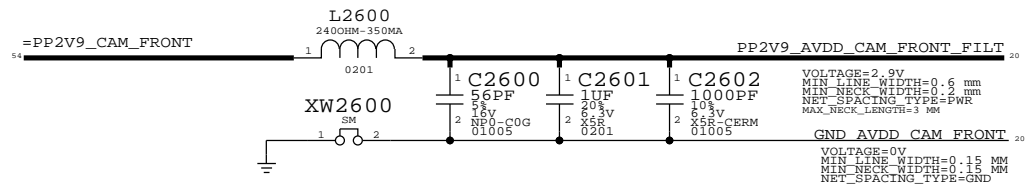


| | | | | | |
|---|--|-----------------|--|---------------|--|
| PAGE TITLE | | SYNC MASTER=N/A | | SYNC DATE=N/A | |
| BUTTON: CONN | | DRAWING NUMBER | | SIZE | |
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| | | A.0.0 | | | |
| | | PAGE | | SHEET | |
| | | 21 OF 121 | | 17 OF 54 | |

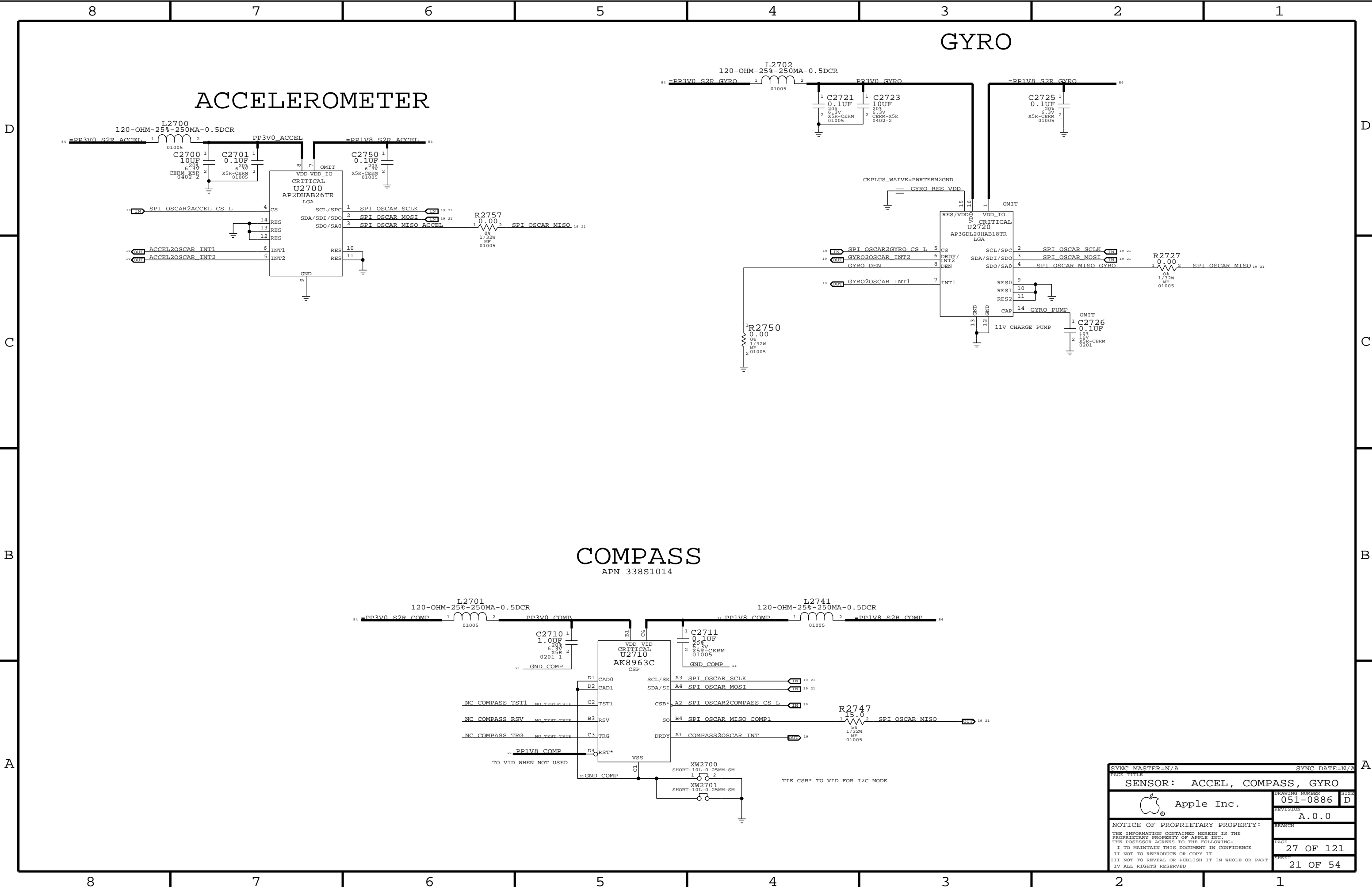
| | | | | | | | |
|---|---|---|---|---|---|---|---|
| 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 |
|---|---|---|---|---|---|---|---|

DD

VGA FRONT CAMERA CONNECTOR

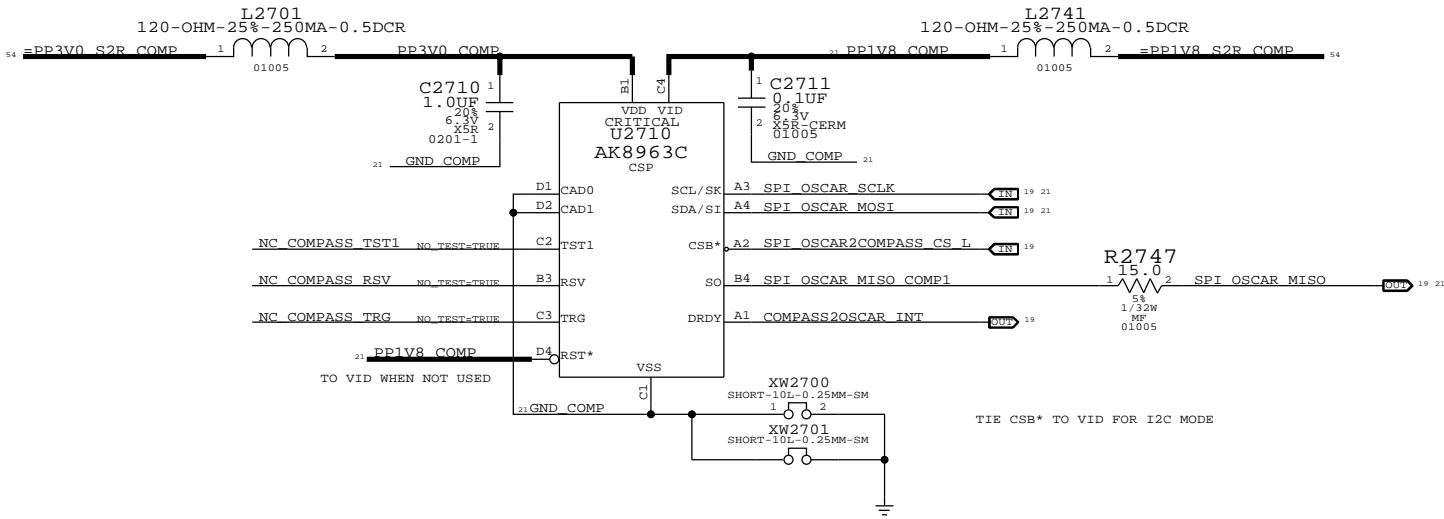


| | | | |
|---|--|----------------------|-----------|
| PAGE TITLE | | SYNC DATE=12/03/2012 | |
| CAMERA: FF-ALS CONN & FILTERS | | DRAWING NUMBER | 051-0886 |
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COMPASS

APN 338S1014



| | | | |
|---|--|-------------|--|
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| SENSOR: ACCEL, COMPASS, GYRO | | 051-0886 | |
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C

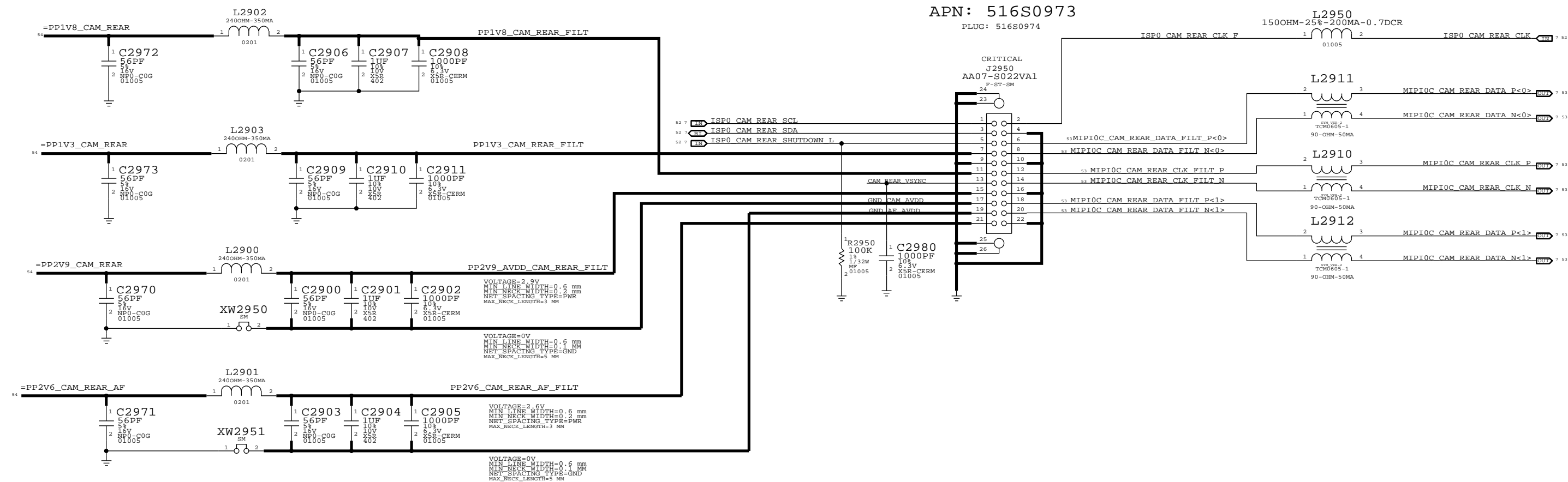
B


A

REAR CAMERA CONNECTOR

APN: 516S0973

PLUG: 516S0974

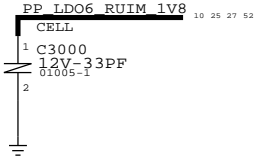


| | | | |
|---|--|----------------|-----------|
| SYNC MASTER=N/A | | SYNC DATE=N/A | |
| PAGE TITLE | | | |
| CAMERA: REAR CONN & FILTERS | | | |
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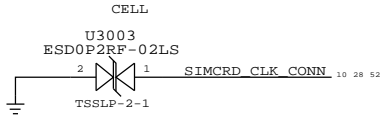
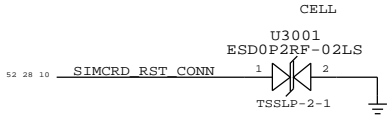
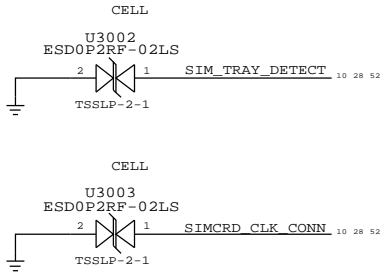
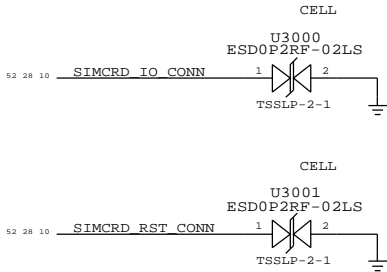
AP INTERFACE & DEBUG CONNECTORS

PROBE POINTS

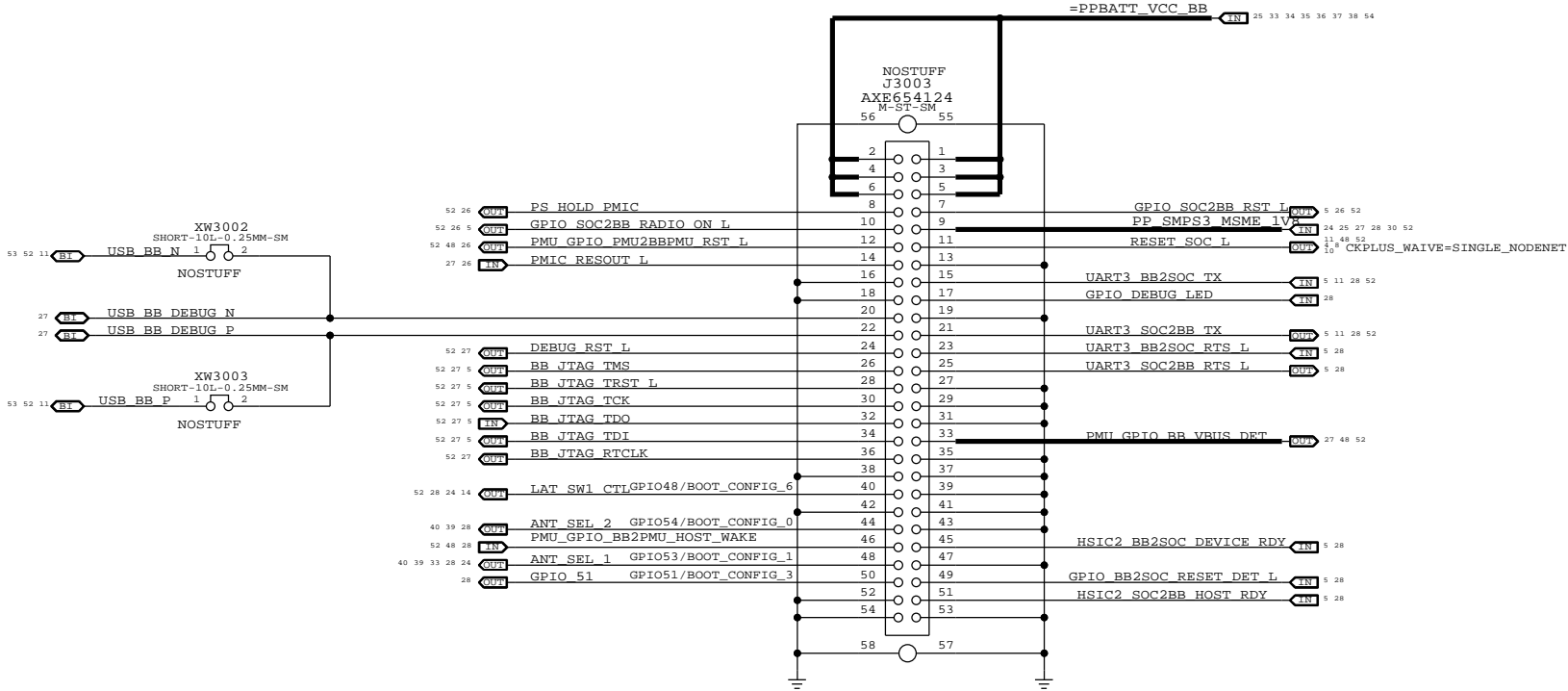
| | | | | |
|--------|------|---|-----------------------|-------|
| PP3000 | P4MM | 1 | BB_ERROR_FLAG | 28 |
| PP3001 | P4MM | 1 | SLEEP_CLK_32K | 26 27 |
| PP3002 | P4MM | 1 | PMIC_SSBI | 26 27 |
| PP3003 | P4MM | 1 | 19P2M_MDM | 26 27 |
| PP3008 | P4MM | 1 | WTR_SSBI_TX_GPS | 28 29 |
| PP3009 | P4MM | 1 | WTR_SSBI_PRX_DRX | 28 29 |
| PP3010 | P4MM | 1 | WTR_RX_ON | 28 29 |
| PP3011 | P4MM | 1 | WTR_RF_ON | 28 29 |
| PP3012 | P4MM | 1 | UART_WLAN2BB_LTE_COEX | 28 44 |
| PP3013 | P4MM | 1 | UART_BB2WLAN_LTE_COEX | 28 44 |



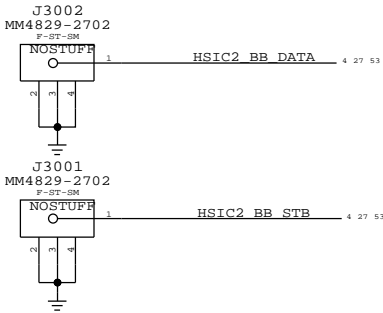
SIM CARD ESD PROTECTION



DEBUG CONNECTOR



| BOOT OPTIONS | BOOT_CONFIG SW REGISTER VALUE | GPIO/BOOT_CONFIG CONFIGURATION | | | | | | | |
|------------------------|-------------------------------|--------------------------------|---|---|---|---|---|---|---|
| | | 6 | 5 | 4 | 3 | 2 | 1 | 0 | |
| BOOT_DEFAULT_OPTION | 0X00 | X | 0 | 0 | 0 | 0 | 0 | 0 | X |
| BOOT_NAND_OPTION | 0X01 | X | 1 | 0 | 0 | 0 | 0 | 0 | 1 |
| BOOT_HSIC_OPTION | 0X02 | X | 1 | 0 | 0 | 0 | 0 | 1 | 0 |
| BOOT_USB_OPTION | 0X03 | X | 1 | 0 | 0 | 0 | 0 | 1 | 1 |
| ENABLE_SAHARA_PROTOCOL | 0X08 | X | 1 | 0 | 0 | 1 | 0 | X | X |



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|---|----------------|-----------|
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| CELL:AP INTERFACE & DEBUG CONNECTORS | | |
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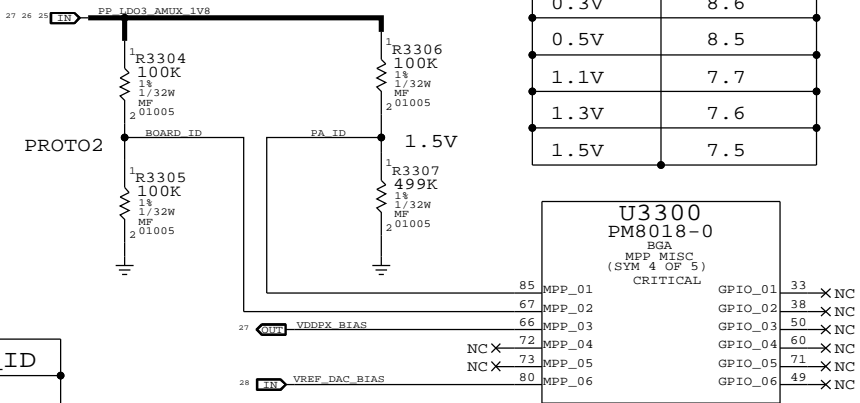
A

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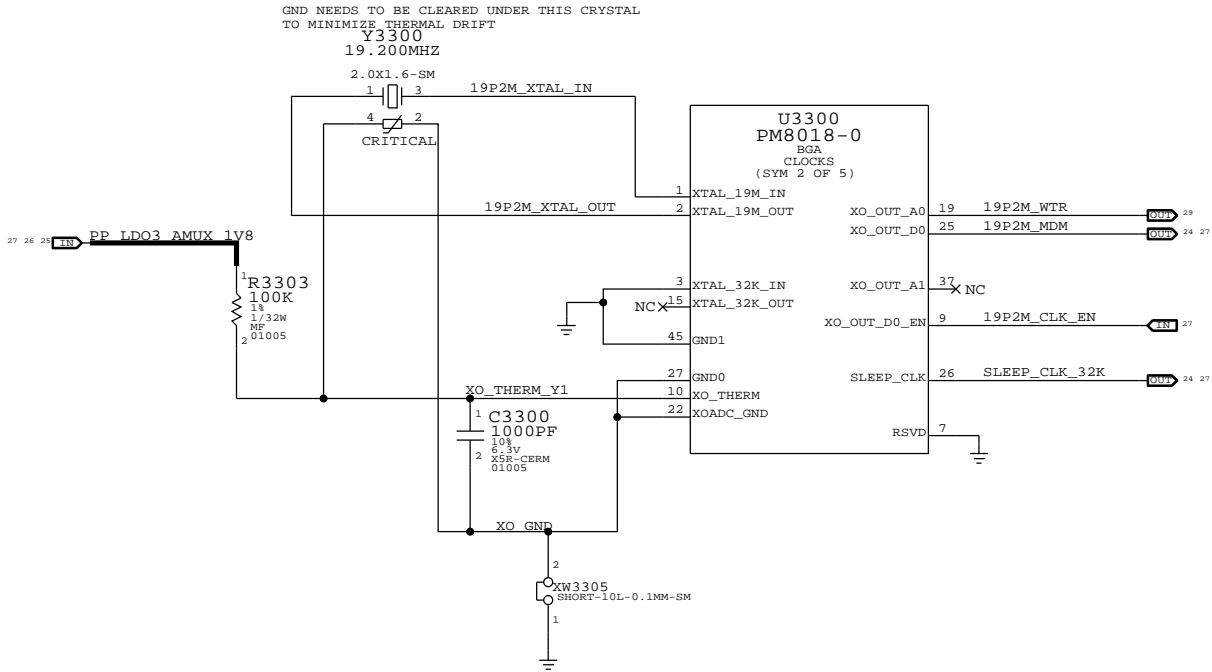
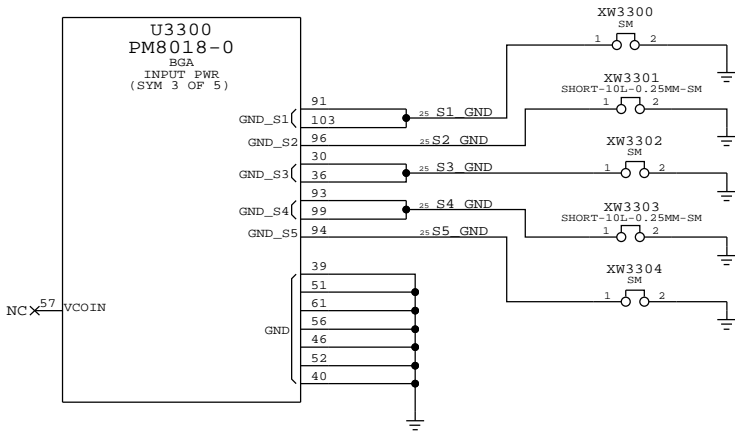
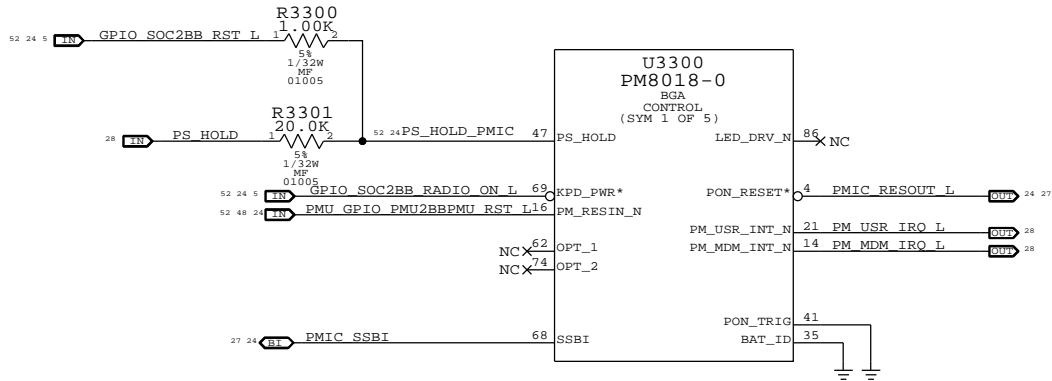
PMU (2 OF 2)


| BOARD_ID | REVISION |
|----------|----------|
| 0.7V | PROTO1 |
| 0.9V | PROTO2 |
| 1.1V | EVT1 |
| 1.3V | EVT2 |
| 1.5V | DVT |
| 1.7V | PVT |

| BB GPIO_29 | PRODUCT_ID |
|------------|------------|
| 1 (1.8V) | JXX |
| 0 (NC, PD) | NXX |

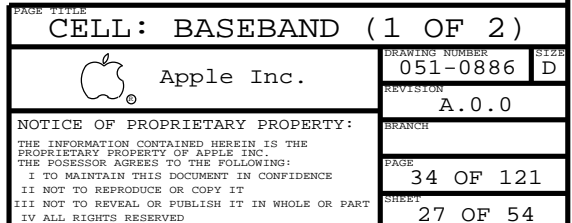


| PA_ID | MAV VER |
|-------|---------|
| 0.1V | 8.7 |
| 0.3V | 8.6 |
| 0.5V | 8.5 |
| 1.1V | 7.7 |
| 1.3V | 7.6 |
| 1.5V | 7.5 |



| PAGE TITLE | | |
|---|----------------|-----------|
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| | SHEET | 26 OF 54 |
| | SIZE | D |

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BASEBAND (2 OF 2)

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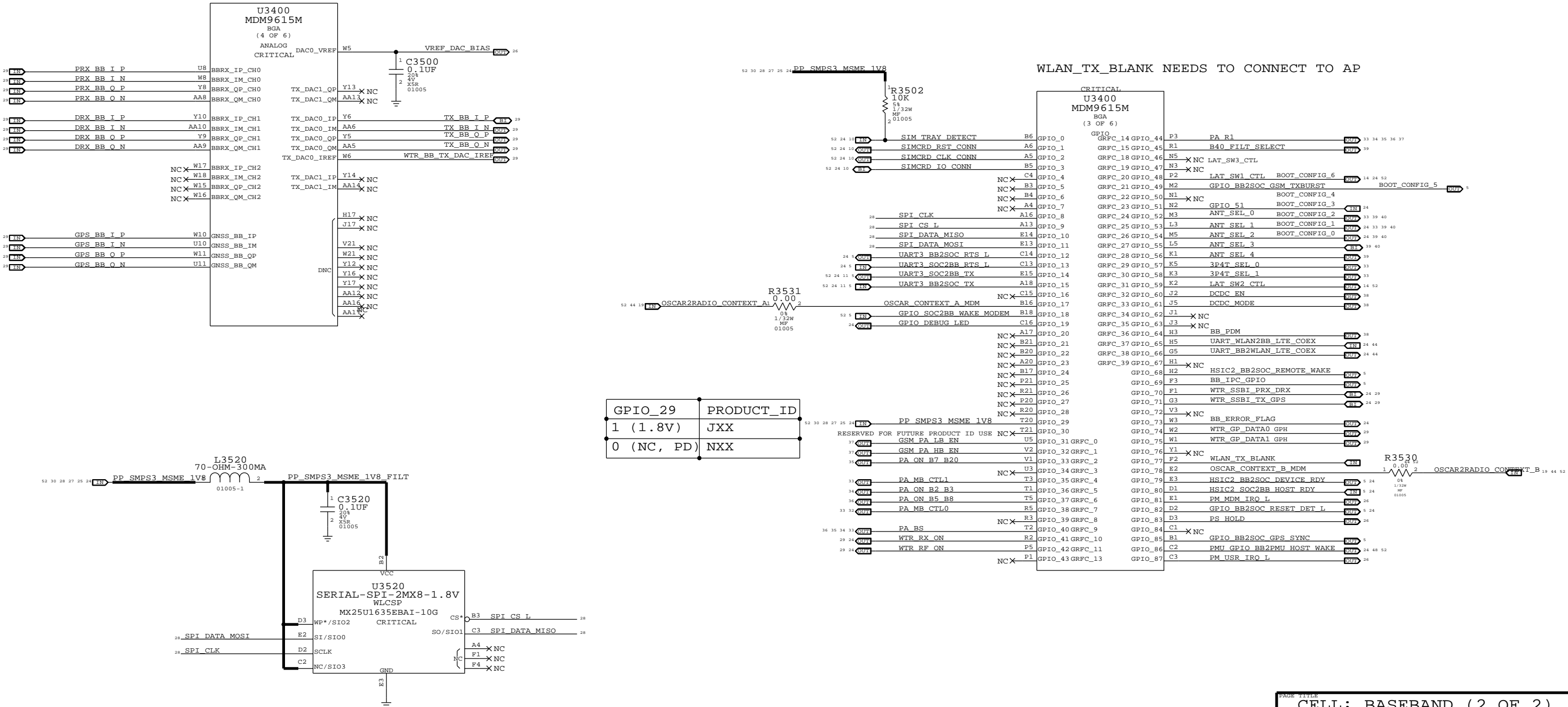
A

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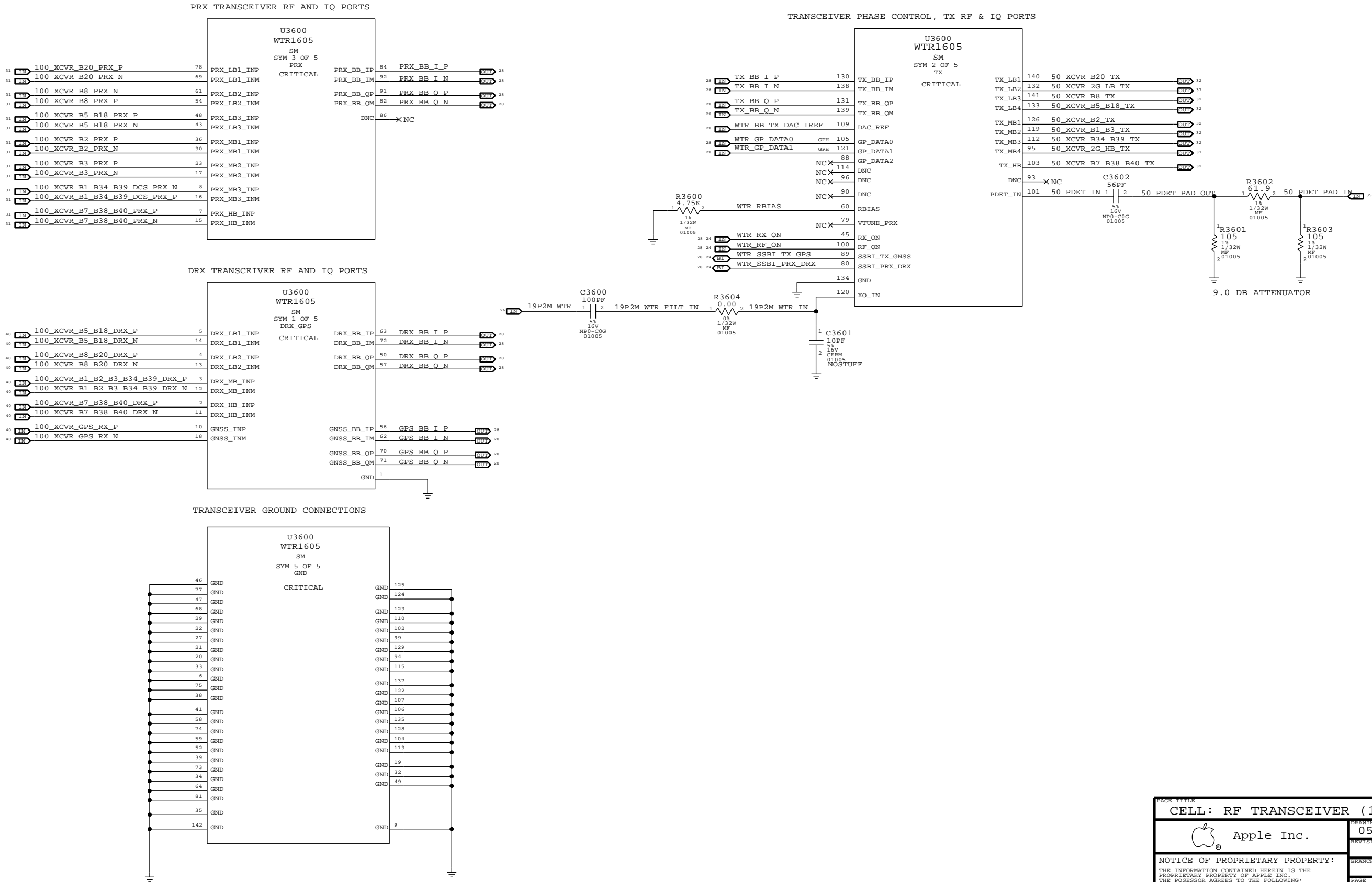
B

A




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|---|-----------------------------------|
| PAGE TITLE CELL: BASEBAND (2 OF 2) | |
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RF TRANSCEIVER (1 OF 2)



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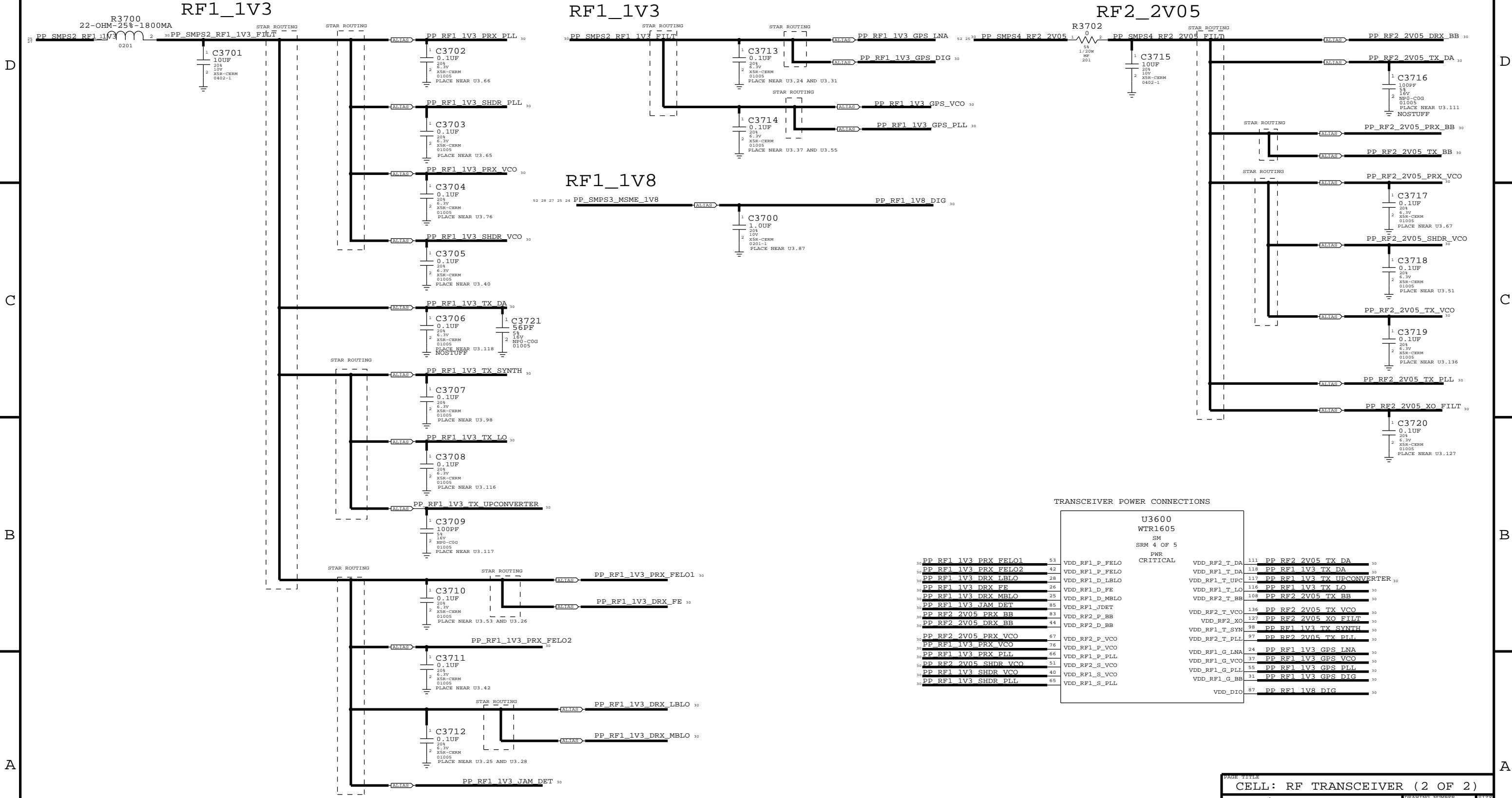
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PAGE
36 OF 121

SHEET
29 OF 54

RF TRANSCEIVER (2 OF 2)

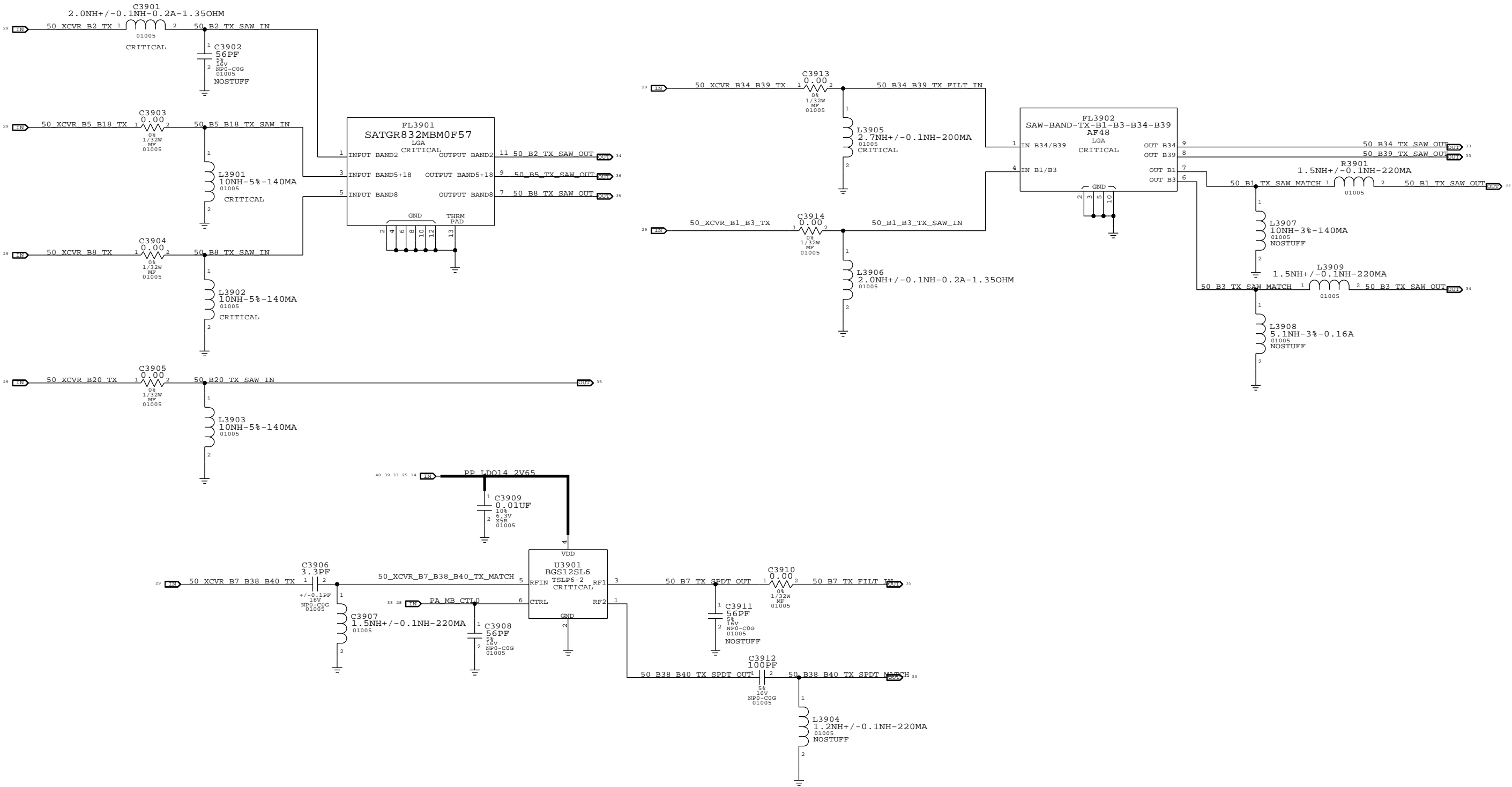


A



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TX INTERSTAGE FILTERS




| | | |
|---|----------------|-----------|
| PAGE TITLE | | |
| CELL: RF TRANSCEIVER (3 OF 4) | | |
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| | SHEET | 32 OF 54 |
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| PAGE TITLE | |
| CELL: PENTABAND PA | |
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| | D |
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| BRANCH | PAGE |
| | 40 OF 121 |
| SHEET | 33 OF 54 |

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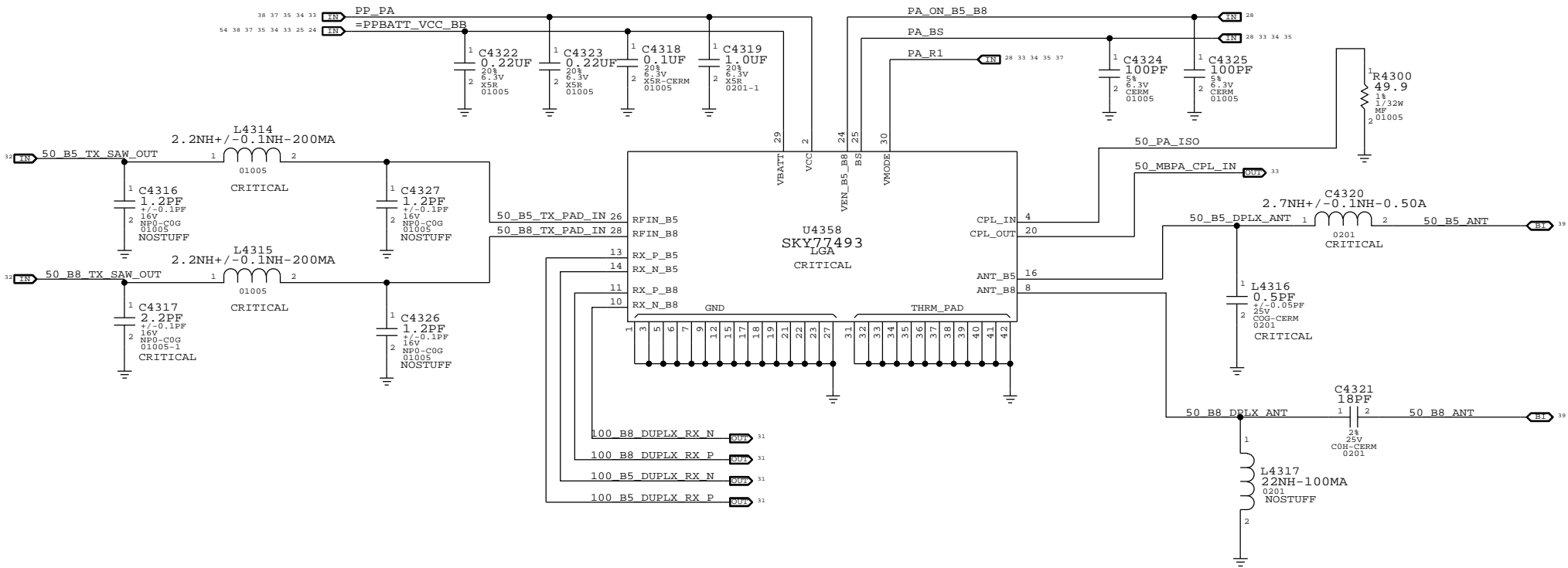
BD

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BAND 5 / 8 PAD



| BAND | PA | POWER | MODE | PA_BS | PA_ON_B5_B8 | PA_R1 |
|-------|-------|-------|-------|-------|-------------|-------|
| ===== | ===== | ===== | ===== | ===== | ===== | ===== |
| OFF | | X | | X | 0 | X |
| B5 | | HPM | | 0 | 1 | 0 |
| B5 | | LPM | | 0 | 1 | 1 |
| B8 | | HPM | | 1 | 1 | 0 |
| B8 | | LPM | | 1 | 1 | 1 |

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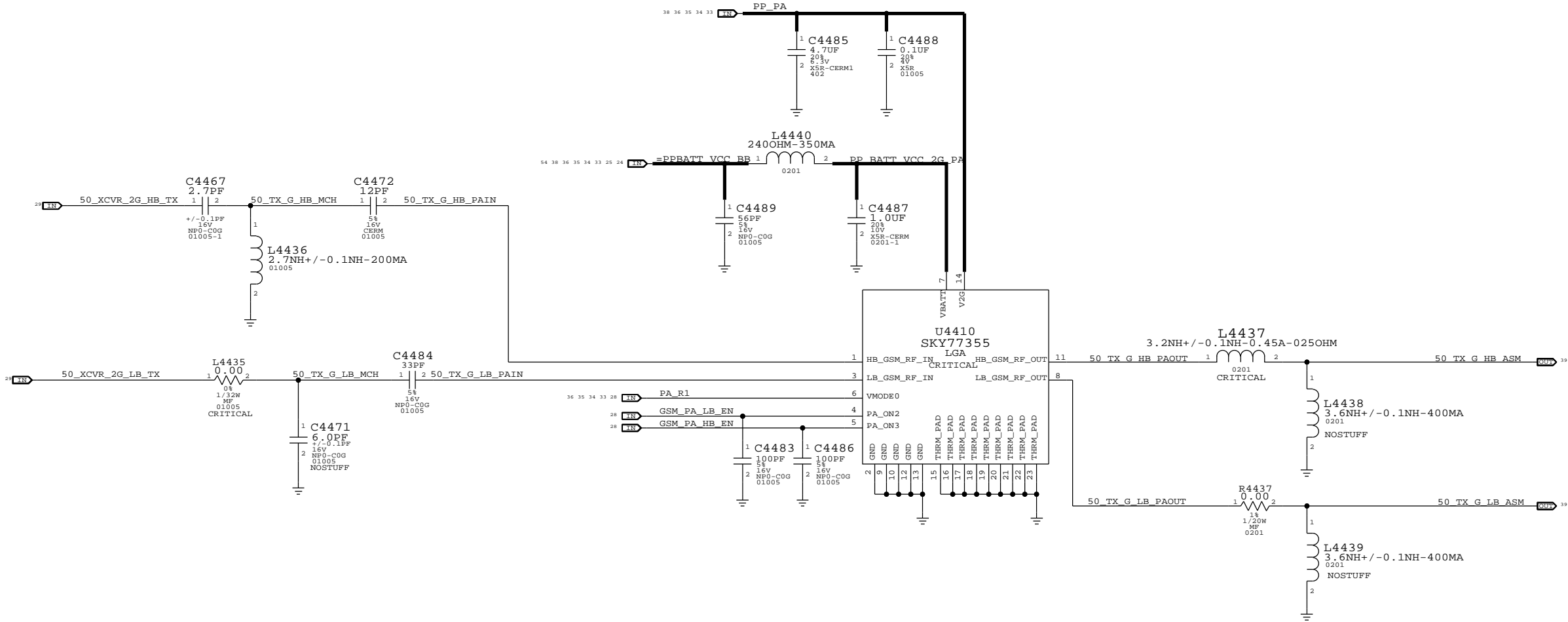
| | | |
|---|----------------|-----------|
| PAGE TITLE | | |
| CELL: BAND 5/8 PAD | | |
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2G PA


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2G PA GAIN MODES

| BAND | MODE | GAIN MODE | PA_R1 | PCL RANGE |
|-----------|------|-----------|-------|-----------|
| LOW BAND | GSM | ULTRA LOW | HIGH | 16 TO 19 |
| LOW BAND | GSM | LOW | HIGH | 14 TO 15 |
| LOW BAND | GSM | MEDIUM | LOW | 7 TO 13 |
| LOW BAND | GSM | HIGH | LOW | 5 TO 6 |
| HIGH BAND | GSM | ULTRA LOW | HIGH | 10 TO 15 |
| HIGH BAND | GSM | LOW | HIGH | 7 TO 9 |
| HIGH BAND | GSM | HIGH | LOW | 0 TO 6 |
| LOW BAND | EDGE | LOW | HIGH | 15 TO 19 |
| LOW BAND | EDGE | MEDIUM | LOW | 10 TO 14 |
| LOW BAND | EDGE | HIGH | LOW | 8 TO 9 |
| HIGH BAND | EDGE | LOW | HIGH | 9 TO 15 |
| HIGH BAND | EDGE | HIGH | LOW | 2 TO 8 |



CELL: 2G PA

 Apple Inc.

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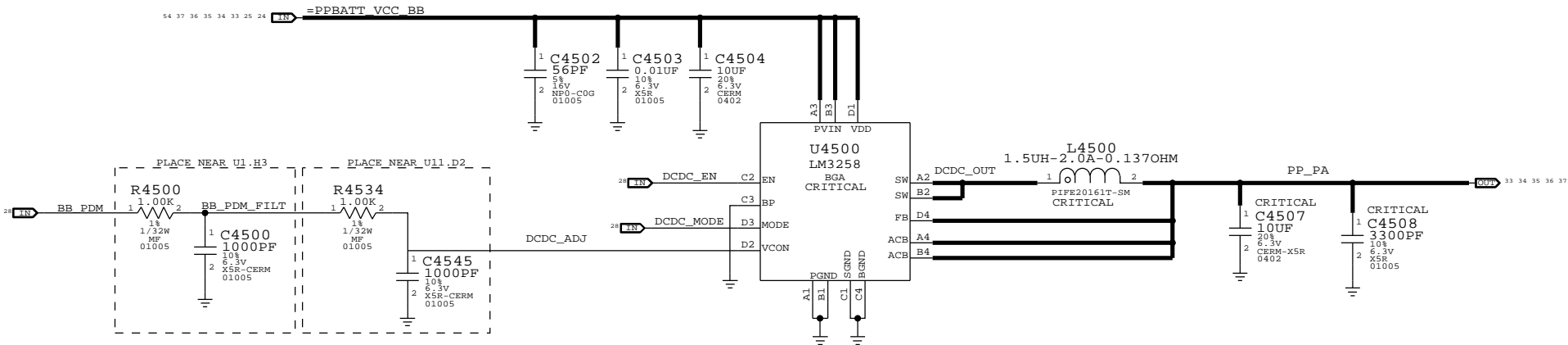
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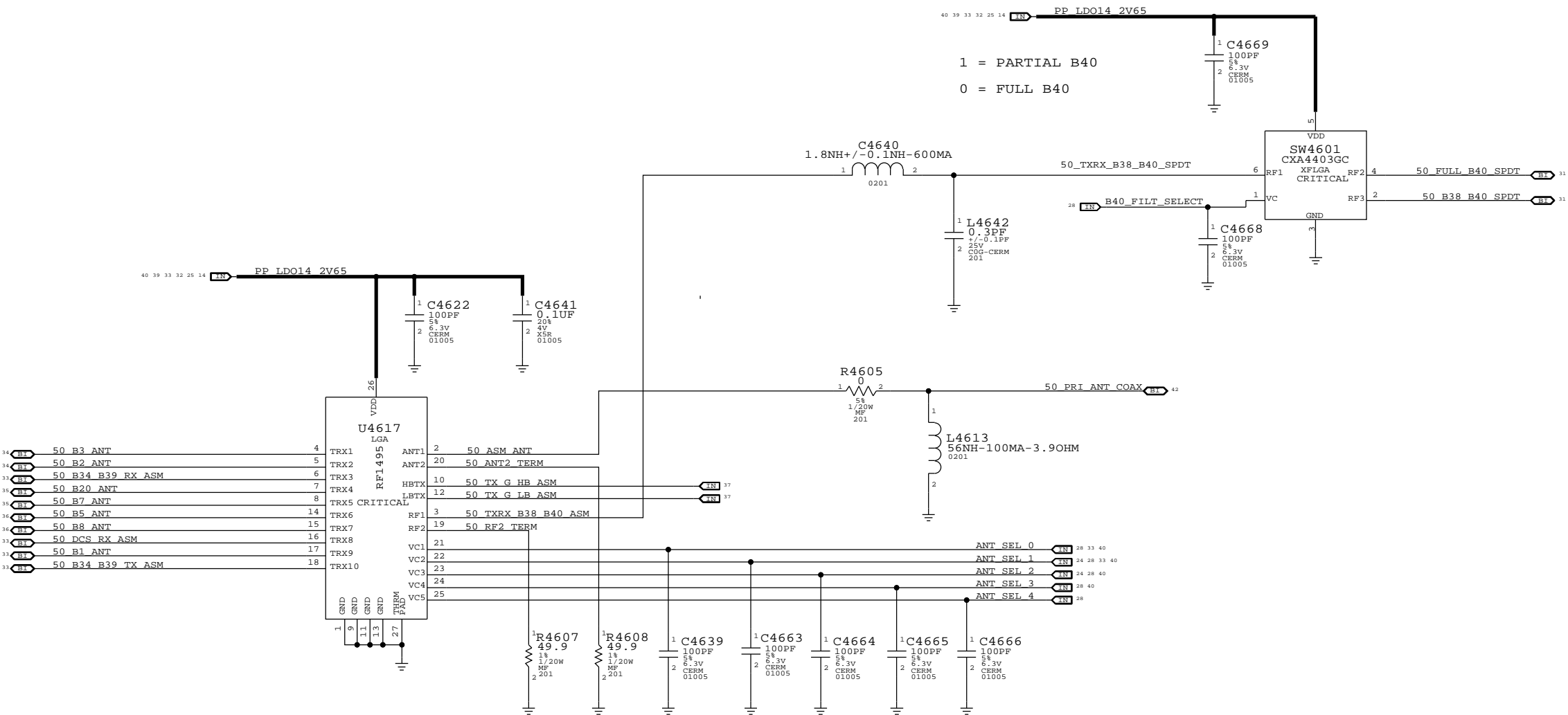
37 OF 54


PA DC/DC CONVERTER



| PAGE TITLE | | |
|---|----------------|-----------|
| CELL: PA DCDC CONVERTER | | |
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| | SHEET | 38 OF 54 |

PRIMARY ASM



| PAGE TITLE | | |
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| | SHEET | 39 OF 54 |
| | SIZE | D |

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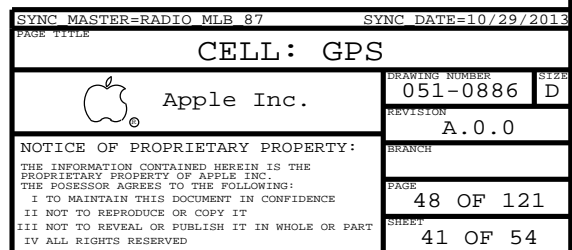
B

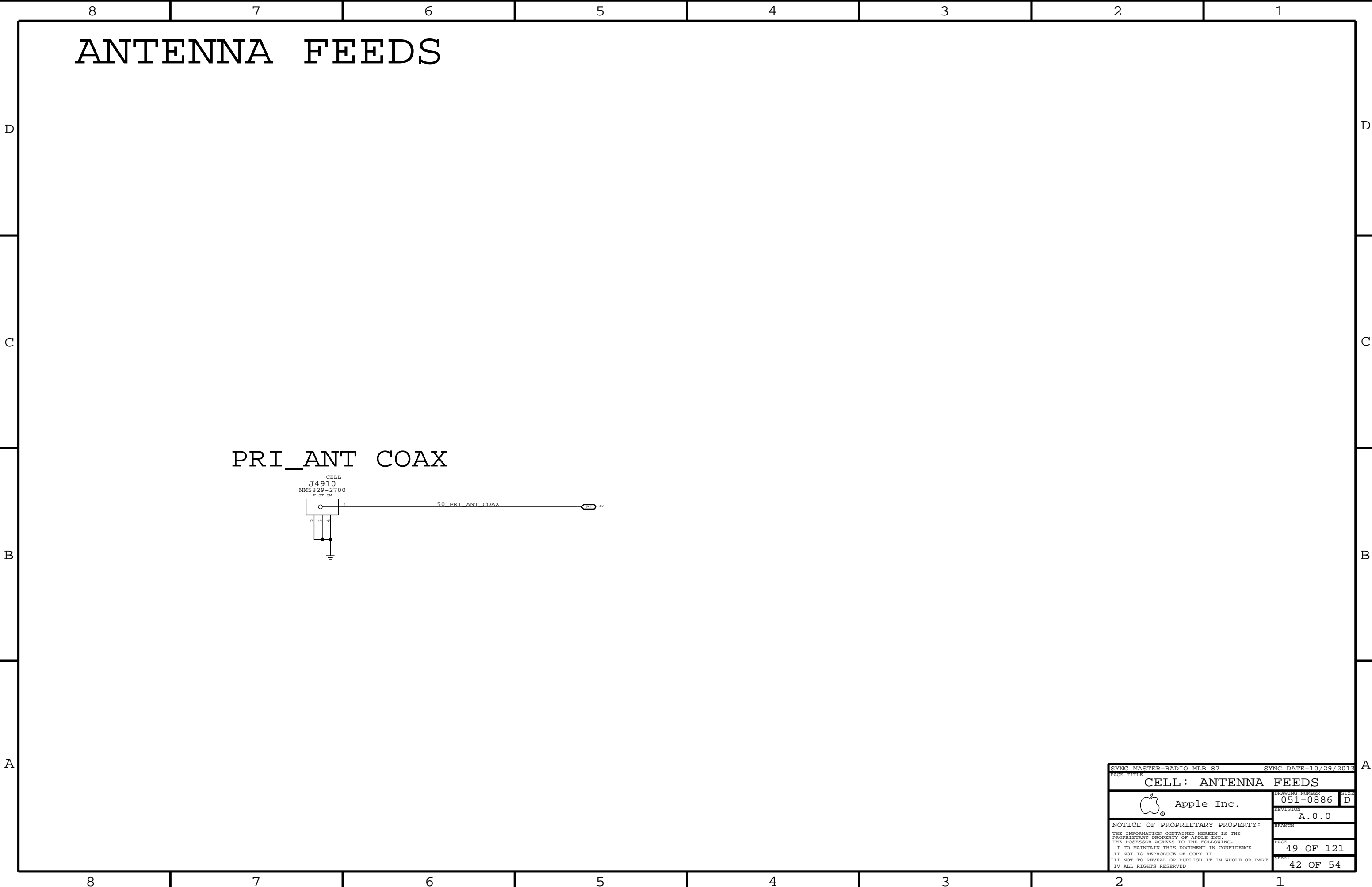
A |

| | | | | | |
|--|--|----------------|--|-----------|--|
| PAGE TITLE | | DRAWING NUMBER | | SIZE | |
| CELL: RX DIVERSITY | | 051-0886 | | D | |
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SYNC MASTER=RADIO MLB 87

SYNC DATE=10/29/2013

CELL: ANTENNA FEEDS

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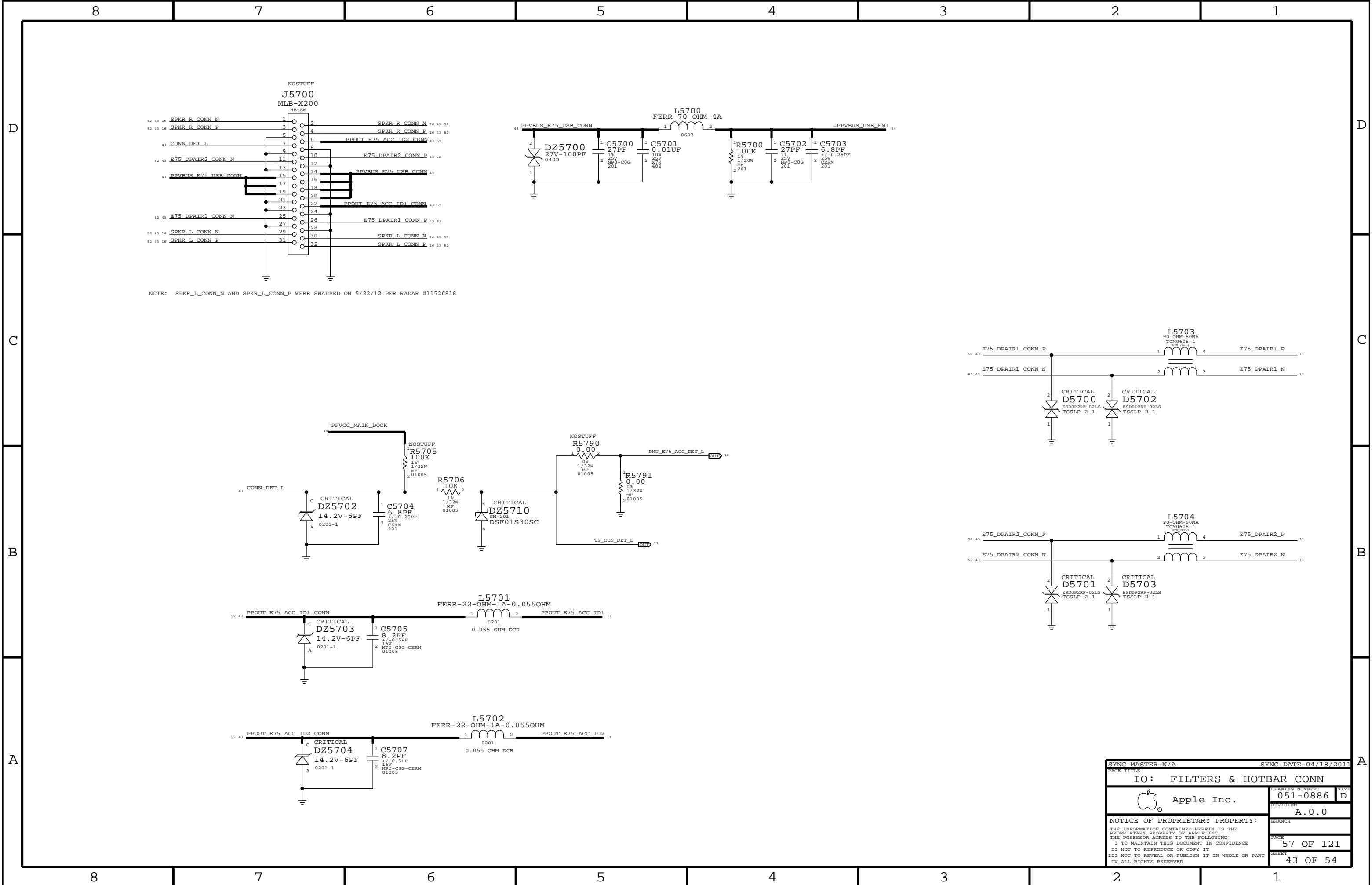
BRANCH


PAGE

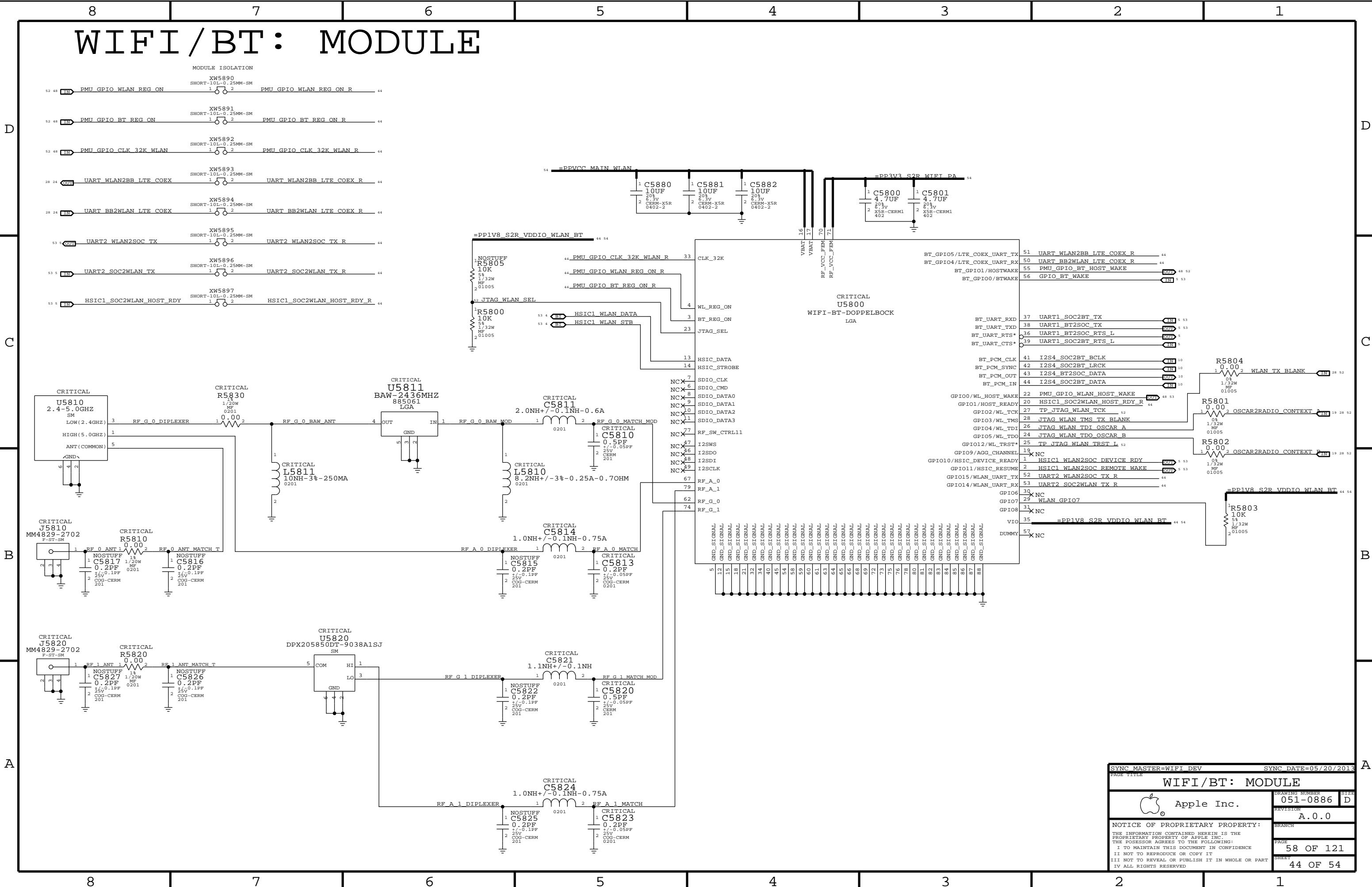
49 OF 121

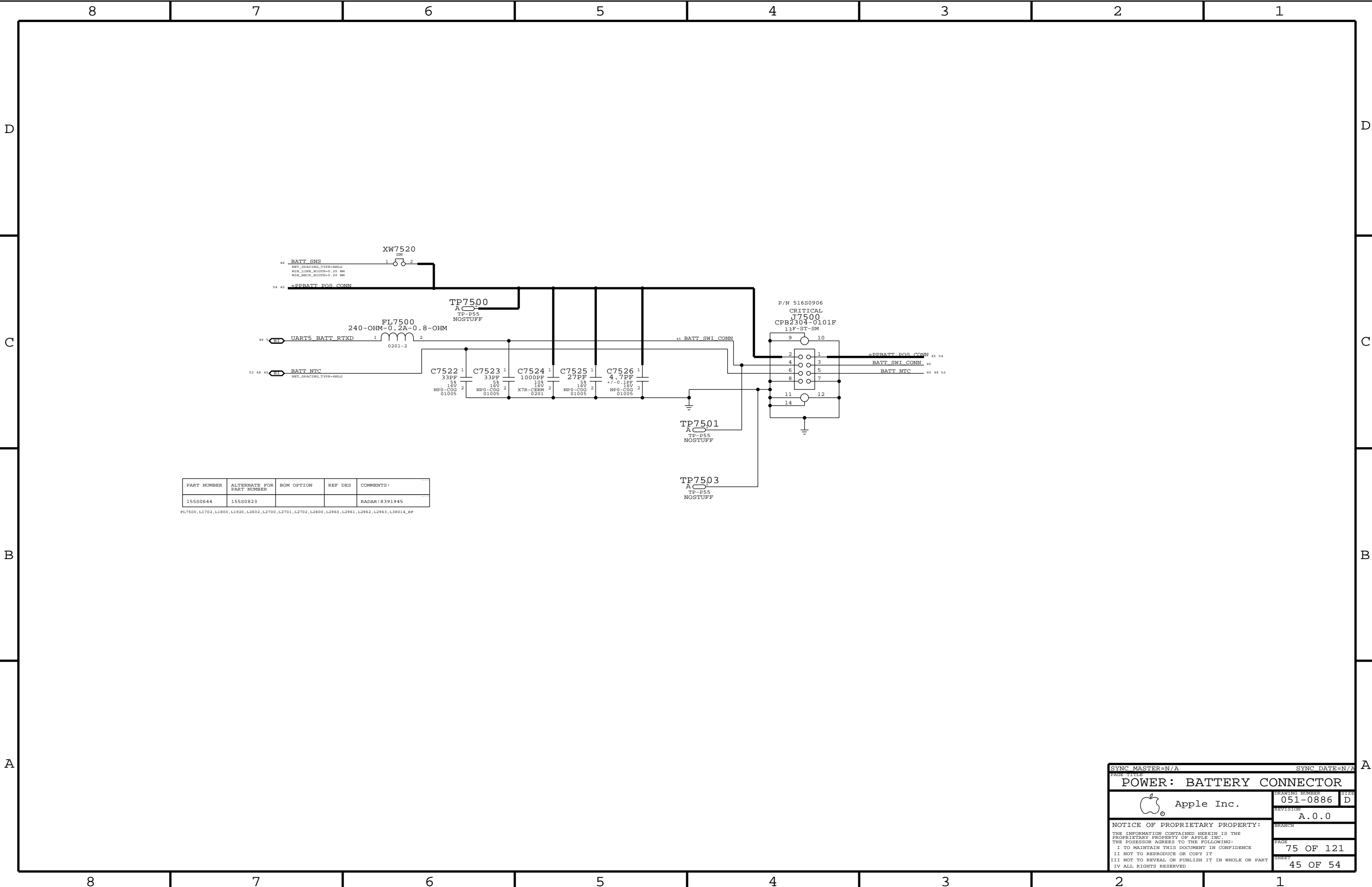
SHEET

42 OF 54



| | | | |
|--|----------------|----------------------|------|
| SYNC MASTER=N/A | | SYNC DATE=04/18/2011 | |
| PAGE TITLE | | | |
| IO: FILTERS & HOTBAR CONN | | | |
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| BRANCH | | | |
| PAGE | | | |
| 57 OF 121 | | | |
| SHEET | | | |
| 43 OF 54 | | | |






SYNC_MASTER=N/A

SYNC_DATE=N/A

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POWER: BATTERY CONNECTOR



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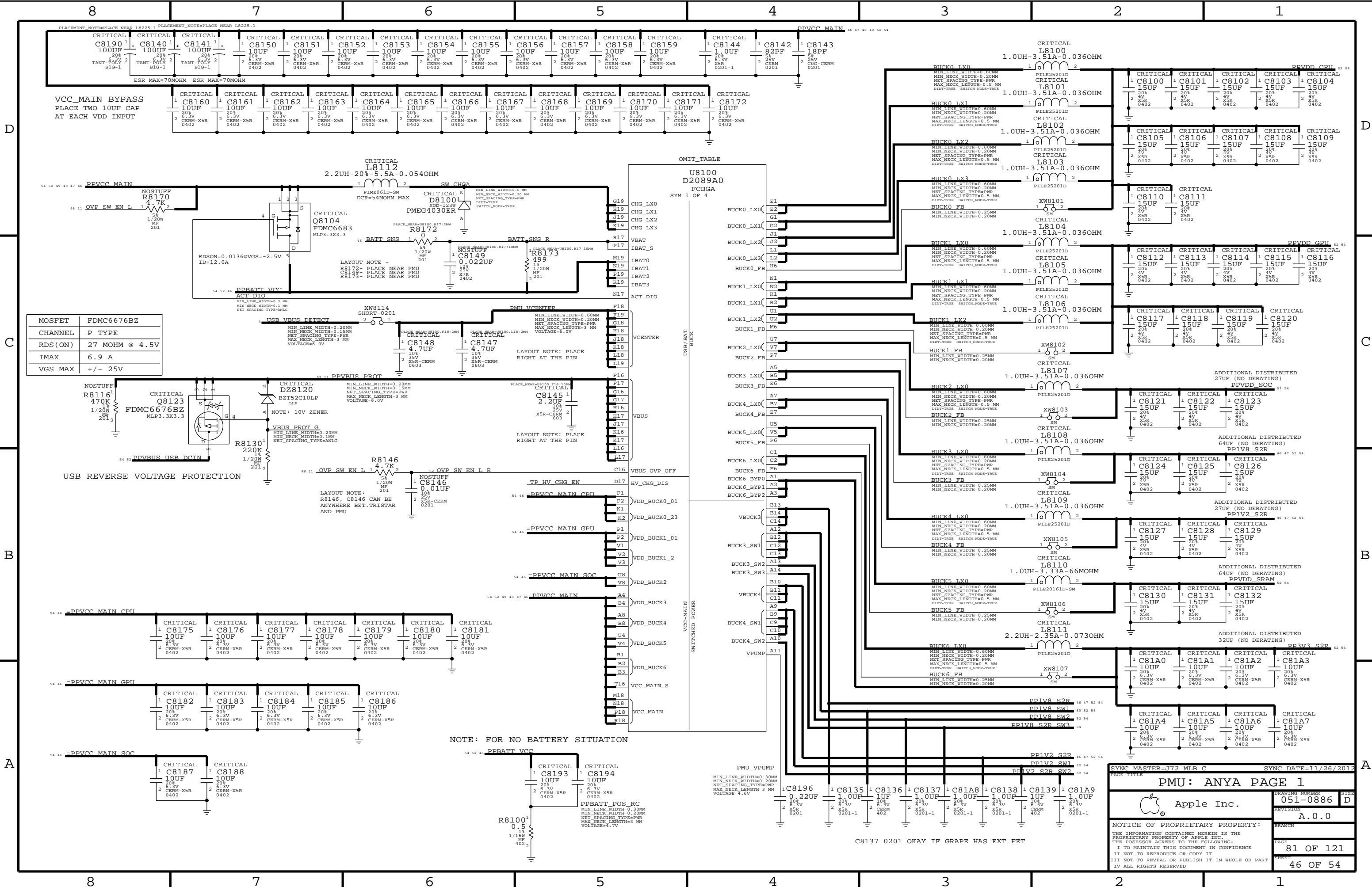
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PAGE

75 OF 121

SHEET

45 OF 54



VCC_MAIN BYPASS
PLACE TWO 10UF CAP
AT EACH VDD INPUT

| | |
|---------|----------------|
| MOSFET | FDMC6676BZ |
| CHANNEL | P-TYPE |
| RDS(ON) | 27 MOHM @-4.5V |
| IMAX | 6.9 A |
| VGS MAX | +/- 25V |

NOTE: FOR NO BATTERY SITUATION

SYNC MASTER=J72 MLB C

SYNC DATE=11/26/2012

PMU: ANYA PAGE 1

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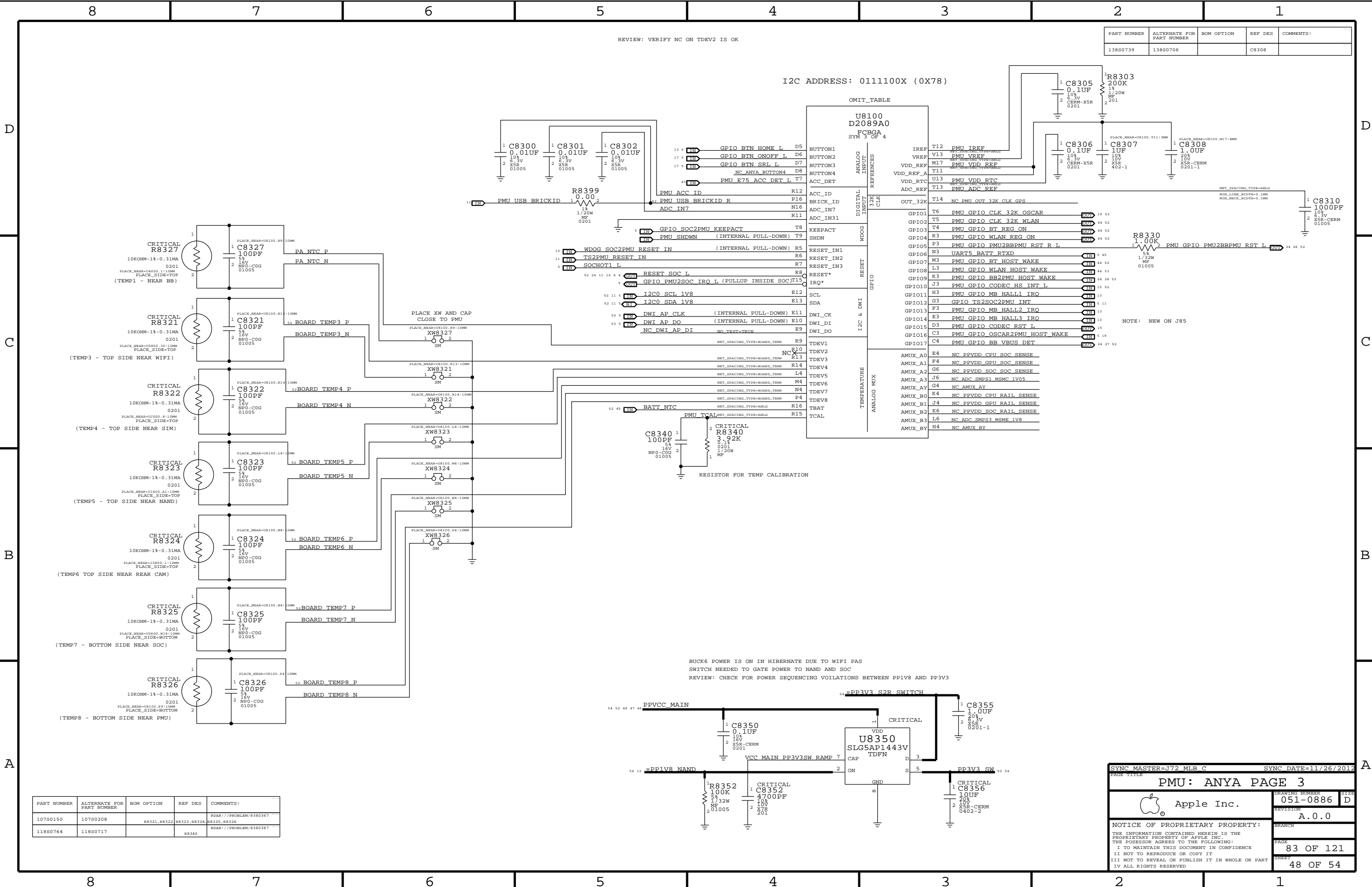
PAGE

SHEET

46 OF 121

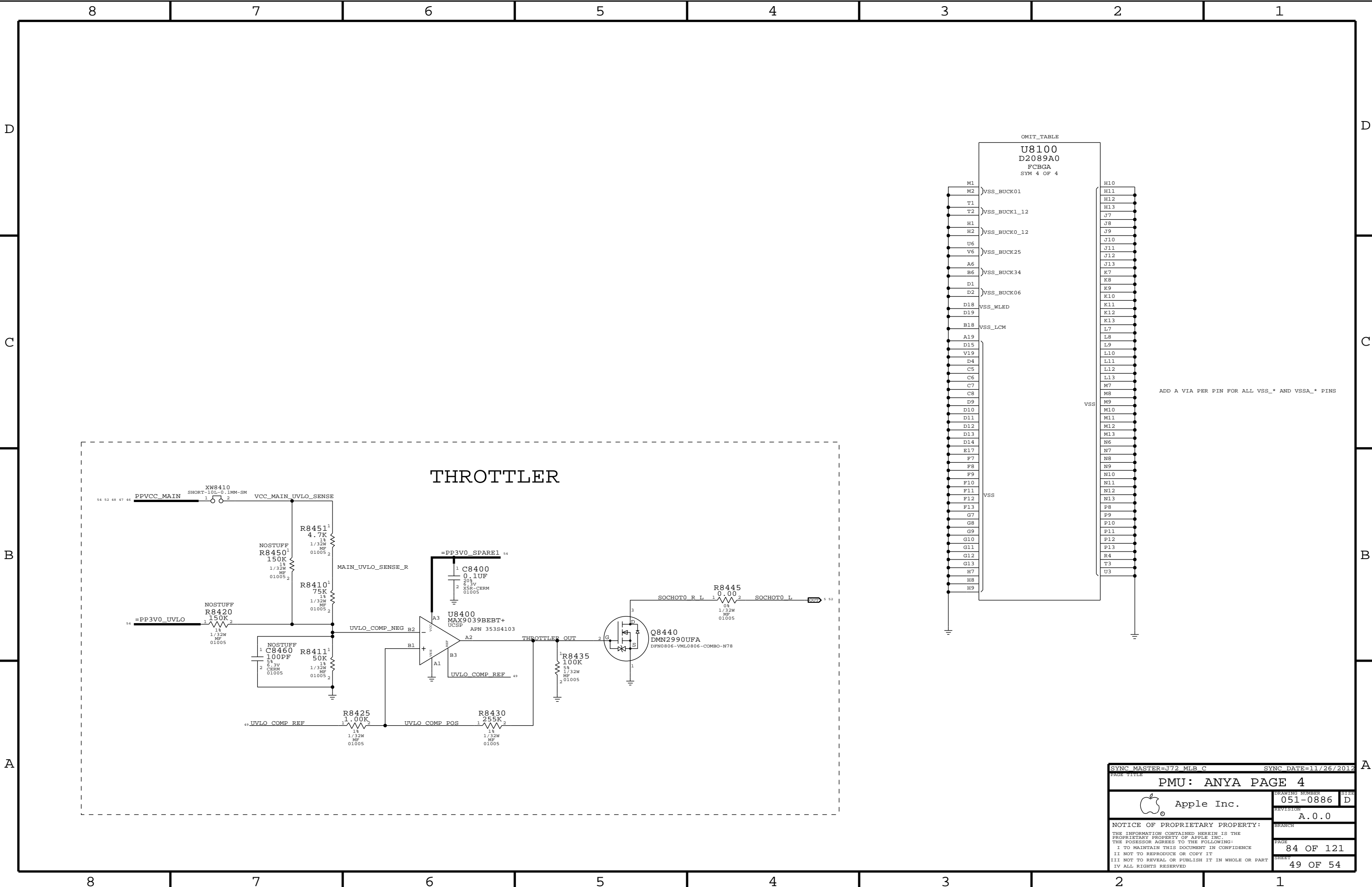
46 OF 54

C8137 0201 OKAY IF GRAPE HAS EXT FET

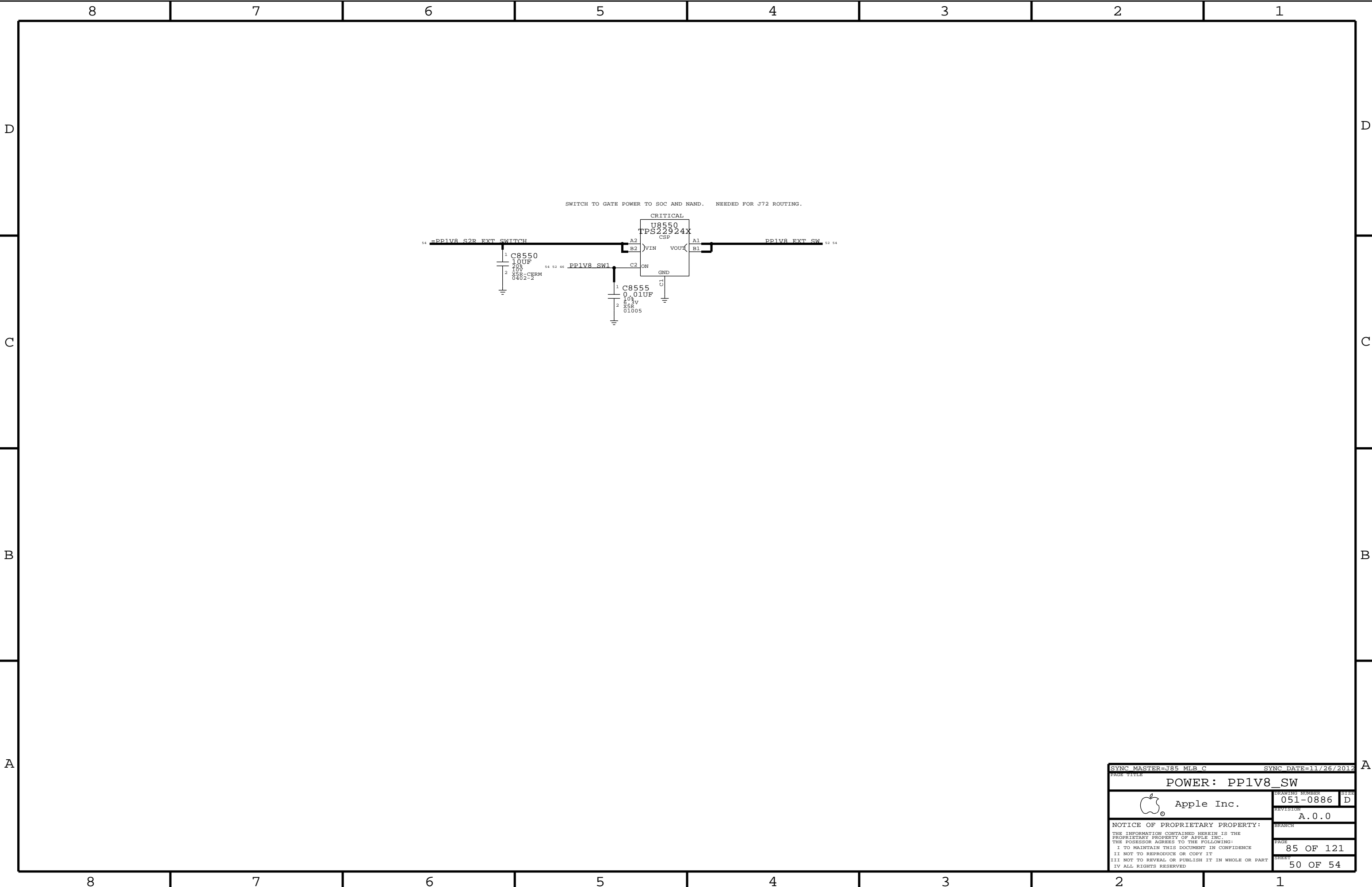


| PART NUMBER | ALTERNATE FOR PART NUMBER | BOM OPTION | REF DES | COMMENTS: |
|-------------|---------------------------|--------------|--------------|--------------------------|
| 107S0150 | 107S0208 | R8321, R8322 | R8323, R8324 | RDAR: // PROBLEM/8380167 |
| 118S0764 | 118S0717 | | R8340 | RDAR: // PROBLEM/8380167 |

| | | | |
|---|--|----------------------|--|
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| | | 48 OF 54 | |



ADD A VIA PER PIN FOR ALL VSS_* AND VSSA_* PINS



8

7

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D

C

C

B

B

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A

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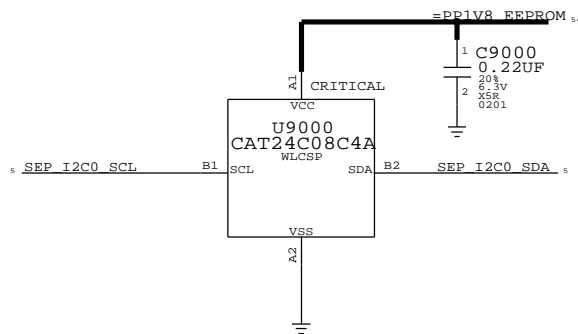
1


DEBUG

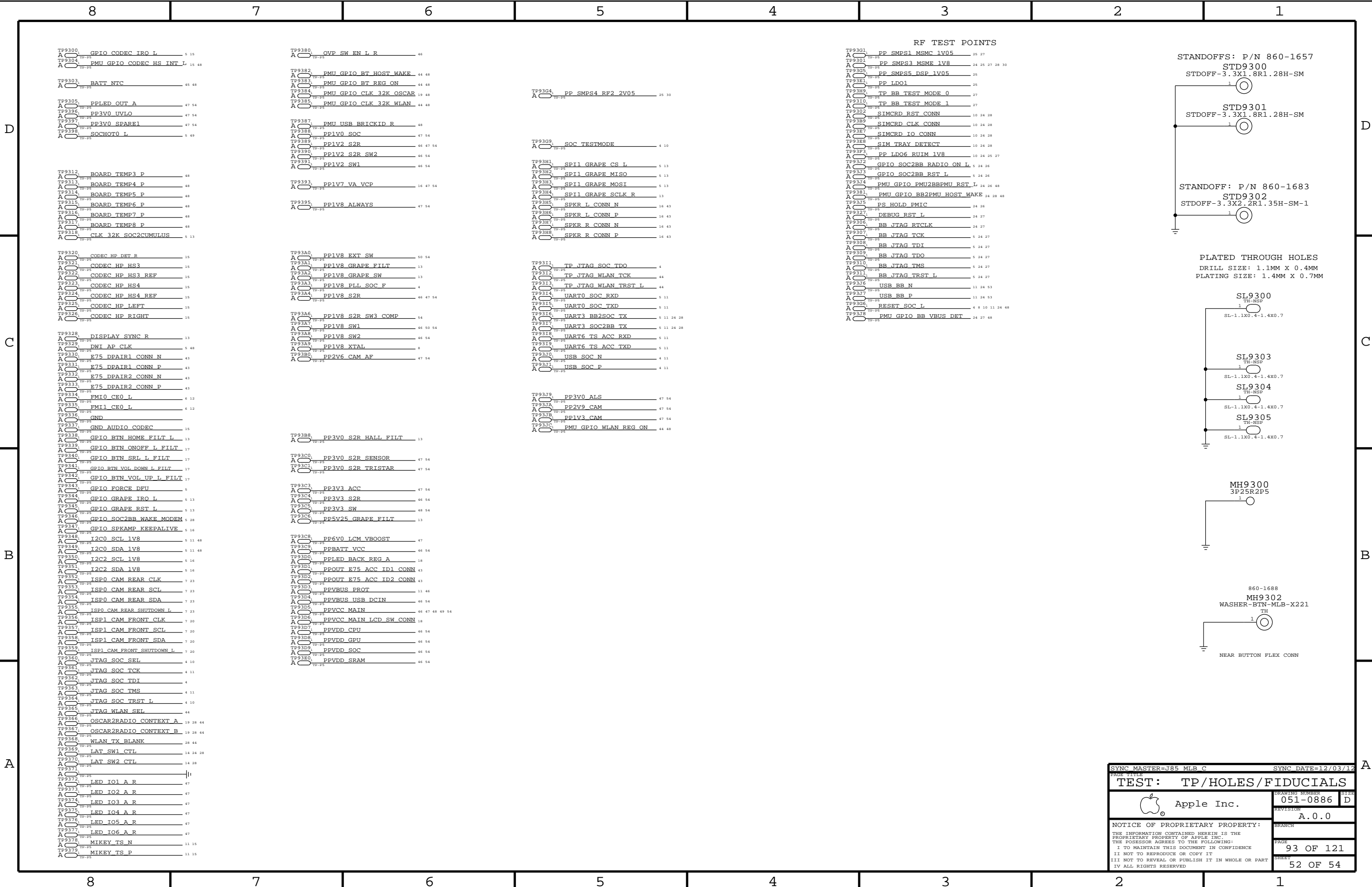


SEP EEPROM

UNPROGRAMMED P/N: 335S0894



| | | | |
|---|--|----------------------------|--|
| SYNCH MASTER=J72 MLB C | | SYNCH DATE=11/26/2012 | |
| PAGE TITLE | | PAGE NO | |
| SEP: EEPROM & SOC DEBUG | | | |
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| | | SHEET 51 OF 54 | |



| | | | | | | | |
|---|---|---|---|---|---|---|---|
| 8 | 7 | 6 | 5 | 4 | 3 | 2 | 1 |
| EE CHARACTERIZATION TP | | | | | | | |
| NAND | | | | | | | |
| PP9401 ¹ FMI0_DQS PLACE_SIDE=BOTTOM PLACE_NEAR=U0652.D34:2MM 4 12 53 | | | | | | | |
| PP9402 ¹ FMI0_AD<3> PLACE_SIDE=BOTTOM PLACE_NEAR=U0652.C34:2MM 6 12 | | | | | | | |
| PP9403 ¹ FMI0_DQS PLACE_NEAR=U0652.D34:2MM 4 12 53 | | | | | | | |
| PP9410 ¹ TP_TCKC_U1400 12 | | | | | | | |
| PP9411 ¹ TP_TM5C_U1400 12 | | | | | | | |
| PP9412 ¹ TP_U1400_RB0 12 | | | | | | | |
| PP9413 ¹ TP_U1400_RB1 12 | | | | | | | |
| DWI | | | | | | | |
| PP9405 ¹ DWI_AP_DO PLACE_SIDE=BOTTOM PLACE_NEAR=U8100:2MM 5 48 | | | | | | | |
| AUDIO | | | | | | | |
| PP9406 ¹ I2S0_CODEC_ASP_MCK PLACE_NEAR=U1900:2MM 5 15 | | | | | | | |
| PP9416 ¹ I2S2_CODEC_XSP_LRCK PLACE_NEAR=U1900:2MM 5 15 | | | | | | | |
| PP9417 ¹ I2S2_CODEC_XSP_DOUT PLACE_NEAR=U1900:2MM 5 15 | | | | | | | |
| PP9419 ¹ I2S1_SPKAMP_MCK PLACE_NEAR=U2040:2MM 5 16 | | | | | | | |
| PP9420 ¹ I2S1_SPKAMP_BCLK PLACE_NEAR=U2040:2MM 5 16 | | | | | | | |
| PP9421 ¹ I2S1_SPKAMP_LRCK PLACE_NEAR=U2040:2MM 5 16 | | | | | | | |
| PP9422 ¹ I2S1_SPKAMP_DOUT PLACE_NEAR=U2040:2MM 5 16 | | | | | | | |
| PP9423 ¹ I2S1_SPKAMP_DIN PLACE_SIDE=BOTTOM PLACE_NEAR=U0652:2MM 5 16 | | | | | | | |
| PP9424 ¹ SPI2_CODEC_SCLK PLACE_NEAR=U1900:2MM 5 15 | | | | | | | |
| PP9425 ¹ SPI2_CODEC_MOSI PLACE_NEAR=U1900:2MM 5 15 | | | | | | | |
| PP9426 ¹ SPI2_CODEC_MISO PLACE_SIDE=BOTTOM PLACE_NEAR=U0652:2MM 5 15 | | | | | | | |
| GRAPE | | | | | | | |
| OSCAR | | | | | | | |
| PP9428 ¹ UART4_SOC2OSCAR_TXD PLACE_SIDE=BOTTOM PLACE_NEAR=U2400:2MM 5 19 | | | | | | | |
| PP9429 ¹ UART4_OSCAR2SOC_RXD PLACE_SIDE=BOTTOM PLACE_NEAR=U0652:2MM 5 19 | | | | | | | |
| UART5 | | | | | | | |
| CAMERA | | | | | | | |
| PP9440 ¹ MIPI1C_CAM_FRONT_CLK_P PLACE_NEAR=U0652.AR33:3MM 7 20 53 | | | | | | | |
| PP9441 ¹ MIPI1C_CAM_FRONT_CLK_N PLACE_NEAR=U0652.AR34:3MM 7 20 53 | | | | | | | |
| PP9442 ¹ MIPI1C_CAM_FRONT_DATA_P<0> PLACE_NEAR=U0652.AT33:3MM 7 20 53 | | | | | | | |
| PP9443 ¹ MIPI1C_CAM_FRONT_DATA_N<0> PLACE_NEAR=U0652.AT34:3MM 7 20 53 | | | | | | | |
| PP9444 ¹ MIPI0C_CAM_REAR_CLK_P PLACE_NEAR=U0652.AU25:3MM 7 23 53 | | | | | | | |
| PP9445 ¹ MIPI0C_CAM_REAR_CLK_N PLACE_NEAR=U0652.AV25:3MM 7 23 53 | | | | | | | |
| PP9446 ¹ MIPI0C_CAM_REAR_DATA_P<0> PLACE_NEAR=U0652.AU27:3MM 7 23 53 | | | | | | | |
| PP9447 ¹ MIPI0C_CAM_REAR_DATA_N<0> PLACE_NEAR=U0652.AV27:3MM 7 23 53 | | | | | | | |
| FOR FRANK (SEG) | | | | | | | |
| PP9450 ¹ PPVDD_SOC_SOC_SENSE PLACE_NEAR=U0652.V31:1MM 9 | | | | | | | |
| PP9451 ¹ PPVDD_CPU_SOC_SENSE PLACE_NEAR=U0652.AN30:1MM 9 | | | | | | | |
| WIFI | | | | | | | |
| PP9480 ¹ HSIC1_WLAN2SOC_DEVICE_RDY 5 44 | | | | | | | |
| PP9481 ¹ HSIC1_WLAN2SOC_REMOTE_WAKE 5 44 | | | | | | | |
| PP9482 ¹ PMU_GPIO_WLAN_HOST_WAKE 44 48 | | | | | | | |
| PP9483 ¹ HSIC1_SOC2WLAN_HOST_RDY 5 44 | | | | | | | |
| GPIO_BT_WAKE FUNC_TEST=TRUE 5 44 | | | | | | | |
| FOR HSIC CHARACTERIZATION | | | | | | | |
| PP9460 ¹ HSIC1_WLAN_DATA PLACE_NEAR=U0652.AN33:3MM 4 44 53 | | | | | | | |
| PP9462 ¹ HSIC1_WLAN_DATA PLACE_NEAR=U5800.13:3MM 4 44 53 | | | | | | | |
| PP9463 ¹ HSIC1_WLAN_STB PLACE_NEAR=U5800.14:3MM 4 44 | | | | | | | |
| PP9468 ¹ UART1_BT2SOC_TX PLACE_NEAR=U0652:3MM 5 44 | | | | | | | |
| PP9469 ¹ UART1_SOC2BT_TX PLACE_NEAR=U5800:3MM 5 44 | | | | | | | |
| PP9471 ¹ UART2_WLAN2SOC_TX PLACE_NEAR=U0652:3MM 5 44 | | | | | | | |
| PP9472 ¹ UART2_SOC2WLAN_TX PLACE_NEAR=U5800:3MM 5 44 | | | | | | | |
| BASEBAND | | | | | | | |
| USB_BB_P FUNC_TEST=TRUE 11 24 52 | | | | | | | |
| USB_BB_N FUNC_TEST=TRUE 11 24 52 | | | | | | | |
| FOR HSIC CHARACTERIZATION | | | | | | | |
| PP9465 ¹ HSIC2_BB_STB PLACE_NEAR=U0652.B27:3MM 4 24 27 | | | | | | | |
| PP9466 ¹ HSIC2_BB_DATA PLACE_NEAR=U3400.C7:3MM 4 24 27 | | | | | | | |
| PROX | | | | | | | |
| HIGH SPEED, NO TEST | | | | | | | |
| MIPI0C_CAM_REAR_CLK_P NO_TEST=TRUE 7 23 53 | | | | | | | |
| MIPI0C_CAM_REAR_CLK_N NO_TEST=TRUE 7 23 53 | | | | | | | |
| MIPI0C_CAM_REAR_DATA_P<0..3> NO_TEST=TRUE 7 23 53 | | | | | | | |
| MIPI0C_CAM_REAR_DATA_N<0..3> NO_TEST=TRUE 7 23 53 | | | | | | | |
| MIPI0C_CAM_REAR_CLK_FILT_P NO_TEST=TRUE 23 | | | | | | | |
| MIPI0C_CAM_REAR_CLK_FILT_N NO_TEST=TRUE 23 | | | | | | | |
| MIPI0C_CAM_REAR_DATA_FILT_P<0..3> NO_TEST=TRUE 23 | | | | | | | |
| MIPI0C_CAM_REAR_DATA_FILT_N<0..3> NO_TEST=TRUE 23 | | | | | | | |
| MIPI1C_CAM_FRONT_CLK_P NO_TEST=TRUE 7 20 53 | | | | | | | |
| MIPI1C_CAM_FRONT_CLK_N NO_TEST=TRUE 7 20 53 | | | | | | | |
| MIPI1C_CAM_FRONT_DATA_P<0> NO_TEST=TRUE 7 20 53 | | | | | | | |
| MIPI1C_CAM_FRONT_DATA_N<0> NO_TEST=TRUE 7 20 53 | | | | | | | |
| MIPI1C_CAM_FRONT_CLK_FILT_P NO_TEST=TRUE 20 | | | | | | | |
| MIPI1C_CAM_FRONT_CLK_FILT_N NO_TEST=TRUE 20 | | | | | | | |
| MIPI1C_CAM_FRONT_DATA_FILT_P<0> NO_TEST=TRUE 20 | | | | | | | |
| MIPI1C_CAM_FRONT_DATA_FILT_N<0> NO_TEST=TRUE 20 | | | | | | | |
| EDP_DATA_P<0..3> NO_TEST=TRUE 7 18 | | | | | | | |
| EDP_DATA_EMI_P<0..3> NO_TEST=TRUE 7 18 | | | | | | | |
| EDP_DATA_EMI_N<0..3> NO_TEST=TRUE 18 | | | | | | | |
| EDP_DATA_EMI_CONN_P<0..3> NO_TEST=TRUE 18 | | | | | | | |
| EDP_DATA_EMI_CONN_N<0..3> NO_TEST=TRUE 18 | | | | | | | |
| PAGE TITLE | | | | | | | |
| TEST: EE TP/PP | | | | | | | |
| DRAWING NUMBER 051-0886 SIZE D | | | | | | | |
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| PAGE 94 OF 121 | | | | | | | |
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