

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

Mon Feb 18 18:52:09 2013

REV	ECN	DESCRIPTION OF REVISION	CK APPD DATE
8	000183/61	ENGINEERING RELEASED	2013-02-18

X145 SINGLE_BRD E1C

PDF PAGE	CSA PAGE	CONTENTS	SYNC MASTER	DATE
2	2	H6P JTAG,USB,PLL,HSIC,XTAL	N/A	N/A
3	3	H6P DIGITAL I/O,BOOTSTRAPPING	N/A	N/A
4	4	H6P VDDCA,VDD1/2,VDD,VDD_CPU,VDD_GPU	N/A	N/A
5	5	H6P GND,VDDIO18,VDDIOD,VDD_SRAM,VDD_SOC	N/A	N/A
6	6	H6P NAND,NAND 12X17	N/A	N/A
7	7	H6P HIGH SPEED DIG (CAM,LCM,DP)	N/A	N/A
8	8	BUTTON FLEX B2B	N/A	N/A
9	9	L67 AUDIO CODEC (1/2)	N/A	N/A
10	10	L67 AUDIO CODEC (2/2)	N/A	N/A
11	11	FRONT CAM FLEX B2B	N/A	N/A
12	12	AMBER PMU(1/2)	N/A	N/A
13	13	AMBER PMU(2/2)	N/A	N/A
14	14	CHESTNUT,BACKLIGHT DRIVER,MESA BOOST	N/A	N/A
15	15	SPKR AMP + STROBE DRIVER	N/A	N/A
16	16	TRISTAR,EEPROM	N/A	N/A
17	17	DOCKFLEX B2B	N/A	N/A
18	18	D403 (TOUCH B2B, DRIVER ICS)	N/A	N/A
19	19	LCM B2B	N/A	N/A
20	20	OSCAR + SENSORS	N/A	N/A
21	21	REAR CAM B2B	N/A	N/A
22	22	BATT B2B, TPS, PD FEATURES	N/A	N/A
23	23	VOLTAGE NETS		
24	24	RADIO_MLB HIERARCH. SYMBOL	N/A	N/A
25	25	Cross Reference Page		

INDUCTOR BOM OPTIONS

UPDATED FOR EVT2 RDAR://12984453

	PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
BUCK0 SLAVE	152S1834	3	BUCK0 SLAVE IND: 0.47UH, CYNTEC	L10,L12,L14	CRITICAL	IND_BUCK0_SLAVE_P47UH_CYNTEC
	152S1839	3	BUCK0 SLAVE IND: 0.47UH, TAIYO	L10,L12,L14	CRITICAL	IND_BUCK0_SLAVE_P47UH_TAIYO
BUCKXX MASTER	152S1801	7	AMBER BUCKXX IND: 1UH CYNTEC	L9,L11,L13,L15,L16,L17,L18	CRITICAL	IND_BUCKXX_1UH_CYNTEC
	152S1840	7	AMBER BUCKXX IND: 1UH TAIYO	L9,L11,L13,L15,L16,L17,L18	CRITICAL	IND_BUCKXX_1UH_TAIYO
STROBE	152S1801	1	STROBE IND: 1UH CYNTEC	L6	CRITICAL	IND_STROBE_1UH_CYNTEC
	152S1840	1	STROBE IND: 1UH TAIYO	L6	CRITICAL	IND_STROBE_1UH_TAIYO
SPKR AMP	152S1836	1	SPKR AMP IND: 1.2UH CYNTEC	L4	CRITICAL	IND_SPKRAMP_1P2UH_CYNTEC
	152S1844	1	SPKR AMP IND: 1.2UH TAIYO	L4	CRITICAL	IND_SPKRAMP_1P2UH_TAIYO
CHARGER	152S1721	1	CHARGER IND: 2.2UH TAIYO	L8	CRITICAL	IND_CHGR_2P2UH_TAIYO
	152S1850	1	CHARGER IND: 2.2UH MURATA	L8	CRITICAL	IND_CHGR_2P2UH_MURATA
CHESTNUT	152S1842	1	TI CHESTNUT: 1.5UH TAIYO	L19	CRITICAL	IND_CHESTNUT_1P5UH_TAIYO
	152S1802	1	TI CHESTNUT: 1.5UH CYNTEC	L19	CRITICAL	IND_CHESTNUT_1P5UH_CYNTEC
	152S1849	1	TI CHESTNUT: 1.5UH MURATA	L19	CRITICAL	IND_CHESTNUT_1P5UH_MURATA

AUDIO BOM OPTION

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
155S0556	2	FERRITE 0402 140HM L9	FL6, FL9	CRITICAL	SPKAMP_FERRITE_REG
155S0731	2	FERRITE 0402 140HM 1P8A	FL6, FL9	CRITICAL	SPKAMP_FERRITE_LOWDCR
132S0396	2	SPKAMP 0.015 150PF	C500, C501	CRITICAL	SPKAMP_CAPFILT_1000PF
132S0437	2	CAP 0.015 150PF	C500, C501	CRITICAL	SPKAMP_CAPFILT_150PF
131S0283	2	CAP 0.015 100PF	DZ13, DZ14	CRITICAL	SPKAMP_ESDFILT_100PF
337S0166	2	VAR 0.005 2V 33PF	DZ13, DZ14	CRITICAL	SPKAMP_ESDFILT_VARS
155S0453	2	120OHM FERRITE BEAD	FL1, FL10	CRITICAL	HS3_HS4_120OHM_BEADS
155S0755	2	240OHM FERRITE BEAD	FL1, FL10	CRITICAL	HS3_HS4_240OHM_BEADS

NAVAJO SPI BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
117S0161	4	00HMS SERIES R ON NAVAJO SPI	FL32,FL63,FL50,FL59	CRITICAL	NAVAJO_SERIES_00HM
155S0453	4	120OHM FERRITES ON NAVAJO SPI	FL32,FL63,FL50,FL59	CRITICAL	NAVAJO_SERIES_FERRITE

OSCAR BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
337S4416	1	OSCAR A1 CSP	U9	CRITICAL	OSCAR_CSP
337S4417	1	OSCAR A1 FCLGA	U9	CRITICAL	OSCAR_FCLGA

MISC BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
118S0723	2	DDR_RREF 2400HM	R73,R72	CRITICAL	DDR_RREF_240
118S0684	2	DDR_RREF 2430HM	R73,R72	CRITICAL	DDR_RREF_243

X145 BOM CALLOUTS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-9478	1	SCH, SINGLE_BRD, X145	SCH	CRITICAL	?
820-3292	1	PCB, SINGLE_BRD, X145	PCB	CRITICAL	?
825-6838	1	LABEL FOR X145 639-4152	EEEE_F7GR	CRITICAL	EEEE_MM_16G
825-6838	1	LABEL FOR X145 639-4153	EEEE_F7GQ	CRITICAL	EEEE_MM_32G
825-6838	1	LABEL FOR X145 639-3465	EEEE_DYJP	CRITICAL	EEEE_MM_64G
825-6838	1	LABEL FOR X145 639-4668	EEEE_FGCC	CRITICAL	EEEE_SM_16G
825-6838	1	LABEL FOR X145 639-4667	EEEE_FGCD	CRITICAL	EEEE_SM_32G
825-6838	1	LABEL FOR X145 639-4669	EEEE_FGCF	CRITICAL	EEEE_SM_64G

SOC BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
339S0207	1	H6P + 1GB ELPIDA	U1	CRITICAL	

SOC BOM ALTERNATES

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
339S0208	339S0207		U1	H6P + 1GB HYNIX

NAND BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
335S0930	1	NAND,19NM,16GX8,MLC,PPN1.5,HYNIX	U4	CRITICAL	NAND_16G
335S0931	1	NAND,19NM,32GX8,MLC,PPN1.5,HYNIX	U4	CRITICAL	NAND_32G
335S0932	1	NAND,19NM,64GX8,MLC,PPN1.5,HYNIX	U4	CRITICAL	NAND_64G

NAND BOM ALTERNATES

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
335S0921	335S0930		U4	NAND_16GB_TOSHIBA
335S0933	335S0930		U4	NAND_16GB_SANDISK
335S0922	335S0931		U4	NAND_32GB_TOSHIBA
335S0934	335S0931		U4	NAND_32GB_SANDISK
335S0923	335S0932		U4	NAND_64GB_TOSHIBA
335S0935	335S0932		U4	NAND_64GB_SANDISK

RADIO_MLB ALTERNATES


PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
335S0895	335S0874		U6_RF	WINBOND NOR ALT
339S0204	339S0205		U8_RF	USI WIFI ALT
339S0209	339S0205		U8_RF	TDK WIFI ALT
197S0491	197S0470		Y1_RF	KYOCERA ALT
197S0482	197S0470		Y1_RF	EPSON ALT

I2C ADDRESS MAP

I2C0	DEVICE	BINARY	7-BIT HEX	8-BIT HEX
	AMBER PMU:	1110100X	0X74	0XE8
	CS35L19B AMP:	1000000X	0X40	0X80
	LM3534 BL DRIVER:	1100011X	0X63	0XC6
	TRISTAR:	0011010X	0X1A	0X34
	CHESTNUT:	0100111X	0X27	0X4E
I2C1	CT814 ALS:	0101001X	0X29	0X52
RCAM I2C	OPEL STROBE DRIVER:	1100011X	0X63	0XC6
	REAR FACING CAM:	0010000X	0X10	0X20
	ADI VCM AF DRIVER:	0001110X	0X0E	0X1C
	ROHM VCM AF DRIVER:	0001100X	0X0C	0X18
FCAM I2C	FRONT FACING CAM:	0110110X	0X36	0X6C
NOTE: ACCEL, GYRO, COMPASS ALL USING SPI (VIA OSCAR) FOR AP COMMUNICATION.				

FOR CHESTNUT BOMTABLE - SEE PG 14
FOR RADIO BOMTABLE - SEE PG 24
FOR MISC R/L/C - SEE PG 2

SCH 051-9478
BRD 820-3292
MCO 056-5179
BOM 639-4152 (16GB) X145
BOM 639-4153 (32GB) X145
BOM 639-3465 (64GB) X145

DRAWING TITLE			
SCH,SINGLE_BRD,X145			
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```
BOARD_REV[3:0]= {GPIO34, GPIO35, GPIO36, GPIO37}
FLOAT=LOW, PULLUP=HIGH
1111  PROTO2/2A,  TRISAR/L19
1110  EVT1 MAIN BUILD
1101  EVT1 MESA BUILD
1100  EVT1A MESA BUILD
1011  E1B MAIN BUILD
1010  E1C MAIN BUILD  <--- SELECTED (NOSTUFF R3000 & STUFF R3001)
```

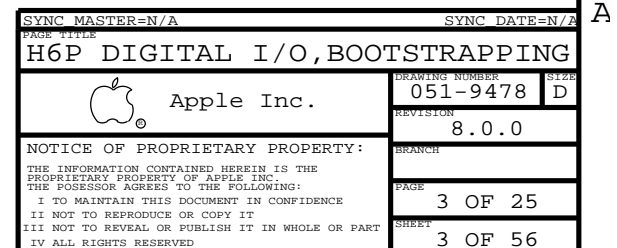
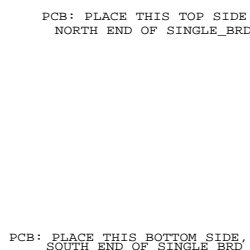
```
BOARD_ID[3:0]={GPIO16, SPI0_MISO, SPI0_MOSI, SPI0_SCLK}
FLOAT=LOW, PULLUP=HIGH
0000 X145 MLB <--- SELECTED
0001 X145 DEV
0010 X152 MLB
0011 X152 DEV
```

```

BOOT_CONFIG[3:0]={GPIO29_CONFIG3,GPIO28_CONFIG2,GPIO25_CONFIG1,GPIO18_CONFIG0}

FLOAT=LOW, PULLUP=HIGH
0000    SPI0
0001    SPI0 TEST MODE
0010    NAND
0011    NAND TEST MODE <---  SELECTED

```



8	7	6	5	4	3	2	1
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VDD_CPU, VDD_GPU

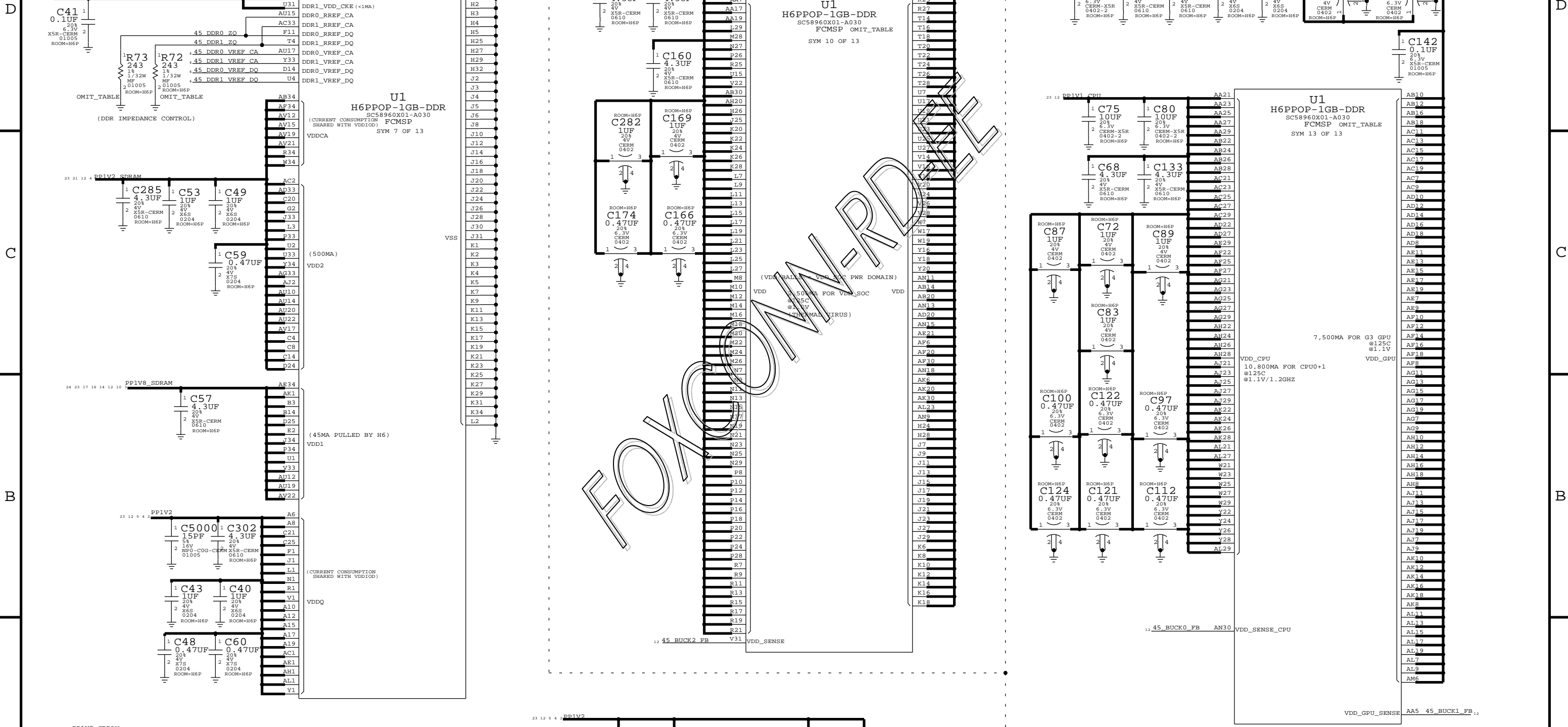


Figure 1 is a schematic diagram of the H6P VDDCA, VDD1/2, VDD, VDD_CPU, VDD_GPU power planes. The diagram shows four identical power plane sections connected in parallel. Each section includes a 0.01uF capacitor (C106, C111, C114, C118), a 10k resistor (R27, R29, R31, R33), a 0.01uF capacitor (C108, C113, C117, C120), a 10k resistor (R28, R30, R32, R34), and a 45 DDR0 VREF CA or 45 DDR1 VREF DQ connection. The components are connected to a common ground plane.

H6P (GND, VDDIO18, VDDIOD, VDD_SRAM, VDD_SOC)

JUST A FEW GNDS

VDDIOD, VDDIO18

VDD_SRAM, VDD_SOC

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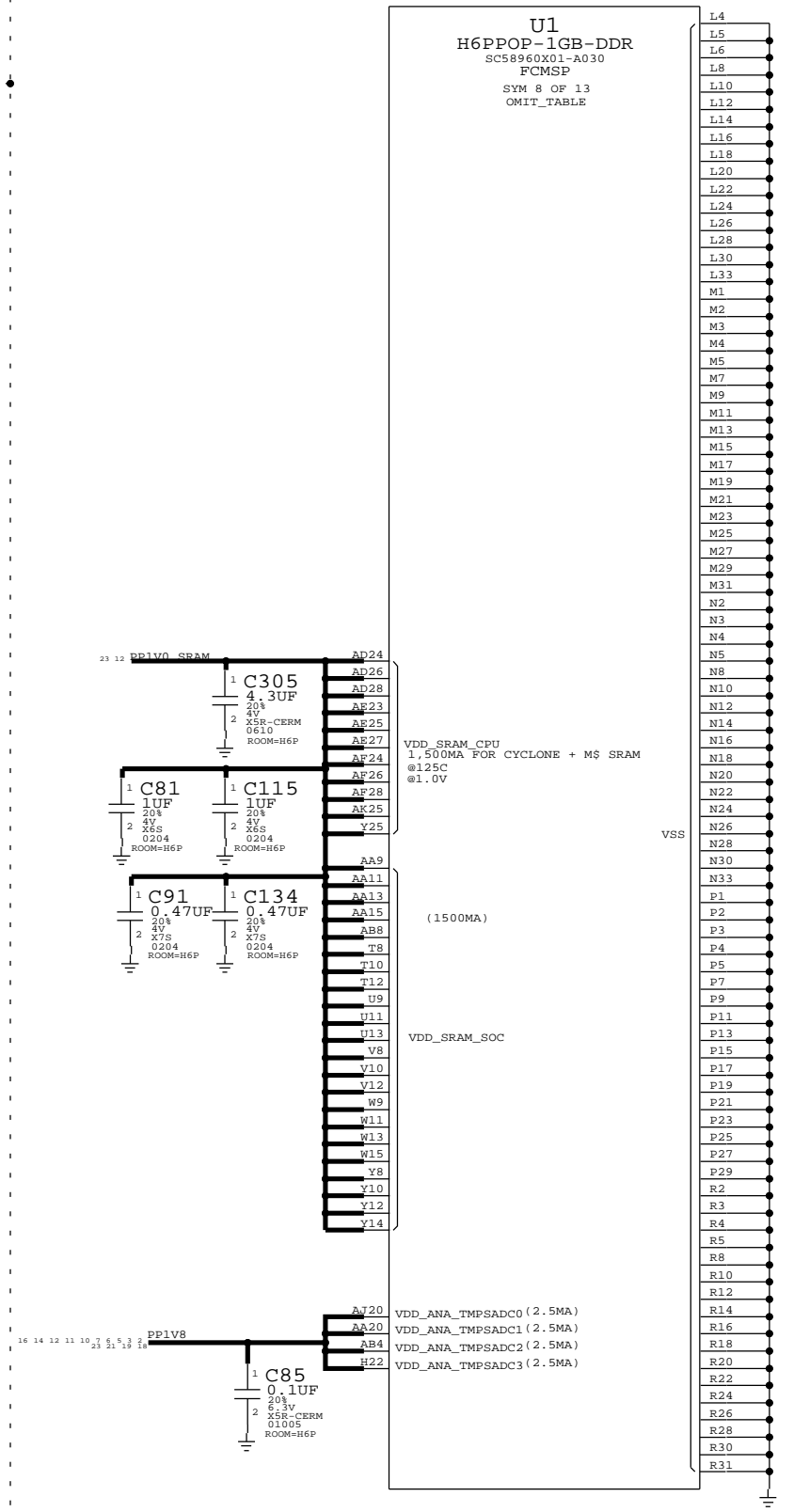
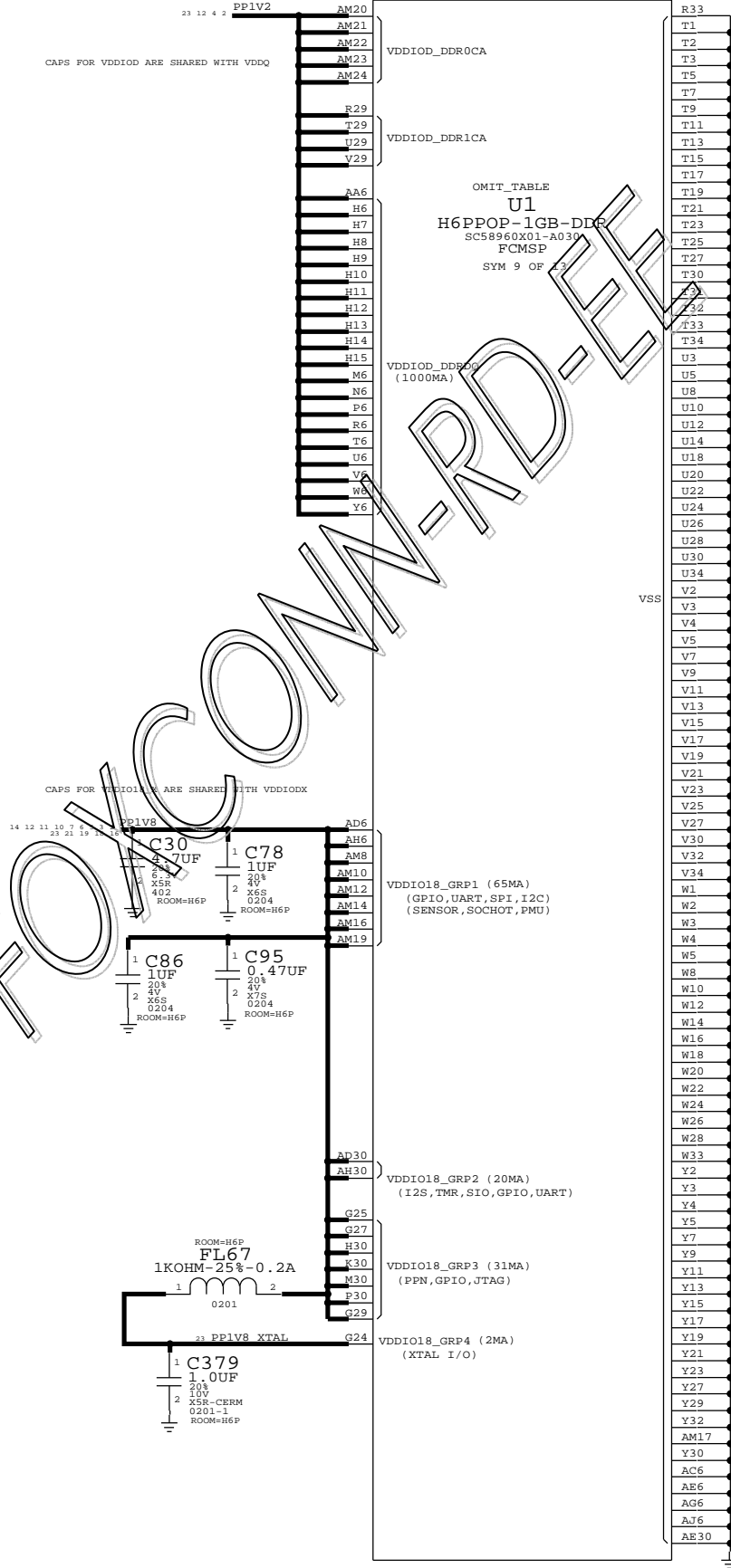
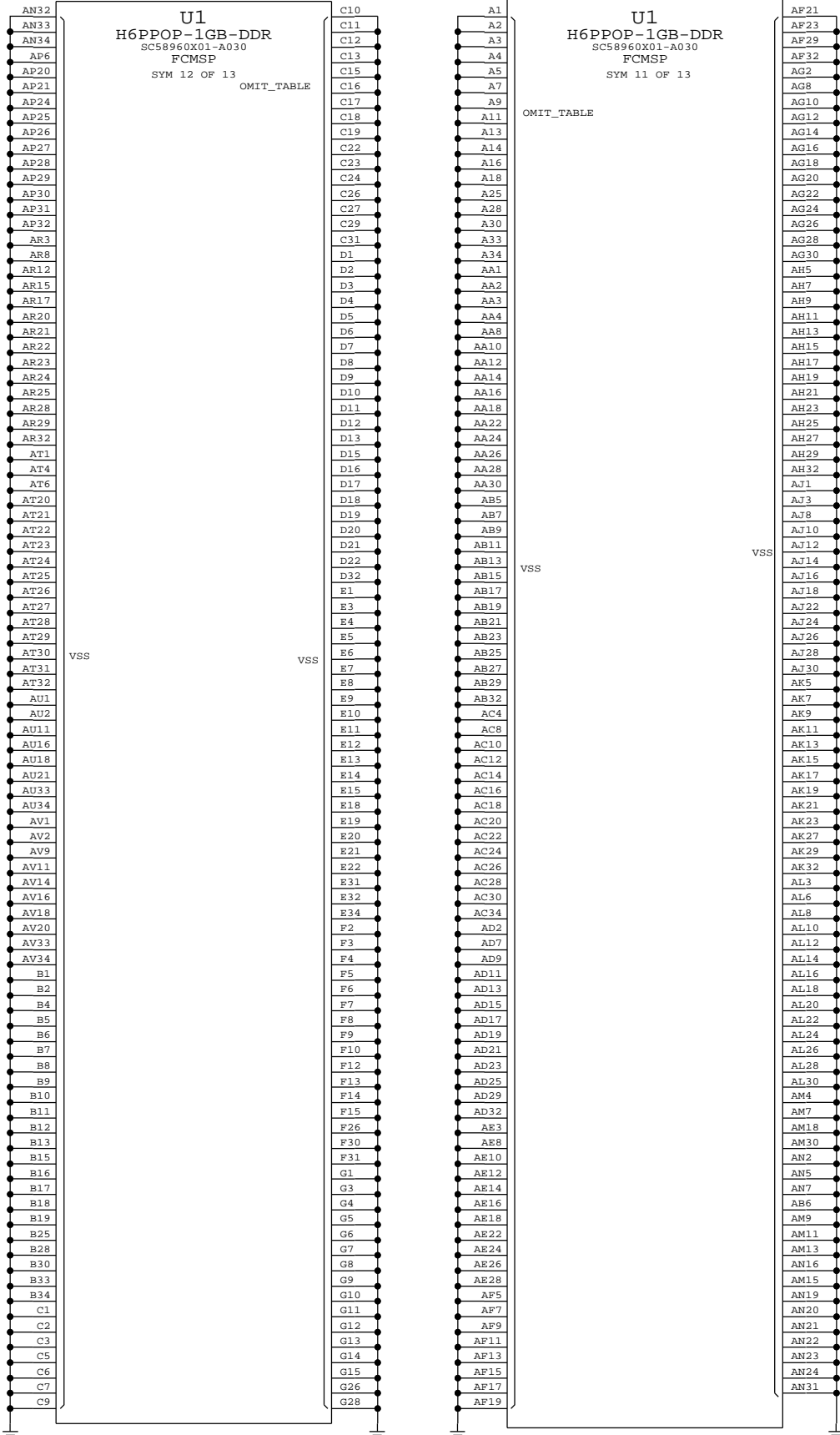
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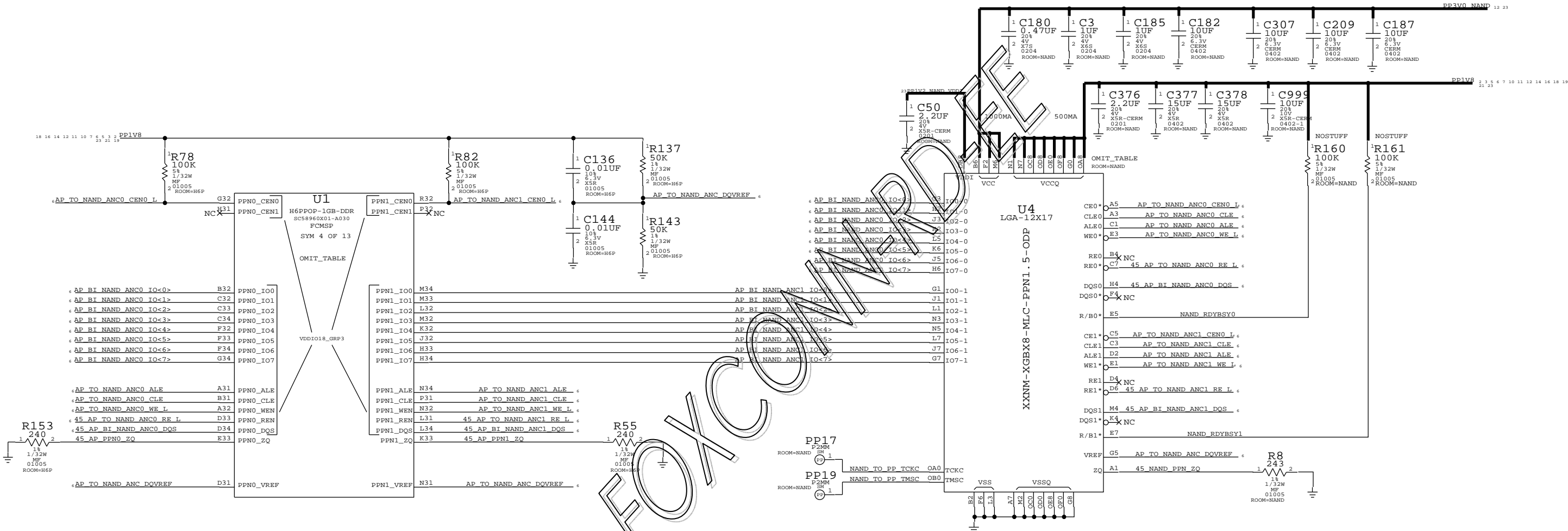
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H6P GND, VDDIO18, VDDIOD, VDD_SRAM, VDD_SOC		DRAWING NUMBER	
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H6P NAND + 12X17 NAND PKG

SUPPORT FOR PPN1.5 (1.8V IO) ONLY



NOTE: IO<6> PREFERRED BY MATT BYOM
(IS A STATUS READY BIT)

PP2
P2MM
ROOM=H6P
1 AP BI NAND ANCO IO<6>

PP3
P2MM
ROOM=H6P
1 45 AP TO NAND ANCO RE L

PP10
P2MM
ROOM=H6P
1 45 AP BI NAND ANCO DQS

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8 7 6 5 4 3 2 1

BUTTON FLEX (VIBE DRIVER, BUTTONS, ANC REF MIC, STROBE, STROBE_NTC)

STROBE:
LED WARM, RETURN


VIBE DRIVE

BUTTONS:
RINGER, HOLD,
VOL_UP/DOWN

STROBE:
STROBE NTC

MIC2 (ANC REF MIC):
MIC2/3 BIAS,
MIC2_P,_N

STROBE:
LED COOL

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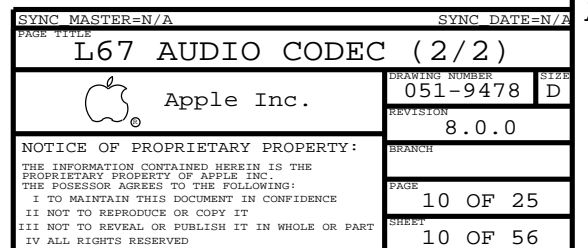
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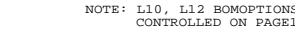


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VPUMP CAP:
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AMBER PMU

(AMUX, GPIO, BUTTONS, ADC, THERMISTORS, SYSTEM I/F, GND)

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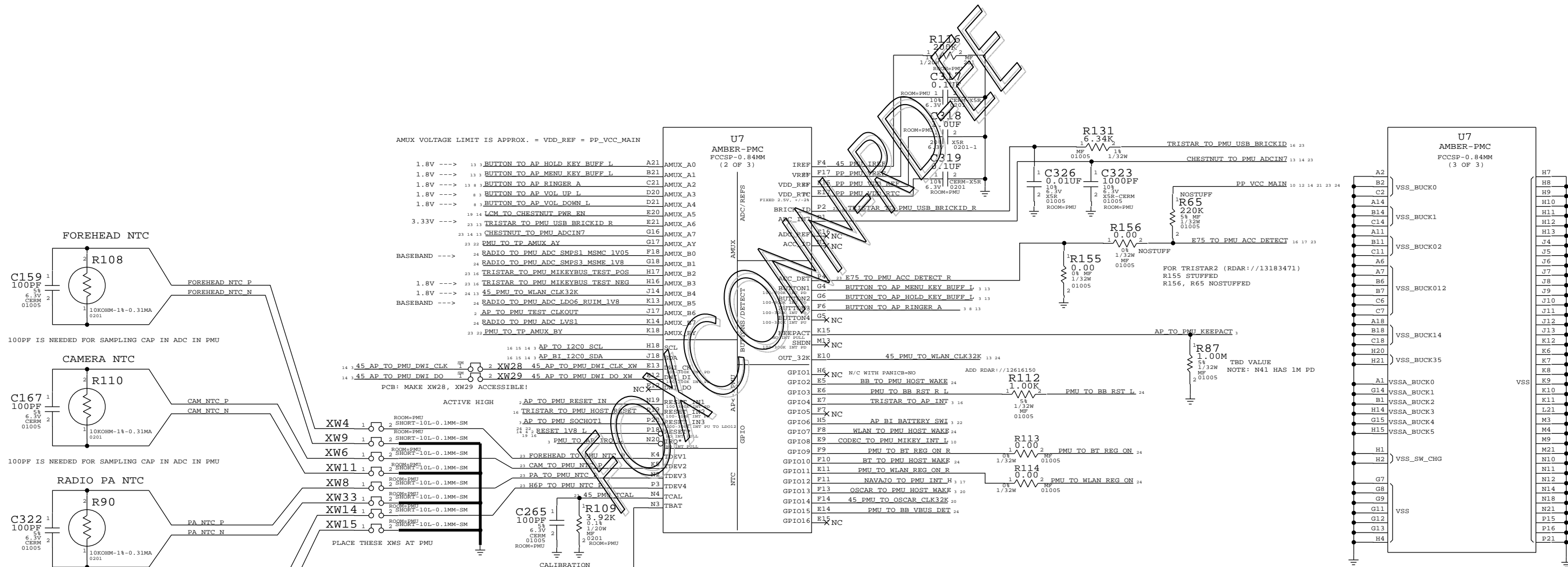
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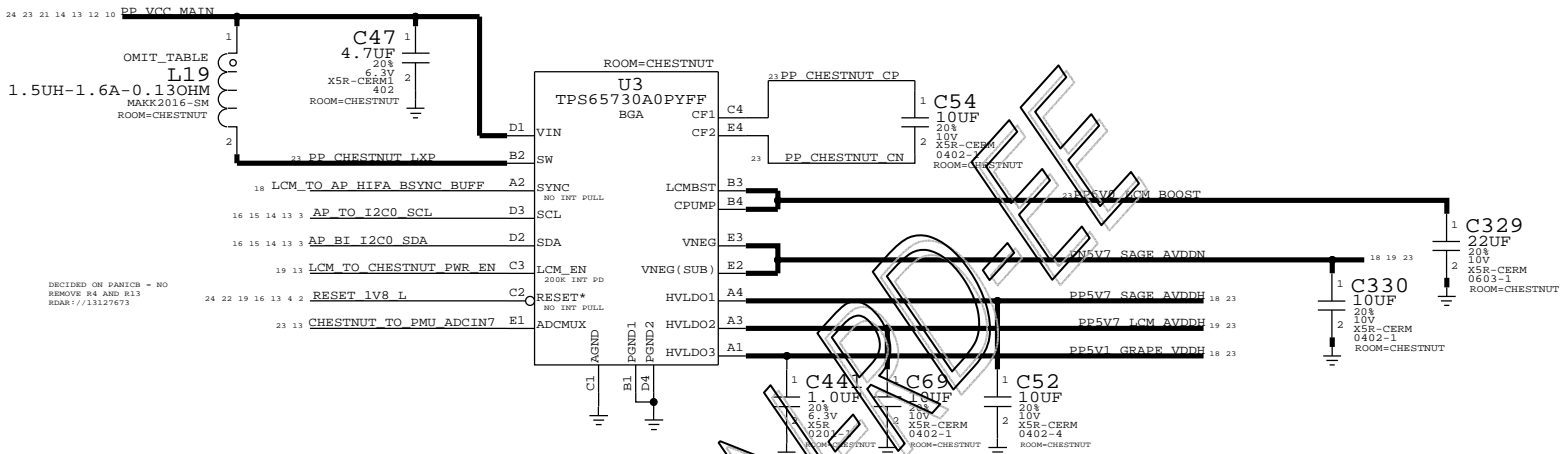
AMBER OTP AI (EVT1)
CHANGES/USEFUL INFO HIGHLIGHTED BELOW

GPIOS, BUTTONS, RESETS	BUCKS, LDOS, TIMING
GPI01 BUCK3_SW1 OUTPUT, DRIVE LOW, OPEN DRAIN	BUCK3_SW3 AND BUCK4_SW2 DEFAULT OFF
GPI02 BUCK3 INPUT WITH PULLDOWN	BUCK5 DEFAULT ON IN ACTIVE.
GPI03 BUCK3 OUTPUT LOW	BUCK2 DEFAULT 1.0V.
GPI04 BUCK3 INPUT WITH PULLDOWN	LD04 DEFAULT OFF IN ACTIVE, OFF IN HIB
GPI05 BUCK3 OUTPUT LOW	LD09 DEFAULT 2.80V.
GPI06 BUCK3 INPUT WITH PULLS DISABLED (EXT PU)	LD03_OFF_SLOT = 7 (MOVE LD03 OFF-SLOT TO -8)
GPI07 BUCK3 INPUT WITH PULLDOWN	LD013_OFF_SLOT = 2 (CHANGE LD013 OFF-SLOT TO -3)
GPI08 BUCK3 INPUT WITH PULLUP	CHANGE OFF-SLOT TIMERS FROM 2MS TO 3MS (T3/T4)
GPI09 BUCK3 OUTPUT LOW	SLOT_TIMINGS T3 = 2 AND T4 = 2 (3MS FOR BOTH)
GPI010 BUCK3 INPUT WITH PULLDOWN	MOVE BUCK5 ON-SLOT TO SLOT 3 AND OFF-SLOT TO SLOT -4.
GPI011 BUCK3 OUTPUT LOW+H12	
GPI012 BUCK3 INPUT WITH PULLUP	
GPI013 BUCK3 INPUT WITH PULLDOWN	
GPI014 BUCK3 OUTPUT LOW	
GPI015 VDD_MAIN OUTPUT LOW	
GPI016 BUCK3_SW1 INPUT WITH PULLDOWN	
BUTTON2 WAKE FROM HIB AND STBY.	
BUTTON4 PULLUP TO BUCK3	
RESET_IN1 ENABLED	
RESET_IN3 ENABLED, ACTIVE LOW, PU ENABLED TO LD012	
NOTE: HIB STATE IS NOT ENABLED FOR ANY GPIOS (OR TEMP_IRQ FOR GPI09).	
NO INPUTS ARE SELECTED AS WAKEUP EVENTS	
ALL PULLED UP INPUTS ARE SELECTED AS PULLED EDGE INPUTS,	
ALL PULLED DOWN INPUTS ARE SELECTED AS RISING EDGE INPUTS	

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AMBER PMU (2/2)			
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CHESTNUT, BACKLIGHT DRIVER, MESA BOOST

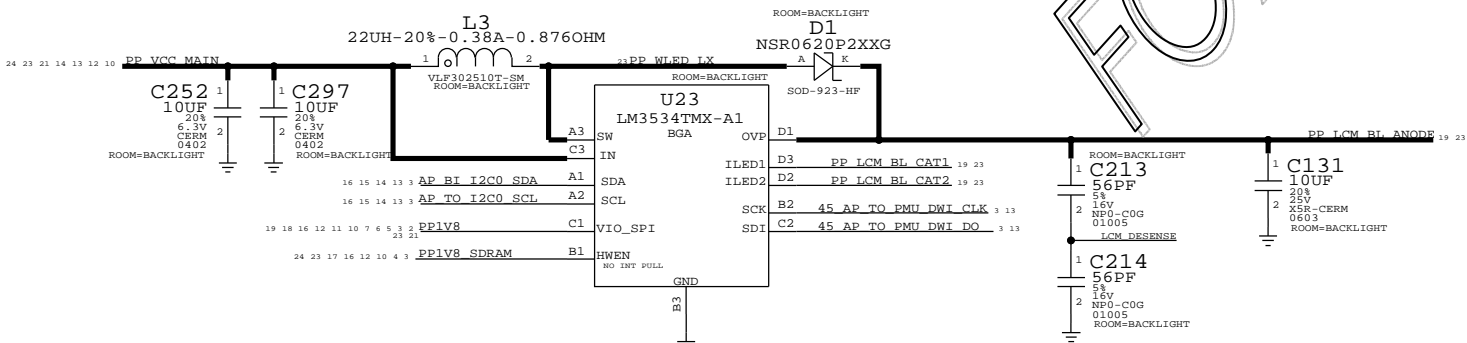
D403 DISPLAY PMU
(TI CHESTNUT, 338S1172)



SAGE NEG BOOST TIMING INFO:
1 MS NOMIAL START UP DELAY FOR LCM POWER SEQUENCING
1 MS DELAY AT SHUTDOWN
ACTIVE DISCHARGE 2MS TO RAIL DOWN

D403 BACKLIGHT DRIVER

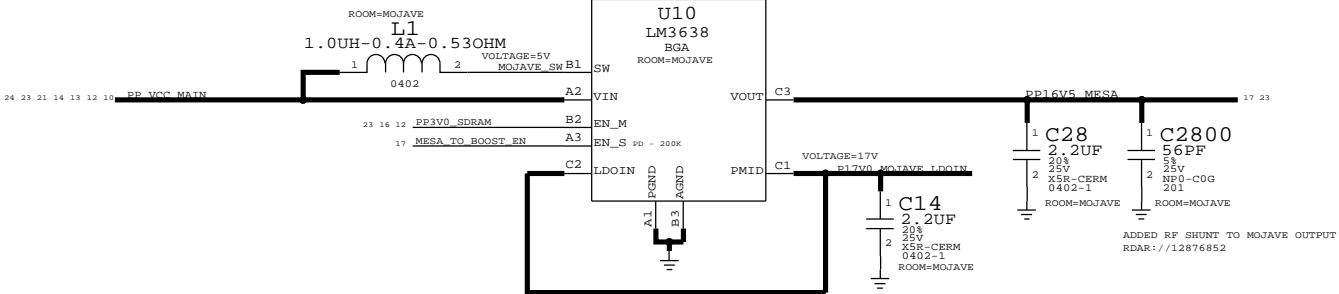
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NOTE: STACKED TO MEET VOLTAGE REQ, LOOK INTO 18+V CAPS

MOJAVE

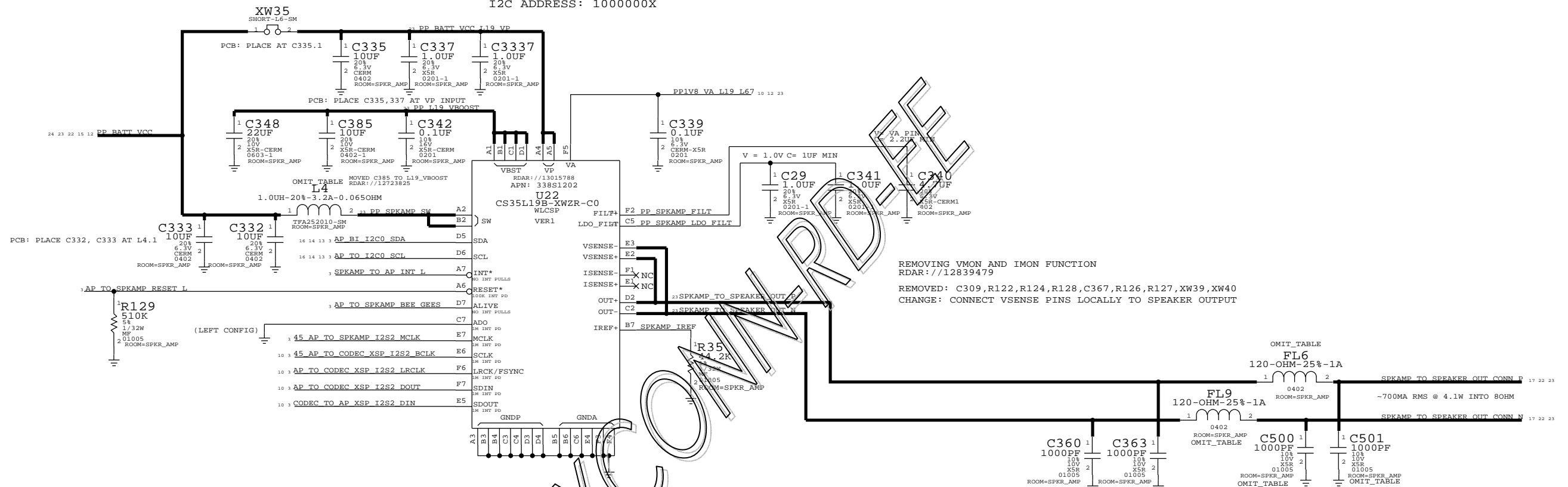
APN: 353S3978
VENDOR: TI



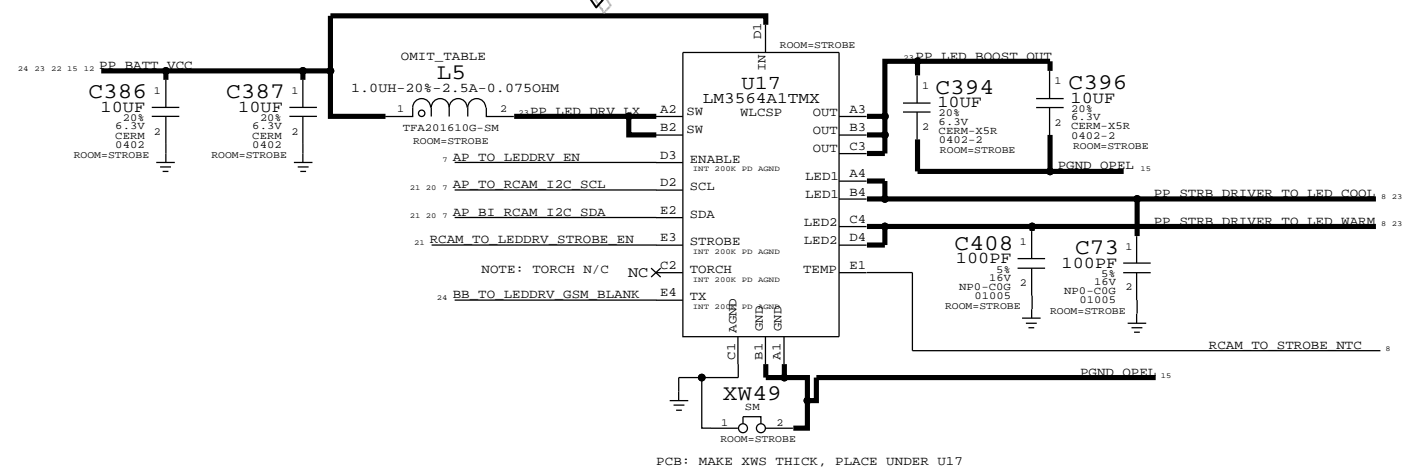
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L19 SPEAKER AMP


I2C ADDRESS: 1000000X



TI APN 353S3899



PCB: MAKE XWS THICK, PLACE UNDER U17

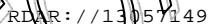
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PAGE TITLE			
SPKR AMP + STROBE DRIVER			
 Apple Inc.		DRAWING NUMBER	SIZE
		051-9478	D
		REVISION	
		8.0.0	
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		PAGE	15 OF 25
		SHEET	15 OF 56

D

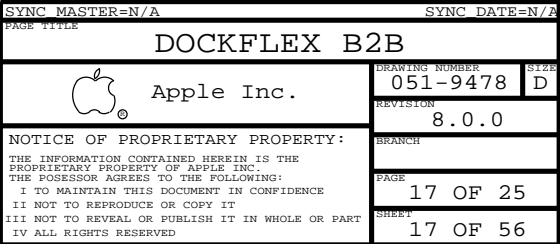


B

A

A


```
(USB VBUS, MENU BTN, SPEAKER, HP, HP EXTMIC, NAVAJO, ANTENNA LAT SW CTRL,  
MIC1 (PRIMARY MIC), ACC DET/ID/PWR, E75 DIFFPAIRS)
```



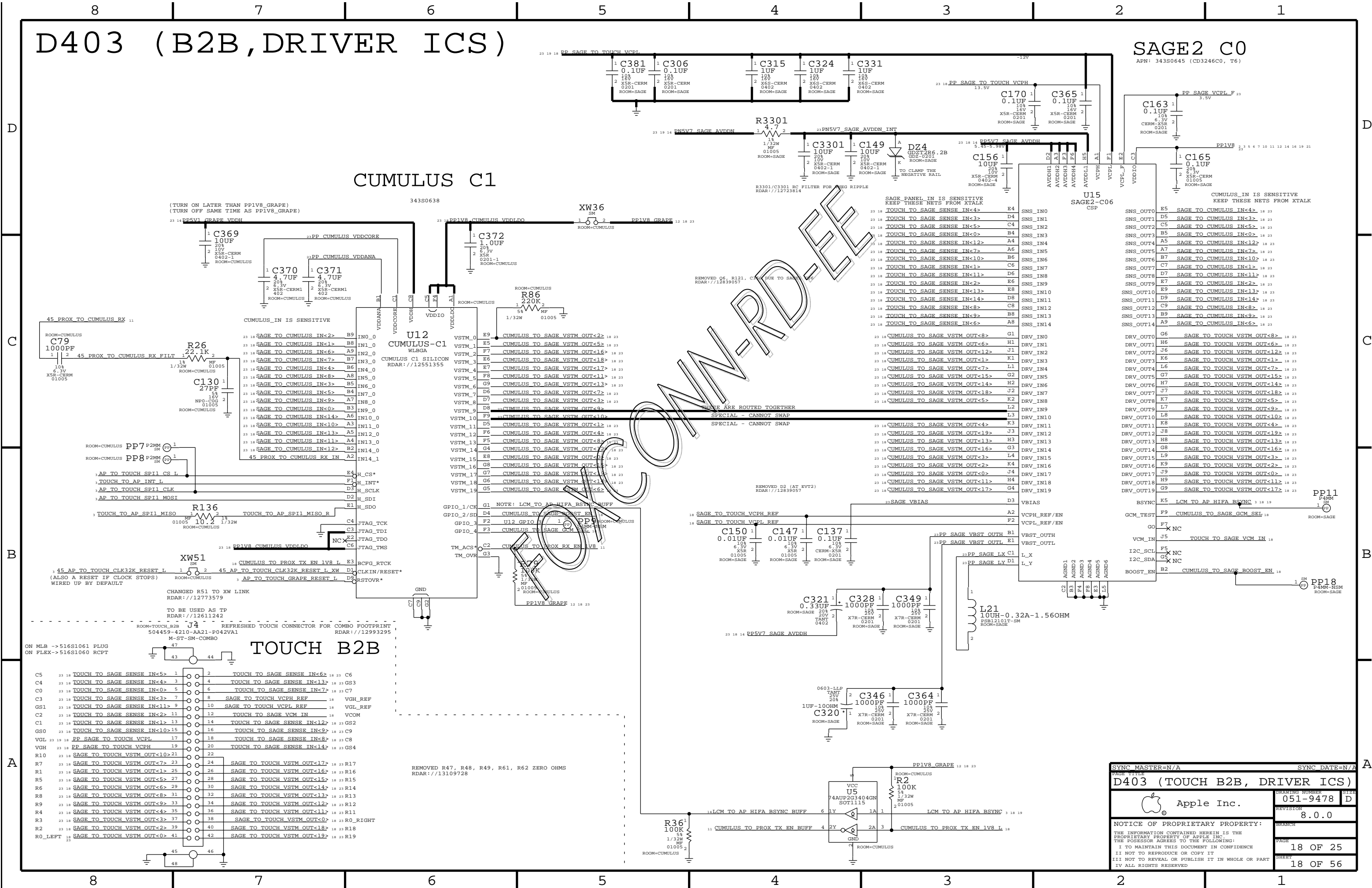
D403 (B2B, DRIVER ICS)

SAGE2 C0

APN: 343S0645 (CD3246C0, T6)

CUMULUS C1

TOUCH B2B



LCM B2B

D

C

B

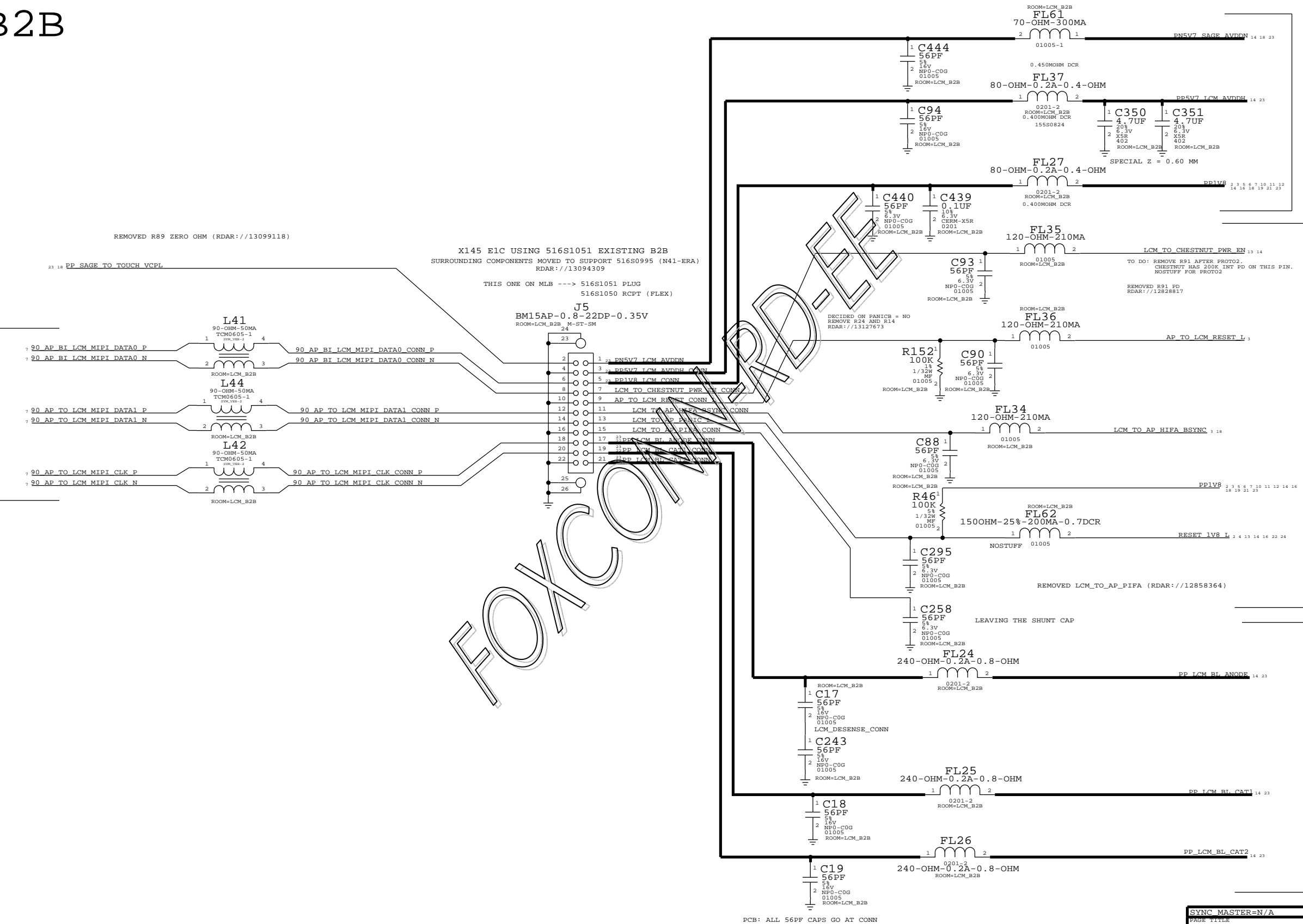
A

D

C

B

A




LCM:
POWER
(1.8V DVDD)
(+5.7V AVDD)
(-5.7V AVDD)

LCM:
DIGITAL I/F
(PWR_EN, RESET
PIFA, BSYNC)

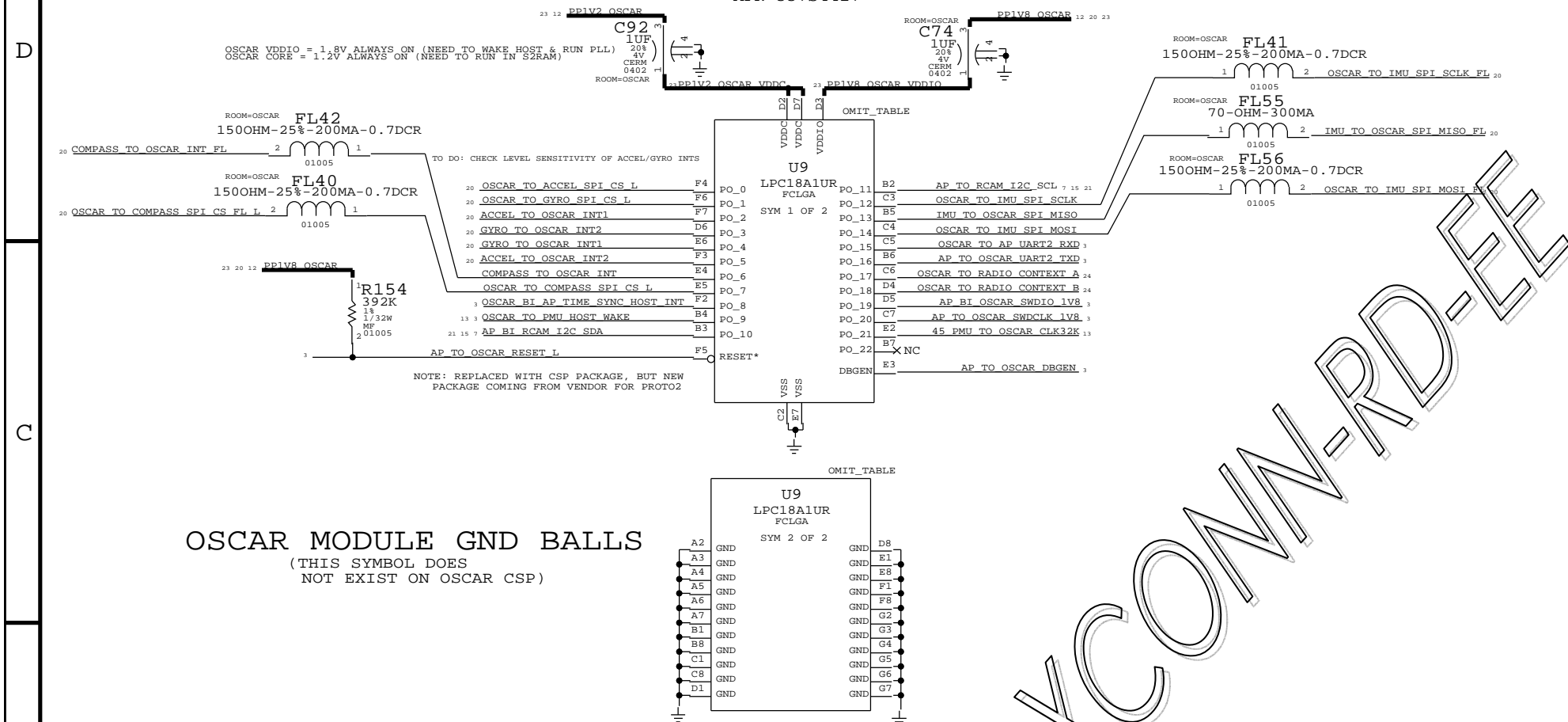
LCM:
BACKLIGHT

PCB: ALL 56PF CAPS GO AT CONN

SYNC MASTER=N/A		SYNC DATE=N/A	
PAGE TITLE			
LCM B2B			
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	BRANCH		
	PAGE		
	19 OF 25		
SHEET			
19 OF 56			

OSCAR + SENSORS

OSCAR MODULE (CONFORMAL COATED)
APN 337S4417

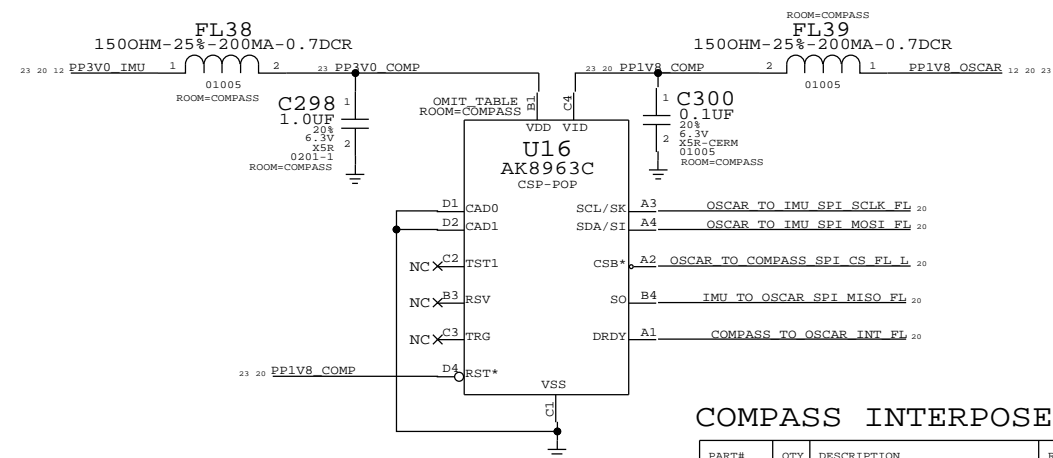


OSCAR MODULE GND BALLS
(THIS SYMBOL DOES
NOT EXIST ON OSCAR CSP)

THIS PART OUTSIDE OF SHIELD

COMPASS

COMPASS CSP: 338S1014
COMPASS INTERPOSER (FOOTPRINT ONLY): 998-5120
COMPASS INTERPOSER MODULE: 639-4269



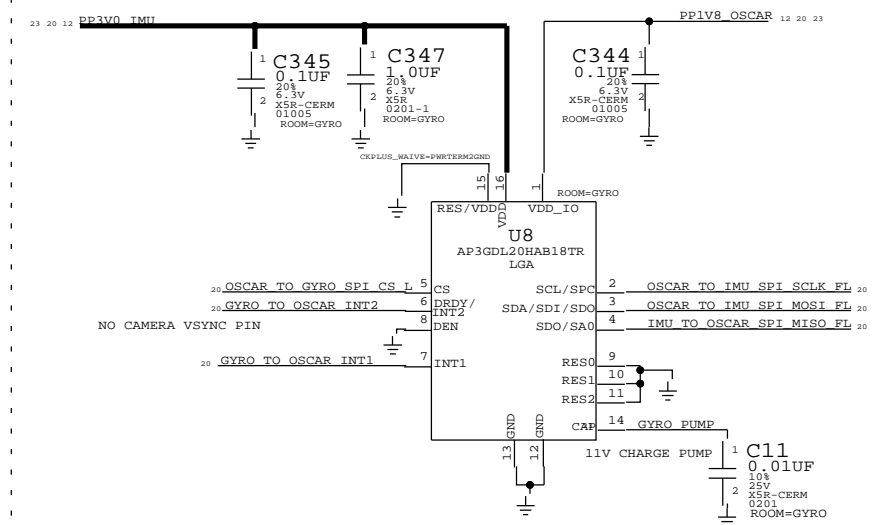
COMPASS INTERPOSER

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
639-4269	1	COMPASS INTERPOSER X152/X145	U16	CRITICAL	COMPASS_INTERPOSER

THESE PARTS INSIDE OF SHIELD

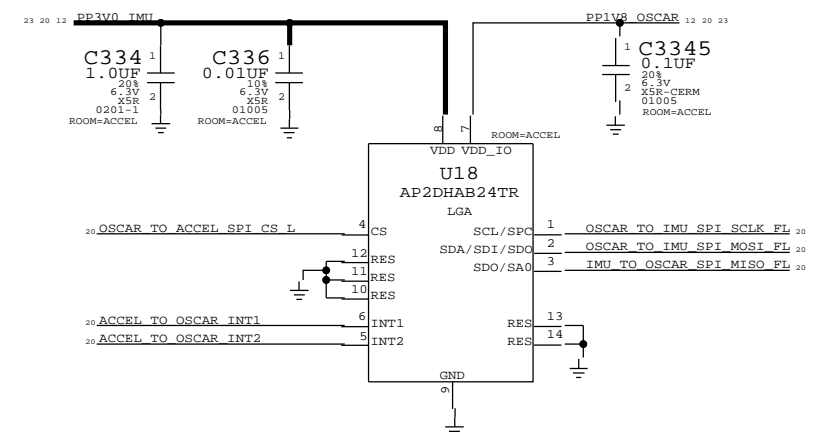
GYRO

ST MICRO AP3GDL20HAB, APN 338S1192




ACCELEROMETER

ST MICRO AP2DHAB, APN 338S1191



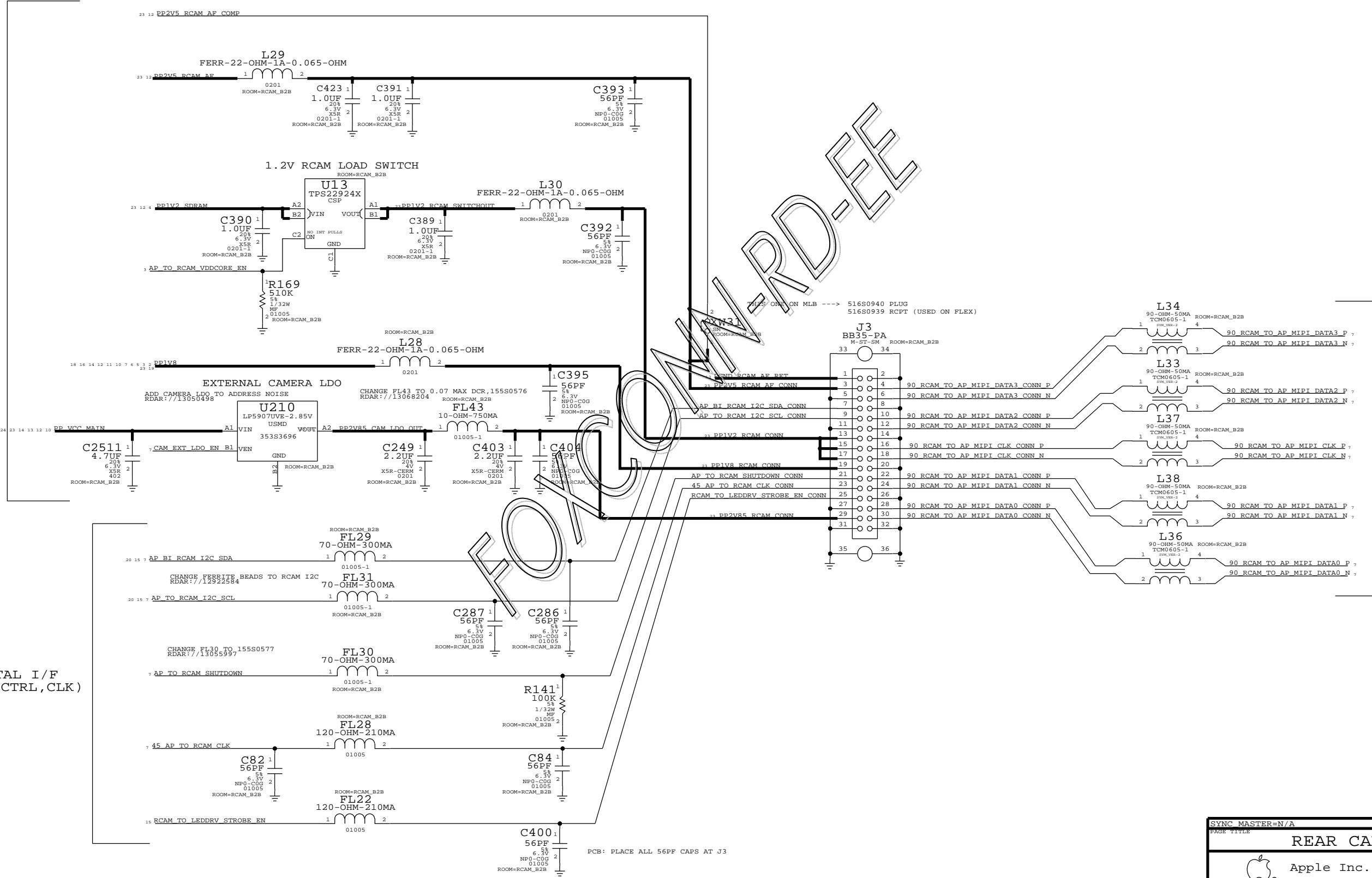
TO DO: VERIFY CONNECTIONS ON ACCEL (CS, SDO PINS)

SYNC MASTER=N/A		SYNC DATE=N/A		A	
PAGE TITLE					
OSCAR + SENSORS					
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RCAM B2B (REAR CAMERA CONNECTOR)

RCAM:
POWER:
(1.8V DVDD)
(2.8V AVDD)
(1.2V VCC)
(2.5V AF)

RCAM:
DIGITAL I/F
(I2C, CTRL, CLK)



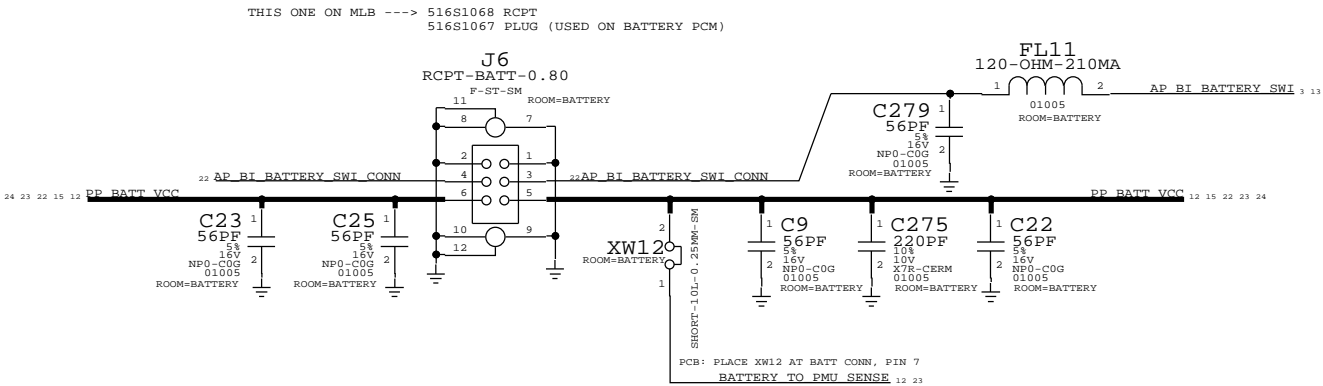
PCB: PLACE ALL 56PF CAPS AT J3

RCAM:
4-LANE MIPI

PAGE TITLE		SYNC MASTER=N/A		SYNC DATE=N/A	
REAR CAM B2B		DRAWING NUMBER		SIZE	
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BATT CONN, TPS, STANDOFFS/SHIELDS/FIDUCIALS

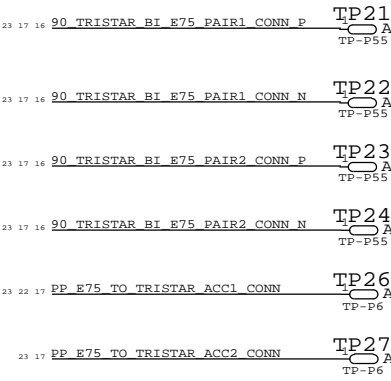
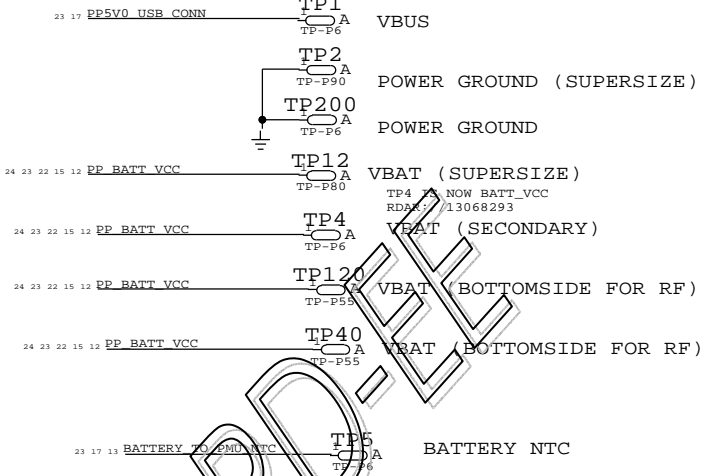
BATTERY CONN



POWER TP

TESTPOINTS

E75 - USB/UART/ID/POWER



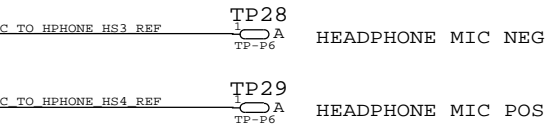
ACCESSORY ID AND POWER

SUPER TP

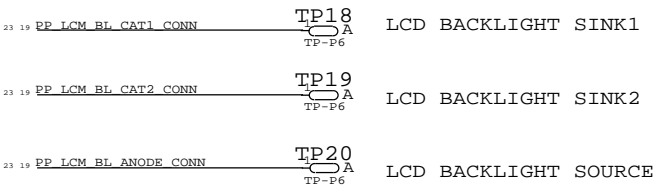
RESET

DFU

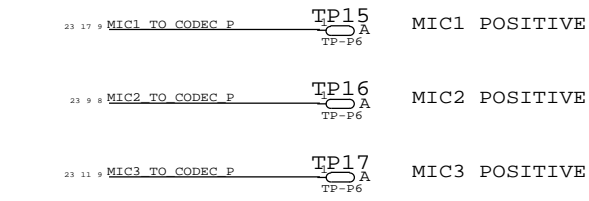
HEADPHONE MIC



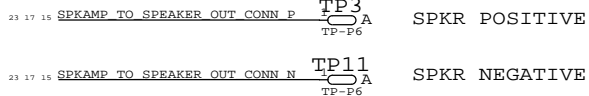
LCM BACKLIGHT



MIC AUDIO



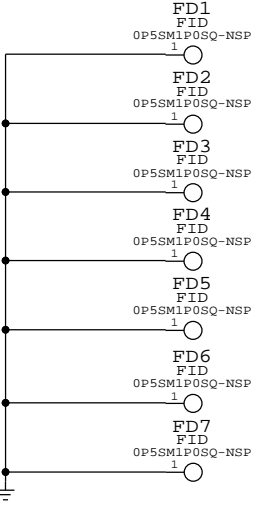
DRIVE MIC WRT NEAREST GROUND TEST POINT



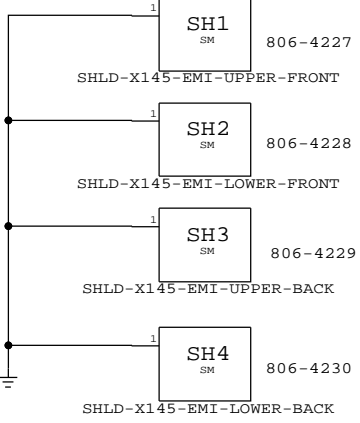
ADDED PER
RDAR://12611131

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BATT B2B, TPS, PD FEATURES		DRAWING NUMBER	051-9478
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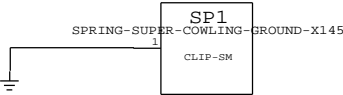
FIDUCIALS



SHIELDS

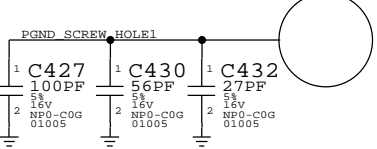


COWLING

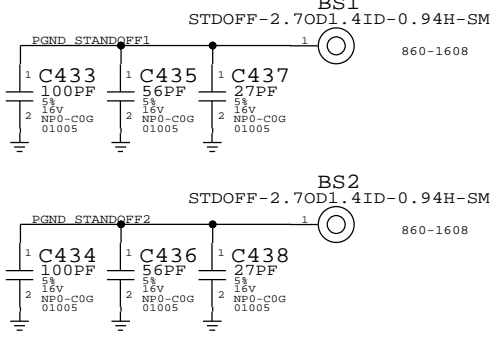


AC COUPLED SCREW HOLES + STANDOFFS
(ON NORTH END OF SINGLE_BRD, TO MITIGATE COMPASS RETURN CURRENTS)

SCREW HOLES



STANDOFFS



D

C

B

A

18 VOLTAGE=4.55V CUMULUS_TO_SAGE_VSTM_OUT<0>
19 VOLTAGE=4.55V CUMULUS_TO_SAGE_VSTM_OUT<1>
18 VOLTAGE=4.55V CUMULUS_TO_SAGE_VSTM_OUT<2>
18 VOLTAGE=4.55V CUMULUS_TO_SAGE_VSTM_OUT<3>
18 VOLTAGE=4.55V CUMULUS_TO_SAGE_VSTM_OUT<4>
18 VOLTAGE=4.55V CUMULUS_TO_SAGE_VSTM_OUT<5>
18 VOLTAGE=4.55V CUMULUS_TO_SAGE_VSTM_OUT<6>
18 VOLTAGE=4.55V CUMULUS_TO_SAGE_VSTM_OUT<7>
18 VOLTAGE=4.55V CUMULUS_TO_SAGE_VSTM_OUT<8>
18 VOLTAGE=4.55V CUMULUS_TO_SAGE_VSTM_OUT<9>
18 VOLTAGE=4.55V CUMULUS_TO_SAGE_VSTM_OUT<10>
18 VOLTAGE=4.55V CUMULUS_TO_SAGE_VSTM_OUT<11>
18 VOLTAGE=4.55V CUMULUS_TO_SAGE_VSTM_OUT<12>
18 VOLTAGE=4.55V CUMULUS_TO_SAGE_VSTM_OUT<13>
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18 VOLTAGE=4.55V SAGE_TO_TOUCH_VSTM_OUT<19>

11 9 VOLTAGE=3.8V CODEC_TO_RCVR_P
11 9 VOLTAGE=3.8V CODEC_TO_RCVR_N
11 9 VOLTAGE=3.8V CODEC_TO_RCVR_CONN_P
11 9 VOLTAGE=3.8V CODEC_TO_RCVR_CONN_N
11 9 VOLTAGE=3.8V CODEC_TO_HAC_P
11 9 VOLTAGE=3.8V CODEC_TO_HAC_N
11 9 VOLTAGE=3.8V CODEC_TO_HAC_CONN_P
11 9 VOLTAGE=3.8V CODEC_TO_HAC_CONN_N
11 9 VOLTAGE=3.114V CODEC_TO_HPHONE_L
11 9 VOLTAGE=3.114V CODEC_TO_HPHONE_R
11 9 VOLTAGE=3.114V CODEC_TO_HPHONE_L_CONN
11 9 VOLTAGE=3.114V CODEC_TO_HPHONE_R_CONN
11 9 VOLTAGE=2.7V CODEC_TO_HPHONE_HS3
11 9 VOLTAGE=2.7V CODEC_TO_HPHONE_HS4
22 17 9 VOLTAGE=2.7V CODEC_TO_HPHONE_HS3_REF
22 17 9 VOLTAGE=2.7V CODEC_TO_HPHONE_HS4_REF
17 9 VOLTAGE=2.7V CODEC_TO_HPHONE_HS3_CONN
17 9 VOLTAGE=2.7V CODEC_TO_HPHONE_HS4_CONN
17 9 VOLTAGE=2.7V CODEC_TO_HPHONE_HS3_REF_CONN
17 9 VOLTAGE=2.7V CODEC_TO_HPHONE_HS4_REF_CONN
17 9 VOLTAGE=4.3V HPHONE_TO_CODEC_DET
17 9 VOLTAGE=4.3V HPHONE_TO_CODEC_DET_CONN
9 VOLTAGE=2.5V 90_CODEC_BI_TRISTAR_MIKEYBUS_L67_P
9 VOLTAGE=2.5V 90_CODEC_BI_TRISTAR_MIKEYBUS_L67_N
16 9 VOLTAGE=2.5V 90_CODEC_BI_TRISTAR_MIKEYBUS_P
16 9 VOLTAGE=2.5V 90_CODEC_BI_TRISTAR_MIKEYBUS_N
16 9 VOLTAGE=2.5V 90_CODEC_BI_TRISTAR_MIKEYBUS_DIG_P
16 9 VOLTAGE=2.5V 90_CODEC_BI_TRISTAR_MIKEYBUS_DIG_N
16 9 VOLTAGE=2.5V TRISTAR_TO_PMU_MIKEYBUS_TEST_POS
16 13 VOLTAGE=2.5V TRISTAR_TO_PMU_MIKEYBUS_TEST_NEG
9 VOLTAGE=1.8V MIC1_TO_CODEC_L67_P
9 VOLTAGE=1.8V MIC1_TO_CODEC_L67_N
22 17 9 VOLTAGE=1.8V MIC1_TO_CODEC_P
17 9 VOLTAGE=1.8V MIC1_TO_CODEC_N
9 VOLTAGE=1.8V MIC2_TO_CODEC_L67_P
9 VOLTAGE=1.8V MIC2_TO_CODEC_L67_N
22 9 VOLTAGE=1.8V MIC2_TO_CODEC_P
9 VOLTAGE=1.8V MIC2_TO_CODEC_N
9 VOLTAGE=1.8V MIC3_TO_CODEC_L67_P
9 VOLTAGE=1.8V MIC3_TO_CODEC_L67_N
22 11 9 VOLTAGE=1.8V MIC3_TO_CODEC_P
11 9 VOLTAGE=1.8V MIC3_TO_CODEC_N
11 9 VOLTAGE=3.8V RCVR_TO_CODEC_RCVR_TEST
11 9 VOLTAGE=3.8V RCVR_TO_CODEC_RCVR_TEST_L67
17 9 VOLTAGE=3.114V HPHONE_TO_CODEC_HPHONE_TEST
9 VOLTAGE=3.114V HPHONE_TO_CODEC_HPHONE_TEST_L67
11 9 VOLTAGE=3.8V HAC_TO_CODEC_TEST
9 VOLTAGE=3.8V HAC_TO_CODEC_TEST_L67

12 VOLTAGE=4.6V 45_PMU_VPUMP
12 VOLTAGE=4.3V PMU_ACT_DIO
16 12 VOLTAGE=3.6V TRISTAR_TO_PMU_OVP_SW_EN_L
12 2 VOLTAGE=3.2V USB_VBUS_DETECT
16 12 VOLTAGE=5.25V TRISTAR_TO_PMU_USB_BRICKID
12 VOLTAGE=5.25V TRISTAR_TO_PMU_USB_BRICKID_R

22 17 13 VOLTAGE=2.5V BATTERY_TO_PMU_NTC
17 VOLTAGE=2.5V BATTERY_NTC_CONN
22 13 VOLTAGE=4.2V BATTERY_TO_PMU_SENSE
22 15 VOLTAGE=8V SPKAMP_TO_SPEAKER_OUT_CONN_P
22 15 VOLTAGE=8V SPKAMP_TO_SPEAKER_OUT_CONN_N
15 VOLTAGE=8V SPKAMP_TO_SPEAKER_OUT_P
15 VOLTAGE=8V SPKAMP_TO_SPEAKER_OUT_N
22 17 15 VOLTAGE=5.25V 90_TRISTAR_BI_E75_PAIR1_CONN_P
22 17 15 VOLTAGE=5.25V 90_TRISTAR_BI_E75_PAIR1_CONN_N
22 17 15 VOLTAGE=5.25V 90_TRISTAR_BI_E75_PAIR2_CONN_P
22 17 15 VOLTAGE=5.25V 90_TRISTAR_BI_E75_PAIR2_CONN_N
15 VOLTAGE=3.0V TRISTAR_BYPASS
18 14 VOLTAGE=-5.7V PN5V7_SAGE_AVDDN
18 VOLTAGE=-5.7V PN5V7_SAGE_AVDDN_INT
18 VOLTAGE=-5.7V PN5V7_LCM_AVDDN

18 VOLTAGE=2.5V SAGE_VBIAS
20 VOLTAGE=11V GYRO_PUMP
18 VOLTAGE=XV SAGE_TO_CUMULUS_IN<0>
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17 14 VOLTAGE=18.0V PP16V5_MESA_DOCK_CONN
17 VOLTAGE=1.0V PP1V0
12 7 2 VOLTAGE=1.0V PP1V0_S02
12 4 VOLTAGE=1.0V PP1V0_SRAM
12 5 VOLTAGE=1.1V PP1V1_CPU
12 4 VOLTAGE=1.1V PP1V1_GPU
12 VOLTAGE=1.2V PP1V2
12 VOLTAGE=1.2V PP1V2_NAND_VDDI
12 VOLTAGE=1.2V PP1V2_OSCAR
12 VOLTAGE=1.2V PP1V2_OSCAR_VDDC
21 VOLTAGE=1.2V PP1V2_RCAM_CONN
21 VOLTAGE=1.2V PP1V2_RCAM_SWITCHOUT
21 4 VOLTAGE=1.2V PP1V2_SDRAM

16 12 VOLTAGE=4.3V PP_VCC_MAIN_UVLO_SENSE
16 12 VOLTAGE=2.8V PP2V80_THROTTLER
21 11 VOLTAGE=2.85V PP2V85_CAM_LDO_OUT
16 12 VOLTAGE=2.8V PP2V80_THROTTLER_FB
17 VOLTAGE=1.8V PP1V8_MESA_LOAD_SW_OUT

VOLTAGE=0V GND

18 16 14 12 11 10 7 6 5 3 2 VOLTAGE=1.8V PP1V8
12 5 VOLTAGE=1.8V PP1V8_ALWAYS
2 VOLTAGE=1.8V PP1V8_COMP
1 VOLTAGE=1.8V PP1V8_CUMULUS_VDD_LDO
1 VOLTAGE=1.8V PP1V8_FCAM_CONN
18 12 VOLTAGE=1.8V PP1V8_GRAPE
1 VOLTAGE=1.8V PP1V8_LCM_CONN
1 VOLTAGE=1.8V PP1V8_OSCAR
20 VOLTAGE=1.8V PP1V8_OSCAR_VDDIO
2 VOLTAGE=1.8V PP1V8_PLL
21 VOLTAGE=1.8V PP1V8_RCAM_CONN
21 VOLTAGE=1.8V PP1V8_SDRAM
17 VOLTAGE=1.8V PP1V8_SDRAM_DOCK_CONN
15 12 VOLTAGE=1.8V PP1V8_VA_L19_L67
21 12 VOLTAGE=1.8V PP1V8_XTAL
21 12 VOLTAGE=2.5V PP2V5_RCAM_AF
21 12 VOLTAGE=2.5V PP2V5_RCAM_AF_COMP
21 12 VOLTAGE=2.5V PP2V5_RCAM_AF_CONN

12 VOLTAGE=2.8V PP2V85_FCAM_CONN
21 VOLTAGE=2.8V PP2V85_RCAM_CONN
16 12 VOLTAGE=3.0V PP3V0_ACC
12 VOLTAGE=3.0V PP3V0_ALS
20 VOLTAGE=3.0V PP3V0_COMP
20 12 VOLTAGE=3.0V PP3V0_IMU
12 VOLTAGE=3.0V PP3V0_NAND

17 12 VOLTAGE=3.0V PP3V0_NAVALO
12 VOLTAGE=3.0V PP3V0_NAVALO_CONN
11 VOLTAGE=3.0V PP3V0_PROX
12 11 VOLTAGE=3.0V PP3V0_PROX_ALS
12 11 VOLTAGE=3.0V PP3V0_PROX_IRLED
16 12 VOLTAGE=3.0V PP3V0_SDRAM

12 2 VOLTAGE=3.3V PP3V3_USB
22 4 VOLTAGE=5.0V PP5V0_USB_CONN
17 14 VOLTAGE=5.0V PP5V0_USB_PROT
18 12 VOLTAGE=5.1V PP5V1_GRAPE_VDDH
19 VOLTAGE=5.7V PP5V7_LCM_AVDDH
19 VOLTAGE=5.7V PP5V7_LCM_AVDDH_CONN


18 14 VOLTAGE=5.7V PP5V7_SAGE_AVDDH
14 VOLTAGE=6V PP6V0_LCM_BOOST
24 22 15 VOLTAGE=4.3V PP_BATT_VCC
18 VOLTAGE=4.3V PP_BATT_VCC_L19_VP
12 VOLTAGE=4.3V PP_BUCK0_LX0
12 VOLTAGE=4.3V PP_BUCK0_LX1
12 VOLTAGE=4.3V PP_BUCK0_LX2
12 VOLTAGE=4.3V PP_BUCK0_LX3
12 VOLTAGE=4.3V PP_BUCK1_LX0
12 VOLTAGE=4.3V PP_BUCK1_LX1

12 VOLTAGE=4.3V PP_BUCK2_LX
12 VOLTAGE=4.3V PP_BUCK3_LX
12 VOLTAGE=4.3V PP_BUCK4_LX
12 VOLTAGE=4.3V PP_BUCK5_LX
14 VOLTAGE=-6V PP_CHESTNUT_CN
14 VOLTAGE=6V PP_CHESTNUT_CP
14 VOLTAGE=6V PP_CHESTNUT_LXP

10 VOLTAGE=1.8V PP_CODEC_FILT+
10 VOLTAGE=2.2V PP_CODEC_SPKR_VQ
17 10 VOLTAGE=2.7V PP_CODEC_TO_MIC1_BIAS
17 VOLTAGE=2.7V PP_CODEC_TO_MIC1_BIAS_CONN
11 10 4 VOLTAGE=2.7V PP_CODEC_TO_MIC3_BIAS
11 VOLTAGE=2.7V PP_CODEC_TO_MIC3_BIAS_CONN
10 VOLTAGE=2.5V PP_CODEC_VCPFFILT+
10 VOLTAGE=-2.5V PP_CODEC_VCPFFILT-

10 VOLTAGE=0.2V PP_CODEC_VHP_FLYC
10 VOLTAGE=-2.5V PP_CODEC_VHP_FLYN
10 VOLTAGE=2.5V PP_CODEC_VHP_FLYP
18 VOLTAGE=1.6V PP_CUMULUS_VDDANA
18 VOLTAGE=1.6V PP_CUMULUS_VDDCORE
17 18 VOLTAGE=4.3V PP_E75_TO_TRISTAR_ACC1
22 17 VOLTAGE=4.3V PP_E75_TO_TRISTAR_ACC1_CONN
17 18 VOLTAGE=4.3V PP_E75_TO_TRISTAR_ACC2
22 17 VOLTAGE=4.3V PP_E75_TO_TRISTAR_ACC2_CONN
10 VOLTAGE=2.7V PP_EXTMIC_BIAS
10 VOLTAGE=2.7V PP_EXTMIC_BIAS_FILT
10 VOLTAGE=2.7V PP_EXTMIC_BIAS_FILT_IN
10 VOLTAGE=2.7V PP_EXTMIC_BIAS_IN
15 VOLTAGE=8V PP_L19_VBOOST
19 14 VOLTAGE=22V PP_LCM_BL_ANODE
22 19 VOLTAGE=22V PP_LCM_BL_ANODE_CONN
19 14 VOLTAGE=0.2V PP_LCM_BL_CAT1
22 19 VOLTAGE=0.2V PP_LCM_BL_CAT1_CONN
19 14 VOLTAGE=0.2V PP_LCM_BL_CAT2
22 19 VOLTAGE=0.2V PP_LCM_BL_CAT2_CONN
24 19 VOLTAGE=2.65V PP_LD014_2P65
14 13 VOLTAGE=2.5V CHESTNUT_TO_PMU_ADCIN7
17 14 VOLTAGE=5V E75_TO_PMU_ACC_DETECT
17 14 VOLTAGE=5V E75_TO_PMU_ACC_DETECT_R
22 13 VOLTAGE=5V PMU_TO_TP_AMUX_AY
22 13 VOLTAGE=5V PMU_TO_TP_AMUX_BY
13 VOLTAGE=2.5V FOREHEAD_TO_PMU_NTC_P
13 VOLTAGE=2.5V CAM_TO_PMU_NTC_P
13 VOLTAGE=2.5V PA_TO_PMU_NTC_P
13 VOLTAGE=2.5V H6P_TO_PMU_NTC_P
13 VOLTAGE=2.5V 45_PMU_TCAL
13 VOLTAGE=5V PP_LED_BOOST_OUT
13 VOLTAGE=5V PP_LED_DRV_LX
13 VOLTAGE=0.4V PP_MIPIOD_VREG
7 VOLTAGE=0.4V PP_MIPIID_VREG
12 5 VOLTAGE=3.4V PP_PMU_TO_VIBE
4 VOLTAGE=3.4V PP_PMU_TO_VIBE_CONN
12 VOLTAGE=5.25V PP_PMU_VCENTER
13 VOLTAGE=4.3V PP_PMU_VDD_REF
13 VOLTAGE=2.5V PP_PMU_VDD_RTC
13 VOLTAGE=1.2V PP_PMU_VREF
12 VOLTAGE=5.25V PP_PMU_VSW_CHG
18 VOLTAGE=5.7V PP_SAGE_LX
18 VOLTAGE=17V PP_SAGE_LY
18 VOLTAGE=13.5V PP_SAGE_TO_TOUCH_VCPH

10 VOLTAGE=-12V PP_SAGE_TO_TOUCH_VCPH
15 VOLTAGE=18V PP_SAGE_VBST_OUTH
15 VOLTAGE=-14V PP_SAGE_VBST_OUTL
15 VOLTAGE=-12V PP_SAGE_VCPH_F
15 VOLTAGE=1.8V PP_SPKAMP_FILT
15 VOLTAGE=1V PP_SPKAMP_LDO_FILT
15 VOLTAGE=8V PP_SPKAMP_SW
15 VOLTAGE=5V PP_STRB_DRIVER_TO_LED_COOL
15 VOLTAGE=5V PP_STRB_DRIVER_TO_LED_WARM
10 VOLTAGE=4.3V PP_VCC_MAIN
10 VOLTAGE=4.3V PP_VCC_MAIN_CODEC
10 VOLTAGE=22V PP_WLED_LX

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VOLTAGE NETS			
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RADIO_MLB HIERARCHICAL SYMBOL

AP/RADIO INTERFACE

				SUBDESIGN_SUFFIX=RF	
				I616	
35 23 22 15 1	PP_BATT_VCC	MAKE_BASE=TRUE	PP_BATT_VCC_CONN		
56 23 21 14 13 12 1	PP_VCC_MAIN	MAKE_BASE=TRUE	PP_VCC_MAIN_WLAN		
56 23 17 16 14 12 10 4 3	PP1V8_SDRAM	MAKE_BASE=TRUE	PP_WL_BT_VDDIO_AP		
36 23 17	PP_LDO14_2P65	MAKE_BASE=TRUE	PP_LDO14_2V65		
35 1	AP TO RADIO ON L	MAKE_BASE=TRUE	RADIO_ON_L	BB_JTAG_TCK	MAKE_BASE=TRUE AP TO BB JTAG TCK
35 1	BB TO AP RESET DET L	MAKE_BASE=TRUE	RESET_DET_L	BB_JTAG_TDI	MAKE_BASE=TRUE AP TO BB JTAG TDI
35 13	PMU TO BB RST L	MAKE_BASE=TRUE	RESET_PMU_L	BB_JTAG_TMS	MAKE_BASE=TRUE AP TO BB JTAG TMS
35 1	AP TO BB RST L	MAKE_BASE=TRUE	BB_RST_L	BB_JTAG_TRST_L	MAKE_BASE=TRUE AP TO BB JTAG TRST
35 22 19 16 14 13 4 2	RESET 1V8 L	MAKE_BASE=TRUE	RF_RESET_L	BB_JTAG_TDO	MAKE_BASE=TRUE BB TO AP JTAG TDO
35 13	45 PMU TO WLAN CLK32K	MAKE_BASE=TRUE	CLK32K_AP		
39 15	BB TO LEDDRV GSM BLANK	MAKE_BASE=TRUE	TX_GTR_THRESH		
35 14	90 TRISTAR BI BB USB N	MAKE_BASE=TRUE	90_BB_USB_D_N		
35 14	90 TRISTAR BI BB USB P	MAKE_BASE=TRUE	90_BB_USB_D_P		
35 13	PMU TO BB VBUS DET	MAKE_BASE=TRUE	BB_USB_VBUS		
35 3	AP TO BB UART4 RTS L	MAKE_BASE=TRUE	BB_UART_CTS_L		
35 3	BB TO AP UART4 CTS L	MAKE_BASE=TRUE	BB_UART_RTS_L		
35 16 3	AP TO BB UART4 TXD	MAKE_BASE=TRUE	BB_UART_RXD		
35 16 3	BB TO AP UART4 RXD	MAKE_BASE=TRUE	BB_UART_TXD		
35 13	BB TO PMU HOST WAKE	MAKE_BASE=TRUE	HOST_WAKE_BB		
39 3	BB TO AP PP SYNC	MAKE_BASE=TRUE	PP_SYNC		
35 3	45 AP TO BB I2S1 BCLK	MAKE_BASE=TRUE	BB_I2S_CLKRADIO_BB		
35 3	AP TO BB I2S1 DOUT	MAKE_BASE=TRUE	BB_I2S_RXD		
35 3	BB TO AP I2S1 DIN	MAKE_BASE=TRUE	BB_I2S_TXD		
35 3	AP TO BB I2S1 LRCLK	MAKE_BASE=TRUE	BB_I2S_MCLK		
35 13	RADIO TO PMU ADC SMPS1 MSMC 1V05	MAKE_BASE=TRUE	ADC_SMPS1_MSMC_1V05		
35 13	RADIO TO PMU ADC SMPS3 MSME 1V8	MAKE_BASE=TRUE	ADC_SMPS3_MSME_1V8		
35 13	RADIO TO PMU ADC LDO6 RUIM 1V8	MAKE_BASE=TRUE	ADC_LDO6_RUIM_1V8		
35 13	RADIO TO PMU ADC LVS1	MAKE_BASE=TRUE	ADC_LVS1		
35 13	PMU TO WLAN REG ON	MAKE_BASE=TRUE	WLAN_REG_ON		
56 3	AP TO WLAN UART3 TXD	MAKE_BASE=TRUE	WLAN_UART_RXD		
56 3	WLAN TO AP UART3 RXD	MAKE_BASE=TRUE	WLAN_UART_TXD		
56 13	WLAN TO PMU HOST WAKE	MAKE_BASE=TRUE	HOST_WAKE_WLAN		
35 13	PMU TO BT REG ON	MAKE_BASE=TRUE	BT_REG_ON		
56 3	AP TO BT UART1 RTS L	MAKE_BASE=TRUE	BT_UART_CTS_L		
56 3	BT TO AP UART1 CTS L	MAKE_BASE=TRUE	BT_UART_RTS_L		
56 3	AP TO BT UART1 TXD	MAKE_BASE=TRUE	BT_UART_RXD		
56 3	BT TO AP UART1 RXD	MAKE_BASE=TRUE	BT_UART_TXD		
35 3	AP TO BT WAKE	MAKE_BASE=TRUE	BT_WAKE		
35 1	BT TO PMU HOST WAKE	MAKE_BASE=TRUE	HOST_WAKE_BT		
56 1	45 AP TO BT I2S3 BCLK	MAKE_BASE=TRUE	BT_PCM_CLK		
56 3	AP TO BT I2S3 DOUT	MAKE_BASE=TRUE	BT_PCM_IN		
56 3	BT TO AP I2S3 DIN	MAKE_BASE=TRUE	BT_PCM_OUT		
56 3	AP TO BT I2S3 LRCLK	MAKE_BASE=TRUE	BT_PCM_SYNC		
35 2	50 AP BI BB HSIC1 DATA	MAKE_BASE=TRUE	50_HSIC_BB_DATA		
35 1	50 AP BI BB HSIC1 STB	MAKE_BASE=TRUE	50_HSIC_BB_STROBE		
35 1	AP TO BB HSIC1 RDY	MAKE_BASE=TRUE	AP_HSIC1_RDY		
35 1	BB TO AP HSIC1 RDY	MAKE_BASE=TRUE	PBL_RUN_BB_HSIC1_RDY		
39 1	BB TO AP HSIC1 REMOTE WAKE	MAKE_BASE=TRUE	BB_HSIC1_REMOTE_WAKE		
39 1	AP TO BB WAKE MODEM	MAKE_BASE=TRUE	AP_WAKE_MODEM		
35 2	50 AP BI WLAN HSIC2 DATA	MAKE_BASE=TRUE	50_HSIC_WLAN_DATA		
35 1	50 AP BI WLAN HSIC2 STB	MAKE_BASE=TRUE	50_HSIC_WLAN_STROBE		
35 1	AP TO WLAN HSIC2 RDY	MAKE_BASE=TRUE	AP_HSIC3_RDY		
35 1	WLAN TO AP HSIC2 RDY	MAKE_BASE=TRUE	WLAN_HSIC3_DEVICE_RDY		
35 1	WLAN TO AP HSIC2 REMOTE WAKE	MAKE_BASE=TRUE	WLAN_HSIC3_RESUME		
35 17	BB TO LAT SW1 CTL	MAKE_BASE=TRUE	LAT_SW1_CTL		
39 17	BB TO LAT SW2 CTL	MAKE_BASE=TRUE	LAT_SW2_CTL		
	<OUT> BB_TO_LAT_SW3_CTL	MAKE_BASE=TRUE	BB_SPI_TO_PAC_CS		
39 1	BB TO ANTENNA PAC SPI CS	MAKE_BASE=TRUE	BB_SPI_TO_PAC_CLK		
39 1	BB TO ANTENNA PAC SPI SCLK	MAKE_BASE=TRUE	BB_SPI_TO_PAC_DATA_MOSI		
39 1	BB TO ANTENNA PAC SPI MOSI	MAKE_BASE=TRUE	PAC_TO_BB_SPI_DATA_MISO		
	NOTE: ANTENNA_PAC_TO_BB_SPI_MISO DOES NOT COME TO SINGLE_BRD MLB				
39 1	BB TO AP IPC GPIO	MAKE_BASE=TRUE	BB_IPC_GPIO		
39 1	OSCAR TO RADIO CONTEXT A	MAKE_BASE=TRUE	OSCAR_CONTEXT_A		
39 1	OSCAR TO RADIO CONTEXT B	MAKE_BASE=TRUE	OSCAR_CONTEXT_B		

RF BOARD_ID BOM OPTIONS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
118S0621	1	1.00M 1% 01005	R25_RF	CRITICAL	N51_ALL
118S0732	1	50K 1% 01005	R26_RF	CRITICAL	N51_ALL
117S0159	1	470K 5% 01005	R25_RF	CRITICAL	N51_DTD
118S0626	1	100K 1% 01005	R26_RF	CRITICAL	N51_DTD
118S0626	1	100K 1% 01005	R25_RF	CRITICAL	N53_ALL
118S0726	1	162K 1% 01005	R26_RF	CRITICAL	N53_ALL
118S0626	1	100K 1% 01005	R25_RF	CRITICAL	N53_DTD
118S0623	1	267K 1% 01005	R26_RF	CRITICAL	N53_DTD

SYNC MASTER=N/A		SYNC DATE=N/A			
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	8	7	6	5	4	3	2	1
	AP_TO_PMU_SOCHOT1 AP_TO_PMU_SOCHOT1_H6 P AP_TO_PMU_TEST_CLKOUT T AP_TO_PMU_VIBE_PWM_EN N AP_TO_RADIO_ON_L AP_TO_RADIO_ON_L RADIO_ON_L - @single_brd_lib.RADIO_MLB(1616_page 24) AP_TO_RCAM_I2C_SCL AP_TO_RCAM_I2C_SCL - @single_brd_lib.SINGLE_BRD AP_TO_RCAM_SHUTDOWN AP_TO_RCAM_SHUTDOWN_CONN AP_TO_RCAM_VDDCORE_EN N AP_TO_SPKAMP_BEE_GKES S AP_TO_SPKAMP_RESET_L AP_TO_TOUCH_GRAPE_RESET_L - SET_L AP_TO_TOUCH_SPI1_CLK AP_TO_TOUCH_SPI1_CS L AP_TO_TOUCH_SPI1_MOS I AP_TO_TRISTAR_ACC_UA RT6_TXD AP_TO_TRISTAR_DEBUG_UART1_TXD UART_TXD AP_TO_WLAN_HSIC2_RDY @single_brd_lib.SINGLE_BRD AP_HSIC3_RDY - @single_brd_lib.RADIO_MLB(1616_page 24) AP_TO_WLAN_UART3_TXD @single_brd_lib.SINGLE_BRD WLAN_UART_RXD @single_brd_lib.RADIO_MLB(1616_page 24) BATTERY_NTC_CONN BATTERY_TO_PMU_NTC BATTERY_TO_PMU_SENSE @single_brd_lib.SINGLE_BRD BB_TO_ANTENNA_PAC_SPI_CS @single_brd_lib.SINGLE_BRD BB_SPI_TO_PAC_CS - @single_brd_lib.RADIO_MLB(1616_page 24) BB_TO_ANTENNA_PAC_SPI_MOSI @single_brd_lib.SINGLE_BRD BB_SPI_TO_PAC_DATA_MOSI - @single_brd_lib.RADIO_MLB(1616_page 24) BB_TO_ANTENNA_PAC_SPI_SCLK @single_brd_lib.SINGLE_BRD BB_SPI_TO_PAC_CLK - @single_brd_lib.RADIO_MLB(1616_page 24) BB_TO_AP_HSIC1_RDY @single_brd_lib.SINGLE_BRD PBL_RUN_BB_HSIC1_RDY - @single_brd_lib.RADIO_MLB(1616_page 24) BB_TO_AP_HSIC1_REMOT K_WAKE @single_brd_lib.SINGLE_BRD BB_HSIC1_REMOT_WAKE - @single_brd_lib.RADIO_MLB(1616_page 24) BB_TO_AP_I2S1_DIN @single_brd_lib.SINGLE_BRD BB_I2S_TXD - @single_brd_lib.RADIO_MLB(1616_page 24) BB_TO_AP_IPC_GPIO @single_brd_lib.SINGLE_BRD BB_IPC_GPIO - @single_brd_lib.RADIO_MLB(1616_page 24) BB_TO_AP_JTAG_TDO @single_brd_lib.SINGLE_BRD BB_JTAG_TDO - @single_brd_lib.RADIO_MLB(1616_page 24) BB_TO_AP_PP_SYNC @single_brd_lib.SINGLE_BRD PP_SYNC - @single_brd_lib.RADIO_MLB(1616_page 24) BB_TO_AP_RESET_DET_L @single_brd_lib.SINGLE_BRD RESET_DET_L - @single_brd_lib.RADIO_MLB(1616_page 24) BB_TO_AP_UART4_CTS_L @single_brd_lib.SINGLE_BRD BB_UART_RTS_L - @single_brd_lib.RADIO_MLB(1616_page 24) BB_TO_AP_UART4_RXD @single_brd_lib.SINGLE_BRD BB_UART_TXD - @single_brd_lib.RADIO_MLB(1616_page 24) BB_TO_LAT_SW1_CTL @single_brd_lib.SINGLE_BRD LAT_SW1_CTL - @single_brd_lib.RADIO_MLB(1616_page 24) BB_TO_LAT_SW2_CTL @single_brd_lib.SINGLE_BRD LAT_SW2_CTL - @single_brd_lib.SINGLE_BRD LAT_SW2_CTL -	381 1386 382 283 13C6 3C5 1287 3C8 24D6 35D3 35D8 37C8 7C4 15A6 20D5 21B7 21C4 7C5 21B7 21B4 3C5 21C7 3D8 15C6 3C8 15C7 3C8 1887 384 1888 384 1888 384 1888 384 1888 385 16C4 3C5 16C4 3C2 2486 35C6 35C8 56B3 3C5 24C6 35C8 56B3 17C4 23D5 13A6 17C1 22C4 23D5 12C7 22C6 23D5 24A6 35B8 39C4 53C7 24A6 35B8 39C4 53C7 24A6 35B8 39C4 53C7 3C2 2486 35C1 35D8 39B2 3C2 2486 35C8 39B2 3C4 24C6 35B5 35C8 39B4 7C8 24A6 35A8 39B2 3C7 24D3 35B8 35C3 38B3 3C5 24C6 35C8 39B2 3C8 24D6 35C1 35D8 39B2 3C5 24C6 35C3 35C8 39C4 3C5 16C4 24C6 35C3 35C8 39C4 17B1 24A6 35B8 35C1 39C2 17B1 24A6 35B8 39C2	<div>@single_brd_lib.RADIO_MLB(1616_page 24) BB_TO_LEDDRVR_GSM_BLA NK TX_GTR_THRESH - @single_brd_lib.RADIO_MLB(1616_page 24) BB_TO_PMU_HOST_WAKE @single_brd_lib.SINGLE_BRD HOST_WAKE_BB - @single_brd_lib.RADIO_MLB(1616_page 24) BOARD_INFO @single_brd_lib.SINGLE_BRD BOARD_INFO_M @single_brd_lib.SINGLE_BRD BOARD_INFO_R @single_brd_lib.SINGLE_BRD BT_TO_AP_I2S3_DIN @single_brd_lib.SINGLE_BRD BT_PCM_OUT - @single_brd_lib.RADIO_MLB(1616_page 24) BT_TO_AP_UART1_CTS_L BT_TO_AP_UART1_CTS_L - @single_brd_lib.SINGLE_BRD BT_UART_RTS_L - @single_brd_lib.RADIO_MLB(1616_page 24) BT_TO_AP_UART1_RXD @single_brd_lib.SINGLE_BRD BT_UART_TXD - @single_brd_lib.RADIO_MLB(1616_page 24) BT_TO_PMU_HOST_WAKE BT_TO_PMU_HOST_WAKE - @single_brd_lib.SINGLE_BRD HOST_WAKE_BT - @single_brd_lib.RADIO_MLB(1616_page 24) BUTTON_TO_AP_HOLD_KEY Y_BUFF_L BUTTON_TO_AP_HOLD_KEY_CONN_L Y_CONN_L BUTTON_TO_AP_HOLD_KEY Y_L BUTTON_TO_AP_MENU_KEY Y_BUFF_L BUTTON_TO_AP_MENU_KEY_CONN_L Y_CONN_L BUTTON_TO_AP_MENU_KEY Y_L BUTTON_TO_AP_RINGER_A @single_brd_lib.SINGLE_BRD BUTTON_TO_AP_RINGER_A_CONN @single_brd_lib.SINGLE_BRD BUTTON_TO_AP_VOL_DOWN_CONN_L N_CONN_L BUTTON_TO_AP_VOL_DOWN N_L BUTTON_TO_AP_VOL_UP_CONN_L CONN_L BUTTON_TO_AP_VOL_UP L CAM_EXT_LDO_EN @single_brd_lib.SINGLE_BRD CAM_NTC_N @single_brd_lib.SINGLE_BRD CAM_NTC_P @single_brd_lib.SINGLE_BRD CAM_TO_PMU_NTC_P @single_brd_lib.SINGLE_BRD CHESTNUT_TO_PMU_ADCIN7 @single_brd_lib.SINGLE_BRD CODEC_MBUS_REF @single_brd_lib.SINGLE_BRD CODEC_RESET_L @single_brd_lib.SINGLE_BRD CODEC_TO_AP_ASP_I2S0 _DIN CODEC_TO_AP_INT_L @single_brd_lib.SINGLE_BRD CODEC_TO_AP_SPI3_MISO O CODEC_TO_AP_VSP_I2S4 _DIN CODEC_TO_AP_XSP_I2S2 _DIN CODEC_TO_HAC_CONN_N @single_brd_lib.SINGLE_BRD CODEC_TO_HAC_CONN_P @single_brd_lib.SINGLE_BRD CODEC_TO_HAC_N @single_brd_lib.SINGLE_BRD CODEC_TO_HAC_P @single_brd_lib.SINGLE_BRD CODEC_TO_HPHONE_HS3 @single_brd_lib.SINGLE_BRD CODEC_TO_HPHONE_HS3_CONN @single_brd_lib.SINGLE_BRD CODEC_TO_HPHONE_HS3_REF @single_brd_lib.SINGLE_BRD CODEC_TO_HPHONE_HS3_REF_CONN @single_brd_lib.SINGLE_BRD CODEC_TO_HPHONE_HS4 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23D7 17C5 23C7 9C2 17C1 23D7 17C4 23D7 9C2 17C1 23D7 17C4 23D7 8C4 10C3 13B4 11A6 23D7	<div>CODEC_TO_RCVR_CONN_P CODEC_TO_RCVR_N CODEC_TO_RCVR_P COMPASS_TO_OSCAR_INT COMPASS_TO_OSCAR_INT_FL FI CUMULUS_TO_PROX_RX_EN_LV8 CUMULUS_TO_PROX_RX_EN_LV8_CONN CUMULUS_TO_PROX_TX_EN_LV8_L CUMULUS_TO_PROX_TX_EN_BUFF CUMULUS_TO_SAGE_BOOST_EN CUMULUS_TO_SAGE_GCM_SEL CUMULUS_TO_SAGE_VSTM_OUT<0> CUMULUS_TO_SAGE_VSTM_OUT<1> CUMULUS_TO_SAGE_VSTM_OUT<2> CUMULUS_TO_SAGE_VSTM_OUT<3> CUMULUS_TO_SAGE_VSTM_OUT<4> CUMULUS_TO_SAGE_VSTM_OUT<5> CUMULUS_TO_SAGE_VSTM_OUT<6> CUMULUS_TO_SAGE_VSTM_OUT<7> CUMULUS_TO_SAGE_VSTM_OUT<8> CUMULUS_TO_SAGE_VSTM_OUT<9> CUMULUS_TO_SAGE_VSTM_OUT<10> CUMULUS_TO_SAGE_VSTM_OUT<11> CUMULUS_TO_SAGE_VSTM_OUT<12> CUMULUS_TO_SAGE_VSTM_OUT<13> CUMULUS_TO_SAGE_VSTM_OUT<14> CUMULUS_TO_SAGE_VSTM_OUT<15> CUMULUS_TO_SAGE_VSTM_OUT<16> CUMULUS_TO_SAGE_VSTM_OUT<17> CUMULUS_TO_SAGE_VSTM_OUT<18> CUMULUS_TO_SAGE_VSTM_OUT<19> CUMULUS_TO_SAGE_VSTM_OUT<20> CUMULUS_TO_SAGE_VSTM_OUT<21> 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D	PMU_TO_BB_RST_R_L PMU_TO_BB_VBUS_DET PMU_TO_BT_REG_ON PMU_TO_BT_REG_ON_R PMU_TO_TP_AMUX_AY PMU_TO_TP_AMUX_BY PMU_TO_WLAN_REG_ON PMU_TO_WLAN_REG_ON_R PP5V7_LCM_AVDDN PP5V7_SAGE_AVDDN PP5V7_SAGE_AVDDN_INT PPIV0 PPIV0_SOC PPIV0_SRAM PPIV1_CPU PPIV1_GPU PPIV2 PPIV2_NAND_VDDI PPIV2_OSCAR PPIV2_OSCAR_VDDC PPIV2_RCAM_CONN PPIV2_RCAM_SWITCHOUT PPIV2_SDRAM PPIV8 PPIV8_ALWAYS PPIV8_COMP PPIV8_CUMULUS_VDDLDO PPIV8_FCAM_CONN PPIV8_GRAPE PPIV8_LCM_CONN PPIV8_MESA_LOAD_SW_0 UT PPIV8_OSCAR PPIV8_OSCAR_VDDIO PPIV8_PLL PPIV8_RCAM_CONN PPIV8_SDRAM PP_WL_BT_VDDIO_AP - @single_brd_lib.RADIO_MLB(1616_page 24) PPIV8_SDRAM_DOCK_CON N PPIV8_VA_L19_L67 PPIV8_XTAL PP2V5_RCAM_AF PP2V5_RCAM_AF_COMP PP2V5_RCAM_AF_CONN PP2V80_THROTTLER PP2V80_THROTTLER_FB PP2V85_CAM_LDO_OUT PP2V85_FCAM_CONN PP2V85_RCAM_CONN PP3V0_ACC PP3V0_ALS PP3V0_COMP PP3V0_IMU PP3V0_NAND PP3V0_NAVAJAO PP3V0_NAVAJAO_CONN PP3V0_PROXY @single_brd_lib.SINGLE_BRD	24) PMU_TO_BB_RST_R_L - @single_brd_lib.SINGLE_BRD PMU_TO_BB_VBUS_DET - @single_brd_lib.SINGLE_BRD BB_USB_VBUS - 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@single_brd_lib.RADIO_MLB(1616_page 24) PP_BATT_VCC_L19_VP @single_brd_lib.SINGLE_BRD PP_BUCK0_LX0 @single_brd_lib.SINGLE_BRD PP_BUCK0_LX1 @single_brd_lib.SINGLE_BRD PP_BUCK0_LX2 @single_brd_lib.SINGLE_BRD PP_BUCK0_LX3 @single_brd_lib.SINGLE_BRD PP_BUCK1_LX0 @single_brd_lib.SINGLE_BRD PP_BUCK1_LX1 @single_brd_lib.SINGLE_BRD PP_BUCK2_LX @single_brd_lib.SINGLE_BRD PP_BUCK3_LX @single_brd_lib.SINGLE_BRD PP_BUCK4_LX @single_brd_lib.SINGLE_BRD PP_BUCK5_LX @single_brd_lib.SINGLE_BRD PP_CHESTNUT_CN @single_brd_lib.SINGLE_BRD PP_CHESTNUT_CP @single_brd_lib.SINGLE_BRD PP_CHESTNUT_LXP @single_brd_lib.SINGLE_BRD PP_CODEC_FLIT+ @single_brd_lib.SINGLE_BRD PP_CODEC_SPKR_VQ @single_brd_lib.SINGLE_BRD PP_CODEC_MIC1_BIAS @single_brd_lib.SINGLE_BRD PP_CODEC_MIC1_BIAS_CONN - S_CONN @single_brd_lib.SINGLE_BRD PP_CODEC_MIC2_3_BIAS @single_brd_lib.SINGLE_BRD PP_CODEC_MIC3_BIAS_CONN - S_CONN @single_brd_lib.SINGLE_BRD PP_CODEC_VCPFILT+ @single_brd_lib.SINGLE_BRD PP_CODEC_VCVPFILT- @single_brd_lib.SINGLE_BRD PP_CODEC_VHP_PLYC @single_brd_lib.SINGLE_BRD PP_CODEC_VHP_PLYN @single_brd_lib.SINGLE_BRD PP_CODEC_VHP_PLYP @single_brd_lib.SINGLE_BRD PP_CUMULUS_VDDANA @single_brd_lib.SINGLE_BRD PP_CUMULUS_VDDCORE @single_brd_lib.SINGLE_BRD PP_E75_TO_TRISTAR_ACCL C1 @single_brd_lib.SINGLE_BRD PP_E75_TO_TRISTAR_ACCL C1_CONN @single_brd_lib.SINGLE_BRD PP_E75_TO_TRISTAR_ACCL C2 @single_brd_lib.SINGLE_BRD PP_E75_TO_TRISTAR_ACCL C2_CONN @single_brd_lib.SINGLE_BRD PP_EXTMIC_BIAS @single_brd_lib.SINGLE_BRD PP_EXTMIC_BIAS_FLIT @single_brd_lib.SINGLE_BRD PP_EXTMIC_BIAS_FLIT_IN @single_brd_lib.SINGLE_BRD PP_EXTMIC_BIAS_IN @single_brd_lib.SINGLE_BRD PP_L19_VBOOST @single_brd_lib.SINGLE_BRD PP_LCM_BL_ANODE @single_brd_lib.SINGLE_BRD PP_LCM_BL_ANODE_CONN @single_brd_lib.SINGLE_BRD PP_LCM_BL_CAT1 @single_brd_lib.SINGLE_BRD PP_LCM_BL_CAT1_CONN @single_brd_lib.SINGLE_BRD PP_LCM_BL_CAT2 @single_brd_lib.SINGLE_BRD PP_LCM_BL_CAT2_CONN @single_brd_lib.SINGLE_BRD PP_LD014_2P65 @single_brd_lib.SINGLE_BRD PP_LD014_2P65 - @single_brd_lib.SINGLE_BRD PP_LD014_2V65 - @single_brd_lib.RADIO_MLB(1616_page 53D3) PP_LED_BOOST_OUT @single_brd_lib.SINGLE_BRD PP_LED_DRV_LX @single_brd_lib.SINGLE_BRD	11B8 11C8 12A2 23C3 11A2 12A2 23C3 12A2 14B3 16D6 23C3 2C3 12B2 23B3 17A6 22D5 23B3 12D8 16D1 17A8 23B3 14C3 18D7 23B3 14C3 19D2 23B3 19C5 23B3 14C3 18B4 18D3 23B4 14C4 23B4 14B1 17D7 23C5 17C5 23C5 12C8 15B7 15D7 22C4 22D4 22D4 22D5 22D5 22D8 23B4 24D6 35D1 35D8 36C8 44C6 45C6 46C6 47C6 48C6 49C6 50C8 15D6 23B4 12D5 23B4 12D5 23B4 12C5 23			

8			7			6			5			4			3			2			1				
D	U12_GPIO_3	U12_GPIO_3 - @single_brd.lib.SINGLE_BRD	18B6	50_B8_TX_PAD_IN	50_B8_TX_PAD_IN - @single_brd.lib.RADIO_MLB	47C6	50_XCVR_B2_B25_TX	50_XCVR_B2_B25_TX - @single_brd.lib.RADIO_MLB	40D2 42C6	BB_HSI1C1_REMOTE_WAKE	BB_TO_AP_HSI1C1_REMOTE_WAKE - @single_brd.lib.SINGLE_BRD	3C2 24B6	D	U160_IN_NEG	U160_IN_NEG - @single_brd.lib.SINGLE_BRD	16A4	50_B8_TX_SAW_OUT	50_B8_TX_SAW_OUT - @single_brd.lib.RADIO_MLB	43C2 47B7	50_XCVR_B3_B4_TX	50_XCVR_B3_B4_TX - @single_brd.lib.RADIO_MLB	40D2 42D6	BB_I2S_CLK	BB_I2S_CLK - @single_brd.lib.RADIO_MLB	35C8 39B2
	U160_IN_POS	U160_IN_POS - @single_brd.lib.SINGLE_BRD	16A4	50_B13_ANT	50_B13_ANT - @single_brd.lib.RADIO_MLB	48C1 50B7	50_XCVR_B5_B18_TX	50_XCVR_B5_B18_TX - @single_brd.lib.RADIO_MLB	40D2 43C4	BB_I2S_RXD	BB_I2S_RXD - @single_brd.lib.SINGLE_BRD	3C4 24C6		U160_REF	U160_REF - @single_brd.lib.SINGLE_BRD	16A3 16A5	50_B13_DPLX_ANT	50_B13_DPLX_ANT - @single_brd.lib.RADIO_MLB	48C3	50_XCVR_B8_TX	50_XCVR_B8_TX - @single_brd.lib.RADIO_MLB	40D2 43C4	BB_I2S_TXD	BB_I2S_TXD - @single_brd.lib.RADIO_MLB	35B5 35C8 39B4
	USB_CONN_SNUB	USB_CONN_SNUB - @single_brd.lib.SINGLE_BRD	17A6	50_B13_LPF_IN	50_B13_LPF_IN - @single_brd.lib.RADIO_MLB	48C3	50_XCVR_B13_B17_B20_TX	50_XCVR_B13_B17_B20_TX - @single_brd.lib.RADIO_MLB	40D2 43C4	BB_I2S_WS	BB_I2S_WS - @single_brd.lib.SINGLE_BRD	3C4 24C6		USB_REXT	USB_REXT - @single_brd.lib.SINGLE_BRD	2B4	50_B13_TX_PAD_IN	50_B13_TX_PAD_IN - @single_brd.lib.RADIO_MLB	48C6	90_BB_USB_D_N	90_TRISTAR_B1_BB_USB_N - @single_brd.lib.SINGLE_BRD	16C4 24C6	BB_I2S_RXD	BB_I2S_RXD - @single_brd.lib.RADIO_MLB	35B5 35C8 39B4
	USB_VBUS_DETECT	USB_VBUS_DETECT - @single_brd.lib.SINGLE_BRD	2B3 12D8 23A7	50_B13_TX_SAW_OUT	50_B13_TX_SAW_OUT - @single_brd.lib.RADIO_MLB	43C2 48C7	90_BB_USB_D_P	90_TRISTAR_B1_BB_USB_P - @single_brd.lib.SINGLE_BRD	35C3 35C8 38A5	BB_I2S_TXD	BB_I2S_TXD - @single_brd.lib.SINGLE_BRD	3C4 24C6		WLAN_TO_AP_HSI1C2_RDY	WLAN_TO_AP_HSI1C2_RDY - @single_brd.lib.SINGLE_BRD	3C2 24B6	50_B17_ANT	50_B17_ANT - @single_brd.lib.RADIO_MLB	48B2 50B7	100_BB_USB_D_P	90_BB_USB_D_P - @single_brd.lib.RADIO_MLB	16D4 24C6	BB_I2S_WS	BB_I2S_WS - @single_brd.lib.RADIO_MLB	35B5 35C8 39B4
	WLAN_TO_AP_HSI1C2_REM_OTX_WAKE	WLAN_TO_AP_HSI1C2_REM_OTX_WAKE - @single_brd.lib.SINGLE_BRD	35C6 35C8 56B3	50_B17_DPLX_ANT	50_B17_DPLX_ANT - @single_brd.lib.RADIO_MLB	48B3	100_B1_B4_DUPLX_RX_N	100_B1_B4_DUPLX_RX_N - @single_brd.lib.RADIO_MLB	35C3 35C8 38A5	BB_IPC_GPIO	BB_TO_AP_IPC_GPIO - @single_brd.lib.SINGLE_BRD	3C4 24C6		WLAN_TO_AP_HSI1C2_REM_OTX_WAKE	WLAN_TO_AP_HSI1C2_REM_OTX_WAKE - @single_brd.lib.SINGLE_BRD	35C6 35D8 56B3	50_B17_TX_PAD_IN	50_B17_TX_PAD_IN - @single_brd.lib.RADIO_MLB	48C6	100_B1_B4_DUPLX_RX_P	100_B1_B4_DUPLX_RX_P - @single_brd.lib.RADIO_MLB	42B4 44B5	BB_I2S_WS	BB_I2S_WS - @single_brd.lib.SINGLE_BRD	35B5 35C8 39B4
	WLAN_TO_AP_HSI1C2_REM_OTX_WAKE	WLAN_TO_AP_HSI1C2_REM_OTX_WAKE - @single_brd.lib.SINGLE_BRD	35C6 35D8 56B3	50_B17_TX_SAW_OUT	50_B17_TX_SAW_OUT - @single_brd.lib.RADIO_MLB	43C2 48B7	100_B5_B18_DUPLX_RX_N	100_B5_B18_DUPLX_RX_N - @single_brd.lib.RADIO_MLB	42B4 47B5	BB_JTAG_RTCLK	BB_TO_AP_JTAG_RTCLK - @single_brd.lib.SINGLE_BRD	35C3 38B3		WLAN_TO_AP_UART3_RXD	WLAN_TO_AP_UART3_RXD - @single_brd.lib.SINGLE_BRD	3C5 24C6	50_B20_ANT	50_B20_ANT - @single_brd.lib.RADIO_MLB	46C2 50B7	100_B5_B18_DUPLX_RX_P	100_B5_B18_DUPLX_RX_P - @single_brd.lib.RADIO_MLB	42B4 47B5	BB_JTAG_TCK	BB_TO_AP_JTAG_TCK - @single_brd.lib.SINGLE_BRD	3B7 24D3
	WLAN_TO_AP_UART3_RXD	WLAN_TO_AP_UART3_RXD - @single_brd.lib.SINGLE_BRD	35C8 56B3	50_B20_DPLX_ANT	50_B20_DPLX_ANT - @single_brd.lib.RADIO_MLB	46C3	100_B8_DUPLX_RX_N	100_B8_DUPLX_RX_N - @single_brd.lib.RADIO_MLB	42B4 47B5	BB_JTAG_TDI	BB_TO_AP_JTAG_TDI - @single_brd.lib.SINGLE_BRD	3C7 24D3		WLAN_UART_TXD	WLAN_UART_TXD - @single_brd.lib.SINGLE_BRD	35C8 56B3	50_B20_TX_PAD_IN	50_B20_TX_PAD_IN - @single_brd.lib.RADIO_MLB	46C6	100_B8_DUPLX_RX_P	100_B8_DUPLX_RX_P - @single_brd.lib.RADIO_MLB	42B4 47B5	BB_JTAG_TDO	BB_TO_AP_JTAG_TDO - @single_brd.lib.SINGLE_BRD	35B8 35C3 38B3
	WLAN_UART_TXD	WLAN_UART_TXD - @single_brd.lib.SINGLE_BRD	35C8 56B3	50_B20_TX_SAW_OUT	50_B20_TX_SAW_OUT - @single_brd.lib.RADIO_MLB	43C2 46C7	100_B13_DUPLX_RX_N	100_B13_DUPLX_RX_N - @single_brd.lib.RADIO_MLB	43C8 44B5	BB_JTAG_TMS	BB_TO_AP_JTAG_TMS - @single_brd.lib.SINGLE_BRD	38B7 24D3		WLAN_TO_PMU_HOST_WAKE	WLAN_TO_PMU_HOST_WAKE - @single_brd.lib.SINGLE_BRD	13B4 24C6	50_COUPLER_TERM	50_COUPLER_TERM - @single_brd.lib.RADIO_MLB	50B2	100_B13_DUPLX_RX_P	100_B13_DUPLX_RX_P - @single_brd.lib.RADIO_MLB	43C8 44B5	BB_JTAG_TMS	BB_TO_AP_JTAG_TMS - @single_brd.lib.SINGLE_BRD	35B8 35C3 38B5
	WLAN_TO_PMU_HOST_WAKE	WLAN_TO_PMU_HOST_WAKE - @single_brd.lib.SINGLE_BRD	35C8 56B3	50_DCS_RX	50_DCS_RX - @single_brd.lib.RADIO_MLB	43B5 50B7	100_B17_DUPLX_RX_N	100_B17_DUPLX_RX_N - @single_brd.lib.RADIO_MLB	43C8 44B5	BB_JTAG_TRST_L	BB_TO_AP_JTAG_TRST_L - @single_brd.lib.SINGLE_BRD	3D5 24D3		E			50_DCS_RX_MATCH	50_DCS_RX_MATCH - @single_brd.lib.RADIO_MLB	43B5	100_B17_DUPLX_RX_P	100_B17_DUPLX_RX_P - @single_brd.lib.RADIO_MLB	43C8 44B5	BB_RST_L	BB_TO_BB_RST_L - @single_brd.lib.SINGLE_BRD	3C8 24D6
				50_DIVERSITY_SWITCH_MATCH	50_DIVERSITY_SWITCH_MATCH - @single_brd.lib.RADIO_MLB	51C6	100_B20_DUPLX_RX_N	100_B20_DUPLX_RX_N - @single_brd.lib.RADIO_MLB	4388 46B5	BB_SPI_TO_PAC_CLK	BB_TO_ANTENNA_PAC_SPI_CLK - @single_brd.lib.SINGLE_BRD	24A6					50_HSI1C1_DATA	50_HSI1C1_DATA - @single_brd.lib.SINGLE_BRD	50B2	100_XCVR_B2_B25_PRX_N	100_XCVR_B2_B25_PRX_N - @single_brd.lib.RADIO_MLB	40D8 42B6	BB_SPI_TO_PAC_DATA_M	BB_TO_ANTENNA_PAC_SPI_DATA_M - @single_brd.lib.SINGLE_BRD	35B8 39C4 53C7
C	Base nets and synonyms for single_brd.lib.RADIO_MLB	Base nets and synonyms for single_brd.lib.RADIO_MLB		50_DRX_ANT	50_DRX_ANT - @single_brd.lib.RADIO_MLB	50C4 51C7	100_XCVR_B1_B4_DRX_N	100_XCVR_B1_B4_DRX_N - @single_brd.lib.RADIO_MLB	40C8 51C3	BB_SPI_TO_PAC_CLK_FILT	BB_TO_ANTENNA_PAC_SPI_CLK_FILT - @single_brd.lib.SINGLE_BRD	53C6	C	2G_FEM_S0	2G_FEM_S0 - @single_brd.lib.RADIO_MLB	39C2 50C3	50_GPS_ANT	50_GPS_ANT - @single_brd.lib.RADIO_MLB	52B7	100_XCVR_B1_B4_PRX_N	100_XCVR_B1_B4_PRX_N - @single_brd.lib.RADIO_MLB	40C8 42C2	BB_RST_L	BB_TO_BB_RST_L - @single_brd.lib.SINGLE_BRD	3C8 24D6
	Base Signal	Synonyms	Location([Zone][dir])	50_GPS_ANT	50_GPS_ANT - @single_brd.lib.RADIO_MLB	52B7	100_XCVR_B1_B4_PRX_P	100_XCVR_B1_B4_PRX_P - @single_brd.lib.RADIO_MLB	40C8 42C2	BB_SPI_TO_PAC_CS	BB_TO_ANTENNA_PAC_SPI_CS - @single_brd.lib.SINGLE_BRD	24A6		2G_FEM_S1	2G_FEM_S1 - @single_brd.lib.RADIO_MLB	35C1 39C2 50C3	50_GPS_LNA_MATCH	50_GPS_LNA_MATCH - @single_brd.lib.RADIO_MLB	52B6	100_XCVR_B2_B25_PRX_N	100_XCVR_B2_B25_PRX_N - @single_brd.lib.RADIO_MLB	40D8 42B6	BB_SPI_TO_PAC_CS_FILT	BB_TO_ANTENNA_PAC_SPI_CS_FILT - @single_brd.lib.SINGLE_BRD	53C6
				50_GPS_ANT_FEED	50_GPS_ANT_FEED - @single_brd.lib.RADIO_MLB	52B5	100_XCVR_B2_B25_PRX_P	100_XCVR_B2_B25_PRX_P - @single_brd.lib.RADIO_MLB	40D8 42B6	BB_SPI_TO_PAC_DATA_M	BB_TO_ANTENNA_PAC_SPI_DATA_M - @single_brd.lib.SINGLE_BRD	24A6		2G_FEM_S2	2G_FEM_S2 - @single_brd.lib.RADIO_MLB	39C2 43C8 50C3	50_GPS_LNA_OUT	50_GPS_LNA_OUT - @single_brd.lib.RADIO_MLB	51C5 52B3	100_XCVR_B3_PRX_N	100_XCVR_B3_PRX_N - @single_brd.lib.RADIO_MLB	40D8 42A2	BB_UART_CTS_L	BB_TO_BB_UART4_CTS_L - @single_brd.lib.SINGLE_BRD	35C3 35C8 39C4
				50_GPS_LNA_MATCH	50_GPS_LNA_MATCH - @single_brd.lib.RADIO_MLB	52B6	100_XCVR_B3_PRX_P	100_XCVR_B3_PRX_P - @single_brd.lib.RADIO_MLB	40D8 42A2	BB_UART_RXTX	BB_TO_BB_UART4_RXTX - @single_brd.lib.SINGLE_BRD	35C3 35C8 39C4		2G_FEM_S3	2G_FEM_S3 - @single_brd.lib.RADIO_MLB	39C2 43C8 50C3	50_HSI1C1_DATA	50_HSI1C1_DATA - @single_brd.lib.SINGLE_BRD	52B6 24B6	100_XCVR_B5_B18_B13_PRX_N	100_XCVR_B5_B18_B13_PRX_N - @single_brd.lib.RADIO_MLB	40C8 51B3	BB_UART_TXD	BB_TO_BB_UART4_TXD - @single_brd.lib.SINGLE_BRD	35C3 35C8 39C4
				50_GPS_LNA_OUT	50_GPS_LNA_OUT - @single_brd.lib.RADIO_MLB	51C5 52B3	100_XCVR_B5_B18_B13_PRX_P	100_XCVR_B5_B18_B13_PRX_P - @single_brd.lib.RADIO_MLB	40C8 51B3	BB_UART_TXD	BB_TO_BB_UART4_TXD - @single_brd.lib.SINGLE_BRD	35C3 35C8 39C4		2G_FEM_S4	2G_FEM_S4 - @single_brd.lib.RADIO_MLB	35C3 39C2 50C3	50_HSI1C2_DATA	50_HSI1C2_DATA - @single_brd.lib.SINGLE_BRD	2B6 24B6	100_XCVR_B5_B18_B13_DRX_N	100_XCVR_B5_B18_B13_DRX_N - @single_brd.lib.RADIO_MLB	40C8 51B3	BB_UART_TXD	BB_TO_BB_UART4_TXD - @single_brd.lib.SINGLE_BRD	35C3 35C8 39C4
				50_HSI1C1_DATA	50_HSI1C1_DATA - @single_brd.lib.SINGLE_BRD	2B6 24B6	100_XCVR_B5_B18_B13_DRX_P	100_XCVR_B5_B18_B13_DRX_P - @single_brd.lib.RADIO_MLB	40C8 51B3	BB_UART_TXD	BB_TO_BB_UART4_TXD - @single_brd.lib.SINGLE_BRD	35C3 35C8 39C4		2G_FEM_S5	2G_FEM_S5 - @single_brd.lib.RADIO_MLB	39C2 50C3	50_HSI1C2_STB	50_HSI1C2_STB - @single_brd.lib.SINGLE_BRD	35B1 35B6 35C8 38B3	100_XCVR_B5_B18_PRX_N	100_XCVR_B5_B18_PRX_N - @single_brd.lib.RADIO_MLB	40D8 42B2	BB_UART_TXD	BB_TO_BB_UART4_TXD - @single_brd.lib.SINGLE_BRD	35C3 35C8 39C4
				50_HSI1C2_DATA	50_HSI1C2_DATA - @single_brd.lib.SINGLE_BRD	2B6 24B6	100_XCVR_B5_B18_PRX_P	100_XCVR_B5_B18_PRX_P - @single_brd.lib.RADIO_MLB	40D8 42B2	BB_UART_TXD	BB_TO_BB_UART4_TXD - @single_brd.lib.SINGLE_BRD	35C3 35C8 39C4		2G_FEM_S6	2G_FEM_S6 - @single_brd.lib.RADIO_MLB	39C2 43C8 50C3	50_HSI1C2_STB	50_HSI1C2_STB - @single_brd.lib.SINGLE_BRD	2B6 24B6	100_XCVR_B5_B18_PRX_P	100_XCVR_B5_B18_PRX_P - @single_brd.lib.RADIO_MLB	40D8 42B2	BB_UART_TXD	BB_TO_BB_UART4_TXD - @single_brd.lib.SINGLE_BRD	35C3 35C8 39C4
				50_HSI1C2_STB	50_HSI1C2_STB - @single_brd.lib.SINGLE_BRD	2B6 24B6	100_XCVR_B5CVR																		

8				7				6				5				4				3				2				1			
D	BT_UART_TXD	@single_brd_lib.SINGLE_BRD						PP_BATT_VCC_CONN -		35D1 35D8 36C8 44C6 45C6						@single_brd_lib.RADIO_MLB															
		BT_UART_RXD -						@single_brd_lib.RADIO_MLB		46C6 47C6 48C6 49C6 50C8						PP_RF2_2V05_TX_PLL -		41B1 41C1													
		@single_brd_lib.RADIO_MLB						PP_BATT_VCC_PA_DCCDC -								@single_brd_lib.RADIO_MLB															
		BT_TO_AP_UART1_RXD -						@single_brd_lib.RADIO_MLB								PP_RF2_2V05_TX_VCO -		41B1 41C1													
		@single_brd_lib.SINGLE_BRD						PP_BATT_VCC_MLAN								@single_brd_lib.RADIO_MLB															
		BT_UART_TXD -						@single_brd_lib.RADIO_MLB								PP_RF2_2V05_XO_FILT -		41B1 41C1													
		BT_WAKE						PP_LD01		36B2						@single_brd_lib.RADIO_MLB															
		AP_TO_BT_WAKE						PP_LD02_XO_HS_1V8		36B1 38B5						PP_SMPF4_RF2_2V05_FILT		41D3													
		@single_brd_lib.SINGLE_BRD						@single_brd_lib.RADIO_MLB							@single_brd_lib.RADIO_MLB																
		BT_WAKE -						PP_LD03_AMUX_1V8		36B1 37B5 37D4 38B6						@single_brd_lib.RADIO_MLB															
C	CLK32K_AP	45_PMU_TO_MLAN_CLK32K -						@single_brd_lib.RADIO_MLB								@single_brd_lib.RADIO_MLB															
		@single_brd_lib.SINGLE_BRD						PP_LD04_VDDA_3V3 -		36B1 38B6						PP_RF2_2V05_TX_VCO -		41B1 41C1													
		CLK32K_AP -						@single_brd_lib.RADIO_MLB							@single_brd_lib.RADIO_MLB																
		@single_brd_lib.RADIO_MLB						PP_LD05_GPS_LNA_2V5 -		36B1 52C4						@single_brd_lib.RADIO_MLB															
		DCCDC_ADJ						@single_brd_lib.RADIO_MLB							@single_brd_lib.RADIO_MLB																
		DCCDC_ADJ -						PP_LD06_RUIM_1V8		35A4 35A6 35A8 35D1 36B1						PP_RF2_2V05_TX_DA -		41B1 41D1													
		DCCDC_EN						@single_brd_lib.RADIO_MLB		38A6						@single_brd_lib.RADIO_MLB															
		DCCDC_MODE						PP_LD07_DAC_1V8		36B1 38A6						PP_RF2_2V05_TX_BB		41B1 41D1													
		DCCDC_OUT						@single_brd_lib.RADIO_MLB							@single_brd_lib.RADIO_MLB																
		DCCDC_OUT -						PP_LD08_VDDPX_1V2		36B1 38A6						PP_RF2_2V05_SHDR_VCO -		41A4 41C1													
B	DCCDC_PGND	@single_brd_lib.RADIO_MLB						@single_brd_lib.RADIO_MLB							@single_brd_lib.RADIO_MLB																
		DCCDC_PGND -						PP_LD09_PLL_1V05		36B1 38B6 38B8 38D8						PP_RF2_2V05_PRX_VCO -		41B4 41C1													
		@single_brd_lib.RADIO_MLB						@single_brd_lib.RADIO_MLB							PP_RF2_2V05_PRX_BB -		41B4 41D1														
		DEBUG_RST_L						PP_LD010_ADSF_1V05		36B1 38C6 38D7						PP_SMPF1_MSMC_1V05															
		@single_brd_lib.RADIO_MLB						@single_brd_lib.RADIO_MLB							@single_brd_lib.RADIO_MLB																
		DRX_ASM_V1						PP_LD011_MDSP_FW_1V0		36B1 38C6 38D6						@single_brd_lib.RADIO_MLB															
		DRX_ASM_V1 -						5							PP_SMPF2_RF1_1V3																
		@single_brd_lib.RADIO_MLB						@single_brd_lib.RADIO_MLB							@single_brd_lib.RADIO_MLB																
		DRX_ASM_V2						PP_LD012_MDSP_SW_1V0		36B1 38B6 38D7						@single_brd_lib.RADIO_MLB															
		DRX_ASM_V2 -						5							@single_brd_lib.RADIO_MLB																
A	DRX_ASM_V3	@single_brd_lib.RADIO_MLB						@single_brd_lib.RADIO_MLB							PP_SMPF4_RF2_2V05																
		DRX_ASM_V3 -						PP_LD013_VDDPX_2V95		36B1 38A8						@single_brd_lib.RADIO_MLB															
		@single_brd_lib.RADIO_MLB						@single_brd_lib.RADIO_MLB							@single_brd_lib.RADIO_MLB																
		DRX_ASM_V4						PP_LD014_2P65 -		17A7 23C2 24D6						PP_SMPF5_DSP_1V05															
		@single_brd_lib.RADIO_MLB						@single_brd_lib.RADIO_MLB							@single_brd_lib.RADIO_MLB																
		DRX_BB_I_N						@single_brd_lib.SINGLE_BRD							PP_SPI_NOR_1V8																
		DRX_BB_I_N -						PP_LD014_2V65		35B8 36B1 43C8 50C8 51C6						PP_SPI_NOR_1V8 -															
		@single_brd_lib.RADIO_MLB						@single_brd_lib.RADIO_MLB							PP_SYNC																
		DRX_BB_I_P						PP_LD014_PAC_2V65		53D3						BB_TO_AP_PP_SYNC -															
		DRX_BB_I_P -						@single_brd_lib.RADIO_MLB		53D4					@single_brd_lib.SINGLE_BRD																
C	DRX_BB_Q_N	@single_brd_lib.RADIO_MLB						@single_brd_lib.RADIO_MLB							PP_SYNC																
		DRX_BB_Q_N -						PP_LVSI		35A8 36D1 38B6					@single_brd_lib.RADIO_MLB																
		@single_brd_lib.RADIO_MLB						PP_PA		44C6 45C6 46C6 47C6 48C6					PP_VCC_MAIN																
		DRX_BB_Q_P						@single_brd_lib.RADIO_MLB		41B4 41B5					@single_brd_lib.SINGLE_BRD																
		@single_brd_lib.RADIO_MLB						PP_RF1_1V3_DRX_FE		41A5 41B4					PP_VREG																
		EBI1_CAL						@single_brd_lib.RADIO_MLB							PP_VSW_S1																
		@single_brd_lib.RADIO_MLB						PP_RF1_1V3_DRX_MBL0		41A5 41B4					@single_brd_lib.RADIO_MLB																
		GPIO_6						@single_brd_lib.RADIO_MLB							PP_VSW_S2																
		GPIO_51						PP_RF1_1V3_DRX_MBL0		41A1 41D3					PP_VSW_S3																
		GPIO_DEBUG_LED						@single_brd_lib.RADIO_MLB		41A1 41D3					PP_VSW_S4																
B	GPS_BB_I_N	@single_brd_lib.RADIO_MLB						PP_RF1_1V3_GPS_DIG		41A1 41D3					PP_VSW_S5																
		GPS_BB_I_N -						@single_brd_lib.RADIO_MLB							PP_VSW_S5 -																
		@single_brd_lib.RADIO_MLB			</																										

8				7				6				5				4				3				2				1			
Title: Cref Part Report Design: single_brd Date: Feb 6 20:18:49 2013				BS1 PCB_STANDOFF single_brd[22A5] BS2 PCB_STANDOFF single_brd[22A5] C1 CAP_01005 single_brd[287]				C1_RF SUPPR_TRANSIENT_2P1_ radio_mlb[35A4]single_brd[24] 01005-1				C2 CAP_0201 single_brd[2C5] C2_RF CAP_0201-1 radio_mlb[36B4]single_brd[24] C3 CAP_0204 single_brd[6D3] C3_RF CAP_0201-1 radio_mlb[36B4]single_brd[24] C4 CAP_01005 single_brd[17C6] C4_RF CAP_0201-1 radio_mlb[36B4]single_brd[24] C5 CAP_01005 single_brd[17C6] C5_RF CAP_0201-1 radio_mlb[36A3]single_brd[24] C6 CAP_01005 single_brd[7C5] C6_RF CAP_0201-1 radio_mlb[36B3]single_brd[24] C7 CAP_01005 single_brd[7C5] C7_RF CAP_0201-1 radio_mlb[36A3]single_brd[24] C8 CAP_01005 single_brd[17C6] C8_RF CAP_0402 radio_mlb[36B3]single_brd[24] C9 CAP_01005 single_brd[22D6] C9_RF CAP_0402 radio_mlb[36A3]single_brd[24] C10 CAP_01005 single_brd[12A5] C10_RF CAP_0402 radio_mlb[36B3]single_brd[24] C11 CAP_0201 single_brd[20C1] C11_RF CAP_0402 radio_mlb[36A2]single_brd[24] C12 CAP_01005 single_brd[17A6] C12_RF CAP_0201-1 radio_mlb[36D2]single_brd[24] C13 CAP_01005 single_brd[17A6] C13_RF CAP_0402 radio_mlb[36B2]single_brd[24] C14 CAP_0402-1 single_brd[14A2] C14_RF CAP_0402 radio_mlb[38D8]single_brd[24] C15_RF CAP_0201-1 radio_mlb[38D8]single_brd[24] C16 CAP_402 single_brd[12A3] C16_RF CAP_0201-1 radio_mlb[38D8]single_brd[24] C17 CAP_01005 single_brd[19B4] C17_RF CAP_0201-1 radio_mlb[38D8]single_brd[24] C18 CAP_01005 single_brd[19A4] C18_RF CAP_0201-1 radio_mlb[38D7]single_brd[24] C19 CAP_01005 single_brd[19A4] C19_RF CAP_0201-1 radio_mlb[38D7]single_brd[24] C20 CAP_01005 single_brd[2C5] C20_RF CAP_0201-1 radio_mlb[38D7]single_brd[24] C21 CAP_01005 single_brd[2C5] C21_RF CAP_0201-1 radio_mlb[38D7]single_brd[24] C22 CAP_01005 single_brd[22D6] C22_RF CAP_0201-1 radio_mlb[38D7]single_brd[24] C23 CAP_01005 single_brd[22D8] C23_RF CAP_0201-1 radio_mlb[38D7]single_brd[24] C24 CAP_402 single_brd[12A3] C24_RF CAP_0201-1 radio_mlb[38D7]single_brd[24] C25 CAP_01005 single_brd[22D7] C25_RF CAP_0201-1 radio_mlb[38D7]single_brd[24] C26 CAP_01005 single_brd[10B7] C26_RF CAP_0201-1 radio_mlb[38D7]single_brd[24] C27 CAP_01005 single_brd[10A7] C27_RF CAP_0201-1 radio_mlb[38D6]single_brd[24] C28 CAP_0402-1 single_brd[14B1] C28_RF CAP_0201-1 radio_mlb[38D6]single_brd[24] C29 CAP_01005 single_brd[15C4] C29_RF CAP_0201-1 radio_mlb[38D6]single_brd[24] C30 CAP_01005 single_brd[5B5] C30_RF CAP_0201-1 radio_mlb[38D6]single_brd[24] C31 CAP_01005 single_brd[16B7] C31_RF CAP_01005 radio_mlb[38B6]single_brd[24] C32_RF CAP_0201-1 radio_mlb[38D6]single_brd[24] C33 CAP_01005 single_brd[2C5] C33_RF CAP_0201-1 radio_mlb[38A6]single_brd[24] C34_RF CAP_0201-1 radio_mlb[38D6]single_brd[24] C35 CAP_01005 single_brd[2C5] C35_RF CAP_0201-1 radio_mlb[38D5]single_brd[24] C36 CAP_01005 single_brd[2C2] C36_RF CAP_0201-1 radio_mlb[38D5]single_brd[24] C37 CAP_01005 single_brd[2C2] C37_RF CAP_01005 radio_mlb[56C4]single_brd[24] C38 CAP_0201-1 single_brd[16D5] C38_RF CAP_01005 radio_mlb[53C5]single_brd[24] C39 CAP_01005 single_brd[16D4] C39_RF CAP_0201 radio_mlb[46B3]single_brd[24] C40 CAP_0402 radio_mlb[42C3]single_brd[24] C41 CAP_01005 single_brd[4D8] C41_RF CAP_402 radio_mlb[49B3]single_brd[24] C42_RF CAP_0402 radio_mlb[36C8]single_brd[24] C43 CAP_0204 single_brd[4B8] C43_RF CAP_0402 radio_mlb[36C7]single_brd[24] C44 CAP_01005 single_brd[11A4] C44_RF CAP_0402 radio_mlb[36C7]single_brd[24] C45 CAP_01005 single_brd[8C3] C45_RF CAP_01005 radio_mlb[36C7]single_brd[24] C46_RF CAP_01005 radio_mlb[36B6]single_brd[24] C47 CAP_402 single_brd[14D5] C47_RF CAP_0402 radio_mlb[36B6]single_brd[24] C48 CAP_0204 single_brd[4A8] C48_RF CAP_0402 radio_mlb[36B6]single_brd[24] C49 CAP_0204 single_brd[4C7] C49_RF CAP_0402 radio_mlb[36B6]single_brd[24] C50 CAP_0201 single_brd[6C4] C50_RF CAP_01005 radio_mlb[36C5]single_brd[24] C51 CAP_01005 single_brd[9B2] C51_RF CAP_0402 radio_mlb[36B5]single_brd[24] C52 CAP_0402-1 single_brd[14C4] C52_RF CAP_0201-1 radio_mlb[36A4]single_brd[24] C53 CAP_0204 single_brd[4C8] C53_RF CAP_0201-1 radio_mlb[36A4]single_brd[24] C54 CAP_0402-1 single_brd[14D4] C54_RF CAP_0201-1 radio_mlb[36A4]single_brd[24] C55 CAP_01005 single_brd[9B7] C55_RF CAP_01005 radio_mlb[36D2]single_brd[24] C56 CAP_01005 single_brd[11B7] C56_RF CAP_0603 radio_mlb[36C2]single_brd[24] C57 CAP_0610 single_brd[4B8] C57_RF CAP_0603-3 radio_mlb[36C2]single_brd[24] C58 CAP_0402 single_brd[12C3] C58_RF CAP_0603-3 radio_mlb[36C2]single_brd[24] C59 CAP_0204 single_brd[4C7] C59_RF CAP_0603 radio_mlb[36B2]single_brd[24] C60 CAP_0204 single_brd[4A7] C60_RF CAP_01005 radio_mlb[36C1]single_brd[24] C61 CAP_01005 single_brd[9B7]				C61_RF CAP_01005 radio_mlb[39B7]single_brd[24] C62 CAP_01005 single_brd[11C6] C62_RF CAP_01005 radio_mlb[39C6]single_brd[24] C63 CAP_01005 single_brd[11C6] C63_RF CAP_SM radio_mlb[48C3]single_brd[24] C64 CAP_01005 single_brd[9B6] C64_RF CAP_01005 radio_mlb[50B7]single_brd[24] C65 CAP_01005 single_brd[9B6] C65_RF CAP_01005 radio_mlb[50C7]single_brd[24] C66 CAP_0402 single_brd[12C1] C66_RF CAP_0402 radio_mlb[50C7]single_brd[24] C67 CAP_01005 single_brd[11C3] C67_RF CAP_01005 radio_mlb[50C6]single_brd[24] C68 CAP_0610 single_brd[4C3] C68_RF CAP_0201-1 radio_mlb[38C8]single_brd[24] C69 CAP_0402-1 single_brd[14C4] C69_RF CAP_0201-1 radio_mlb[38A6]single_brd[24] C70 CAP_01005 single_brd[17B4] C70_RF CAP_0201-1 radio_mlb[38A6]single_brd[24] C71 CAP_01005 single_brd[17B4] C71_RF CAP_0201-1 radio_mlb[38B6]single_brd[24] C72 CAP_4P1_0402 single_brd[4C3] C72_RF CAP_0402 radio_mlb[41D7]single_brd[24] C73 CAP_01005 single_brd[15A4] C73_RF CAP_01005 radio_mlb[41D6]single_brd[24] C74 CAP_0402-1 single_brd[20D5] C74_RF CAP_01005 radio_mlb[41D6]single_brd[24] C75 CAP_0402-2 single_brd[4D3] C75_RF CAP_01005 radio_mlb[41C6]single_brd[24] C76_RF CAP_01005 radio_mlb[41C6]single_brd[24] C77_RF CAP_01005 radio_mlb[41C6]single_brd[24] C78 CAP_0204 single_brd[5B5] C78_RF CAP_01005 radio_mlb[41C6]single_brd[24] C79 CAP_01005 single_brd[18C8] C79_RF CAP_01005 radio_mlb[41B6]single_brd[24] C80 CAP_0402-2 single_brd[4D3] C80_RF CAP_01005 radio_mlb[41B6]single_brd[24] C81 CAP_0204 single_brd[5C3] C81_RF CAP_01005 radio_mlb[41B6]single_brd[24] C82 CAP_01005 single_brd[21A6] C82_RF CAP_01005 radio_mlb[41A6]single_brd[24] C83 CAP_4P1_0402 single_brd[4C3] C83_RF CAP_01005 radio_mlb[41A6]single_brd[24] C84 CAP_01005 single_brd[21A5] C85 CAP_01005 single_brd[5A2] C85_RF CAP_01005 radio_mlb[41D5]single_brd[24] C86 CAP_0204 single_brd[5B5] C86_RF CAP_01005 radio_mlb[41D5]single_brd[24] C87 CAP_4P1_0402 single_brd[4C3] C87_RF CAP_0201-1 radio_mlb[41C5]single_brd[24] C88 CAP_01005 single_brd[19C3] C88_RF CAP_0402 radio_mlb[41D3]single_brd[24] C89 CAP_4P1_0402 single_brd[4C3] C89_RF CAP_01005 radio_mlb[41C1]single_brd[24] C90 CAP_01005 single_brd[19C3] C90_RF CAP_01005 radio_mlb[41C1]single_brd[24] C91 CAP_0204 single_brd[5B3] C91_RF CAP_01005 radio_mlb[41C1]single_brd[24] C92 CAP_4P1_0402 single_brd[20D6] C92_RF CAP_01005 radio_mlb[41B1]single_brd[24] C93 CAP_01005 single_brd[19C3] C93_RF CAP_0201 radio_mlb[42B3]single_brd[24] C94 CAP_01005 single_brd[19D3] C95 CAP_0204 single_brd[5B5] C96 CAP_01005 single_brd[12B7] C96_RF CAP_01005 radio_mlb[53C6]single_brd[24] C97 CAP_4P1_0402 single_brd[4B3] C97_RF CAP_01005 radio_mlb[53C5]single_brd[24] C98 CAP_01005 single_brd[17D2] C98_RF CAP_01005 radio_mlb[53C5]single_brd[24] C99 CAP_01005 single_brd[17A5] C99_RF CAP_01005 radio_mlb[53D4]single_brd[24] C100 CAP_4P1_0402 single_brd[4B3] C101 CAP_01005 single_brd[7D3] C101_RF CAP_201 radio_mlb[56D2]single_brd[24] C102 CAP_01005 single_brd[17A5] C102_RF CAP_402 radio_mlb[56B7]single_brd[24] C103 CAP_01005 single_brd[17D3] C103_RF CAP_0402-2 radio_mlb[56C5]single_brd[24] C104 CAP_0201-1 single_brd[12A4] C104_RF CAP_01005 radio_mlb[56C5]single_brd[24] C105 CAP_01005 single_brd[17D3] C105_RF CAP_01005 radio_mlb[56C4]single_brd[24] C106 CAP_01005 single_brd[4A8] C106_RF CAP_0201 radio_mlb[56C2]single_brd[24] C107 CAP_0610 single_brd[4D3] C107_RF RES_201 radio_mlb[56C2]single_brd[24] C108 CAP_01005 single_brd[4A8] C108_RF CAP_01005-1 radio_mlb[48C6]single_brd[24] C109 CAP_0201-1 single_brd[12A4] C109_RF CAP_01005 radio_mlb[48B6]single_brd[24] C110 CAP_01005 single_brd[17D6] C110_RF CAP_01005 radio_mlb[48C6]single_brd[24] C111 CAP_01005 single_brd[4A7] C111_RF CAP_0201-1 radio_mlb[48C5]single_brd[24] C112 CAP_4P1_0402 single_brd[4B3] C112_RF CAP_0201 radio_mlb[50B4]single_brd[24] C113 CAP_01005 single_brd[4A7] C113_RF CAP_0201 radio_mlb[48B3]single_brd[24] C114 CAP_01005 single_brd[4A5] C114_RF CAP_0201 radio_mlb[48B3]single_brd[24] C115 CAP_0204 single_brd[5C2] C115_RF CAP_01005 radio_mlb[48B2]single_brd[24] C116 CAP_01005 single_brd[17D6] C116_RF CAP_01005 radio_mlb[47C7]single_brd[24] C117 CAP_01005 single_brd[4A5] C117_RF CAP_01005 radio_mlb[47B7]single_brd[24] C118 CAP_01005 single_brd[4A4] C118_RF CAP_01005 radio_mlb[47C6]single_brd[24] C119 CAP_01005 single_brd[17D3] C119_RF CAP_0201-1 radio_mlb[47C5]single_brd[24] C120 CAP_01005 single_brd[4A4] C120_RF CAP_01005 radio_mlb[50B3]single_brd[24] C121 CAP_01005-1 single_brd[4B3] C121_RF CAP_01005 radio_mlb[48C2]single_brd[24] C122 CAP_4P1_0402 single_brd[4B3] C122_RF CAP_201 radio_mlb[47B2]single_brd[24] C123 CAP_0402 single_brd[12C4] C123_RF CAP_0201 radio_mlb[47B2]single_brd[24] C124 CAP_4P1_0402 single_brd[4B3] C124_RF CAP_0201 radio_mlb[50B3]single_brd[24] C126 CAP_01005 single_brd[17D3] C127 CAP_01005 single_brd[12C4] C127_RF CAP_01005 radio_mlb[37B4]single_brd[24]				C128 CAP_0402 single_brd[12D4] C128_RF CAP_01005 radio_mlb[40C4]single_brd[24] C130 CAP_01005 single_brd[18C7] C131 CAP_0603 single_brd[14B5] C133 CAP_0610 single_brd[4C3] C134 CAP_0204 single_brd[5B2] C135 CAP_0402 single_brd[12D3] C136 CAP_01005 single_brd[6C6] C137 CAP_201 single_brd[18B4] C138 CAP_01005 single_brd[9B2] C140 CAP_0402 single_brd[12C2] C141 CAP_0610 single_brd[4D5] C142 CAP_01005 single_brd[4D1] C143 CAP_01005 single_brd[9B2] C144 CAP_01005 single_brd[6C6] C144_RF CAP_01005 radio_mlb[49C6]single_brd[24] C145 CAP_0402 single_brd[12D3] C145_RF CAP_01005 radio_mlb[49C6]single_brd[24] C147 CAP_01005 single_brd[18B4] C147_RF CAP_01005 radio_mlb[45B7]single_brd[24] C148_RF CAP_01005 radio_mlb[45C6]single_brd[24] C149 CAP_0402-1 single_brd[18D4] C149_RF CAP_0201-1 radio_mlb[45C5]single_brd[24] C150 CAP_01005 single_brd[18B4] C151 CAP_0402-2 single_brd[4D3] C152 CAP_0610 single_brd[4D3] C152_RF CAP_0201 radio_mlb[45B2]single_brd[24] C153 CAP_0204 single_brd[4D2] C153_RF CAP_201 radio_mlb[45B2]single_brd[24] C156 CAP_0402-1 single_brd[18D3] C156_RF CAP_01005 radio_mlb[46C6]single_brd[24] C158 CAP_4P1_0402 single_brd[4D2] C159 CAP_01005 single_brd[13C8] C160 CAP_0610 single_brd[4D5] C160_RF CAP_0201 radio_mlb[46B7]single_brd[24] C161 CAP_4P1_0402 single_brd[4D3] C162 CAP_0402 single_brd[2D3] C162_RF CAP_01005 radio_mlb[4C7]single_brd[24] C163 CAP_201 single_brd[2D2] C163_RF CAP_01005 radio_mlb[44B7]single_brd[24] C164_RF CAP_01005 radio_mlb[44C6]single_brd[24] C165 CAP_01005 single_brd[18D3] C165_RF CAP_0201-1 radio_mlb[44C5]single_brd[24] C166 CAP_4P1_0402 single_brd[4C5] C167 CAP_01005 single_brd[4D8] C167_RF CAP_0201 radio_mlb[44B7]single_brd[24] C168 CAP_01005 single_brd[13A8] C168_RF CAP_01005 radio_mlb[44B3]single_brd[24] C169 CAP_4P1_0402 single_brd[4C5] C169_RF CAP_01005 radio_mlb[49C4]single_brd[24] C170 CAP_0201-1 single_brd[18D2] C170_RF CAP_0201 radio_mlb[56B2]single_brd[24] C173 CAP_0204 single_brd[4D2] C174 CAP_4P1_0402 single_brd[4C5] C175 CAP_01005 single_brd[12D4] C176 CAP_01005 single_brd[17B7] C177 CAP_01005 single_brd[4D5] C178 CAP_01005 radio_mlb[35A5]single_brd[24] C179 CAP_0201-1 single_brd[6D4] C180 CAP_0402 single_brd[12D3] C181 CAP_0402-1 single_brd[6D3] C182 CAP_01005 radio_mlb[40C4]single_brd[24] C183 CAP_01005 radio_mlb[42B7]single_brd[24] C184 CAP_01005 single_brd[17D6] C185 CAP_0204 radio_mlb[42A7]single_brd[24] C186 CAP_01005 single_brd[6D3] C187 CAP_01005 radio_mlb[42B7]single_brd[24] C188 CAP_01005 radio_mlb[42B7]single_brd[24] C189 CAP_0402 single_brd[12C3] C190 RF CAP_01005 radio_mlb[49C5]single_brd[24] C190 CAP_0204 single_brd[7D7] C190_RF CAP_01005 radio_mlb[50C7]single_brd[24] C191 CAP_01005 single_brd[7C5] C192 CAP_01005 single_brd[11D6] C193_RF CAP_01005 radio_mlb[45C6]single_brd[24] C194 CAP_0201-1 single_brd[11C6] C195_RF CAP_01005 radio_mlb[46C6]single_brd[24] C196 CAP_01005 single_brd[11C6] C197 CAP_01005 single_brd[8B6] C198 CAP_01005 single_brd[11B6] C199 CAP_01005 single_brd[11B6] C200 CAP_01005 single_brd[11A4] C200_RF CAP_01005-1 radio_mlb[50C7]single_brd[24] C201 CAP_01005 single_brd[11C7] C201_RF CAP_01005 radio_mlb[50B7]single_brd[24] C202 CAP_01005 single_brd[11D6] C202_RF CAP_01005 radio_mlb[50C8]single_brd[24] C203_RF CAP_01005 radio_mlb[50C6]single_brd[24] C204 CAP_0402-2 single_brd[12D7] C206 CAP_01005 single_brd[17B2] C207 CAP_0201-1 single_brd[12A4] C207_RF CAP_201 radio_mlb[50B3]single_brd[24] C208 CAP_01005 single_brd[8B4] C208_RF CAP_01005 radio_mlb[44C6]single_brd[24] C209 CAP_0402-1 single_brd[6D2] C209_RF CAP_201 radio_mlb[44C5]single_brd[24] C210 CAP_01005 single_brd[11B6] C210_RF CAP_01005 radio_mlb[45C6]single_brd[24] C211 CAP_01005 single_brd[11B7] C211_RF CAP_201 radio_mlb[45C5]single_brd[24] C212 CAP_01005 single_brd[11B6] C212_RF CAP_01005 radio_mlb[47C5]single_brd[24] C213 CAP_01005 single_brd[14B5] C213_RF CAP_201 radio_mlb[47C5]single_brd[24] C214 CAP_01005 single_brd[14A5] C214_RF CAP_01005 radio_mlb[47C4]single_brd[24] C215 CAP_0201-1 single_brd[17B7] C215_RF CAP_01005 radio_mlb[47C4]single_brd[24] C216 CAP_01005 single_brd[8C3] C216_RF CAP_01005 radio_mlb[47C4]single_brd[24] C217_RF CAP_01005 radio_mlb[48C5]single_brd[24] C218 CAP_402 single_brd[10B7] C219 CAP_402 single_brd[10A7] C220 CAP_01005 single_brd[9C7] C221 CAP_01005 single_brd[9C7] C222 CAP_01005 single_brd[9C7] C223 CAP_01005 single_brd[9C7] C224 CAP_01005 single_brd[9B7] C225 CAP_01005 single_brd[9B7] C226 CAP_01005 single_brd[9C6]				C226_RF CAP_01005 radio_mlb[50C5]single_brd[24] C227 CAP_01005 single_brd[9C6] C227_RF CAP_01005 radio_mlb[50C4]single_brd[24] C228 CAP_01005 single_brd[9B6] C228_RF CAP_01005 radio_mlb[50C4]single_brd[24] C229 CAP_01005 single_brd[9C6] C229_RF CAP_01005 radio_mlb[50C4]single_brd[24] C230 CAP_01005 single_brd[9C6] C230_RF CAP_01005 radio_mlb[50C4]single_brd[24] C231 CAP_01005 single_brd[9B6] C231_RF CAP_01005 radio_mlb[50C3]single_brd[24] C232 CAP_402 single_brd[10C5] C232_RF CAP_01005 radio_mlb[50C3]single_brd[24] C233 CAP_402 single_brd[10C5] C233_RF CAP_01005 radio_mlb[46C5]single_brd[24] C234 CAP_402 single_brd[10B5] C234_RF CAP_0201 radio_mlb[46C5]single_brd[24] C235 CAP_01005 single_brd[9C3] C235_RF CAP_201 radio_mlb[48C5]single_brd[24] C236 CAP_01005 single_brd[9C3] C236_RF CAP_01005 radio_mlb[44C4]single_brd[24] C237 CAP_0201-1 single_brd[10B8] C237_RF CAP_01005 radio_mlb[44C4]single_brd[24] C238 CAP_402 single_brd[10B8] C238_RF CAP_01005 radio_mlb[44C4]single_brd[24] C239_RF CAP_01005 radio_mlb[46C4]single_brd[24] C240 CAP_01005 single_brd[8B4] C242 CAP_01005 single_brd[17D6] C242_RF CAP_01005 radio_mlb[50C6]single_brd[24] C243 CAP_01005 single_brd[19B4] C243_RF CAP_01005 radio_mlb[50C6]single_brd[24] C244 CAP_01005 single_brd[8C3] C244_RF CAP_01005 radio_mlb[41D1]single_brd[24] C245 CAP_0201-2 single_brd[13B4]single_brd[24] C246_RF CAP_0201 radio_mlb[43B4]single_brd[24] C247_RF CAP_01005 radio_mlb[53D4]single_brd[24] C248_RF CAP_01005 radio_mlb[40C2]single_brd[24] C249 CAP_0201 single_brd[21B6] C249_RF CAP_0201 radio_mlb[44B3]single_brd[24] C250 CAP_0402-1 single_brd[12A7] C250_RF CAP_201 radio_mlb[45B3]single_brd[24] C251 CAP_0402-1 single_brd[12A7] C251_RF CAP_201 radio_mlb[45B3]single_brd[24] C252 CAP_0402-1 single_brd[14B1] C252_RF CAP_0201 radio_mlb[47B3]single_brd[24] C253 CAP_01005 single_brd[11B3] C253_RF CAP_0201 radio_mlb[47B3]single_brd[24] C254 CAP_01005 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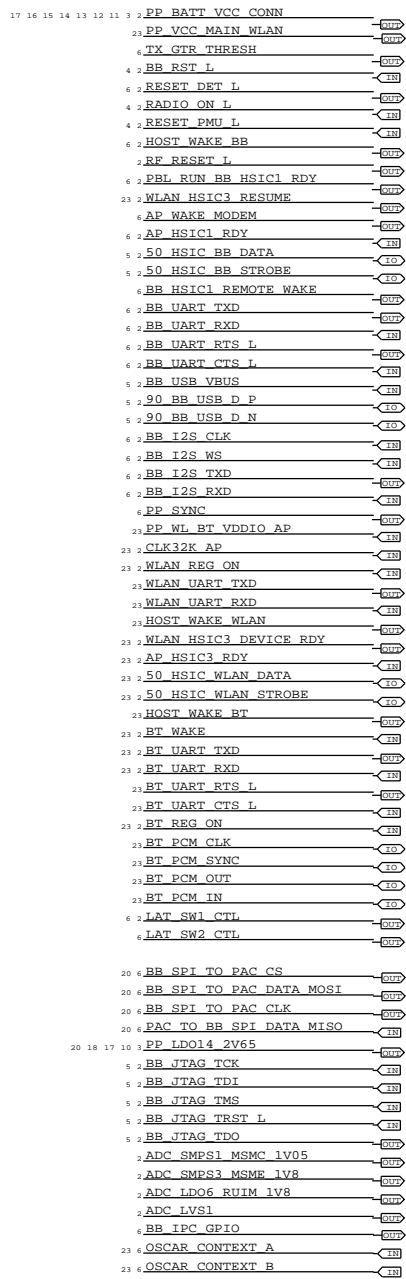
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	C329	CAP_0603-1	single_brd[14C3]	D3	DIODE_SCHOT_DFN1006-	single_brd[8C6]	FL63	FILTER_2P_01005	single_brd[17D2]	L74_RF	IND_0201	radio_mlb[47C3]	single_brd[24]										
	C330	CAP_0402-1	single_brd[14C3]		2		FL64	FILTER_2P_01005-1	single_brd[11A7]	L75_RF	IND_0201	radio_mlb[47B3]	single_brd[24]										
	C331	CAP_0402	single_brd[18D4]	DZ1	SUPPR_TRANSIENT_2P1_	single_brd[8B6]	FL65	FILTER_2P_01005-1	single_brd[11A7]	L77_RF	IND_01005	radio_mlb[48B3]	single_brd[24]										
	C332	CAP_0402-1	single_brd[15C6]		01005-1		FL66	FILTER_2P_01005	single_brd[17D6]	L78_RF	IND_0201	radio_mlb[50B3]	single_brd[24]										
	C333	CAP_0402-1	single_brd[15C7]	DZ2	SUPPR_TRANSIENT_2P1_	single_brd[8B6]	FL67	FILTER_2P_0201	single_brd[5A5]	L79_RF	IND_0201	radio_mlb[51C7]	single_brd[24]										
	C334	CAP_0201-1	single_brd[20C3]		01005-1		FL68	FILTER_2P_01005	single_brd[17B7]	L732_RF	IND_03015	radio_mlb[53D6]	single_brd[24]										
	C335	CAP_0402-1	single_brd[15D6]	DZ3	SUPPR_TRANSIENT_2P1_	single_brd[8B6]	FL69	FILTER_2P_01005	single_brd[17C7]	PP1_RF	PROBEPOINT_SM	single_brd[3D1]											
	C336	CAP_01005	single_brd[20C2]		01005-1		FL74	FILTER_2P_01005-1	single_brd[8C6]	PP2	PROBEPOINT_SM	single_brd[6B8]											
C	C337	CAP_0201-1	single_brd[15D6]	DZ4	ZENER_GDZ-0201	single_brd[18D3]	FL1701_RF	FILTER_3P5_LFE18832M	radio_mlb[53D7]	radio_mlb[35D7]	single_brd[24]	PP2_RF	PROBEPOINT_SM	radio_mlb[35D7]	single_brd[24]								
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	C339	CAP_201	single_brd[15D5]		01005-1		FL2302	FILTER_2P_01005	single_brd[17C2]	J1	CON_F345T_D6MT_SM_F-	single_brd[11C5]	PP3_RF	PROBEPOINT_SM	radio_mlb[35D7]	single_brd[24]							
	C340	CAP_402	single_brd[15C4]	DZ9	SUPPR_TRANSIENT_2P1_	single_brd[17B6]		ST-SM				PP4_RF	PROBEPOINT_SM	radio_mlb[35B6]	single_brd[24]								
	C341	CAP_0201-1	single_brd[15C4]		01005		J1_RF	CON_M545T_D4MT_SM_M-	radio_mlb[35D2]	single_brd[24]	ST-SM	PP5_RF	PROBEPOINT_SM	radio_mlb[35B6]	single_brd[24]								
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	C356	CAP_01005	single_brd[9A6]	DZ101	SUPPR_TRANSIENT_2P1_	single_brd[8C5]		J6_RF	CON_F1ST_COAX_S3MT_S	radio_mlb[55C5]	single_brd[24]	PP14_RF	PROBEPOINT_SM	radio_mlb[35C7]	single_brd[24]								
	C357	CAP_0402-1	single_brd[12C8]		01005-1		J7	CON_M385T_D4MT_SM_M-	single_brd[17C4]	ST-SM	PP15	PROBEPOINT_SM	radio_mlb[35C7]	single_brd[24]									
	C358	CAP_0402-1	single_brd[12C8]	DZ1100	SUPPR_TRANSIENT_2P1_	single_brd[11C3]		J10_RF	CON_F1ST_COAX_S3MT_S	radio_mlb[56C4]	single_brd[24]	PP15_RF	PROBEPOINT_SM	radio_mlb[35C7]	single_brd[24]								
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	C364	CAP_0201	single_brd[18A3]	FD3	FIDUCIAL_0P5SM1P0SQ-	single_brd[22B8]			L1_RF	IND_0806	radio_mlb[36C4]	single_brd[24]	PP40_RF	PROBEPOINT_SM	radio_mlb[35B7]	single_brd[24]							
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	C371	CAP_402	single_brd[18C7]		NSP			L4_RF	IND_0806	radio_mlb[36D3]	single_brd[24]	PP43_RF	PROBEPOINT_SM	radio_mlb[35B7]	single_brd[24]								
	C372	CAP_0201-1	single_brd[18C6]	FD6	FIDUCIAL_0P5SM1P0SQ-	single_brd[22B8]			L5	IND_P_TFA201610G-SM	single_brd[15B6]	PP44	PROBEPOINT_SM	radio_mlb[35B7]	single_brd[24]								

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AP INTERFACE & DEBUG CONNECTORS

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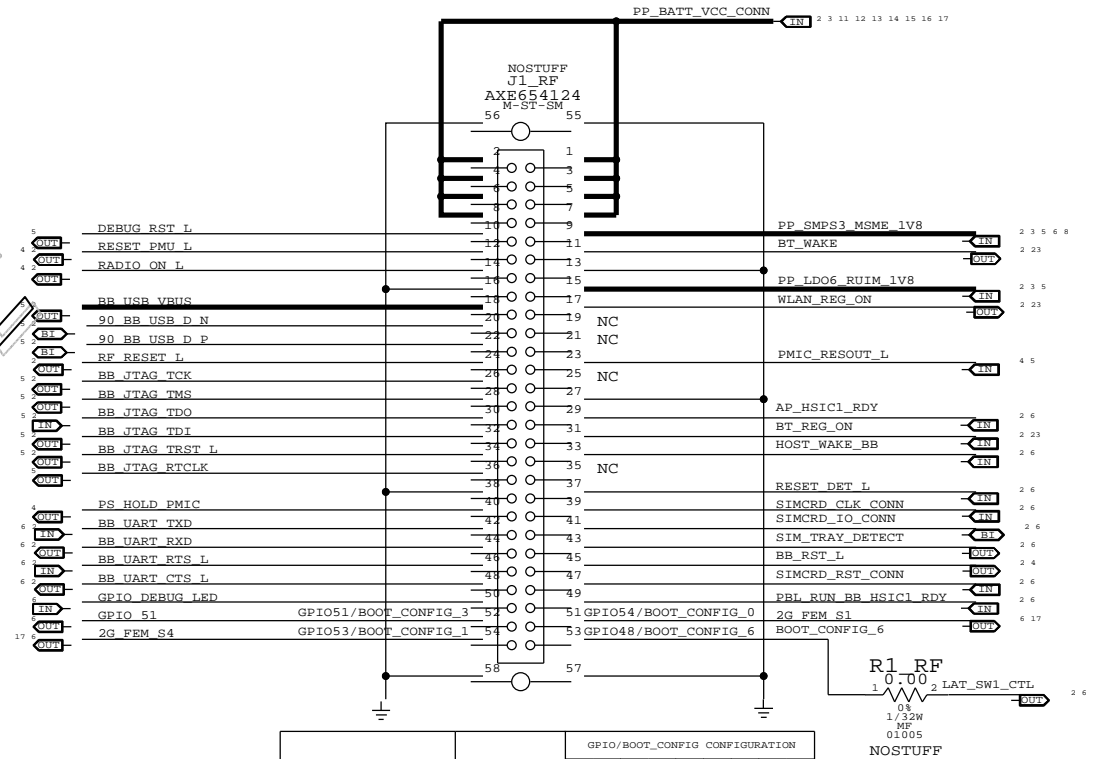
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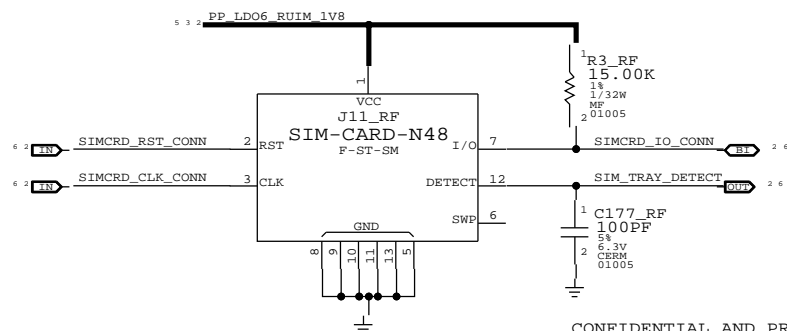


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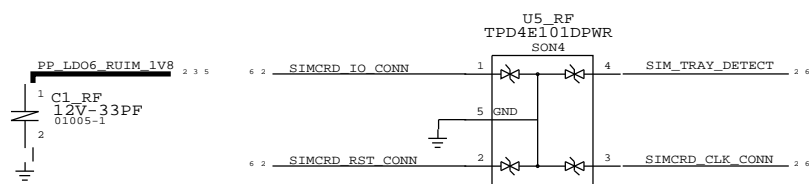


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BOOT_USB_OPTION	0X03	X	1	0	0	0	0	1	1
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SIM CARD CONNECTOR



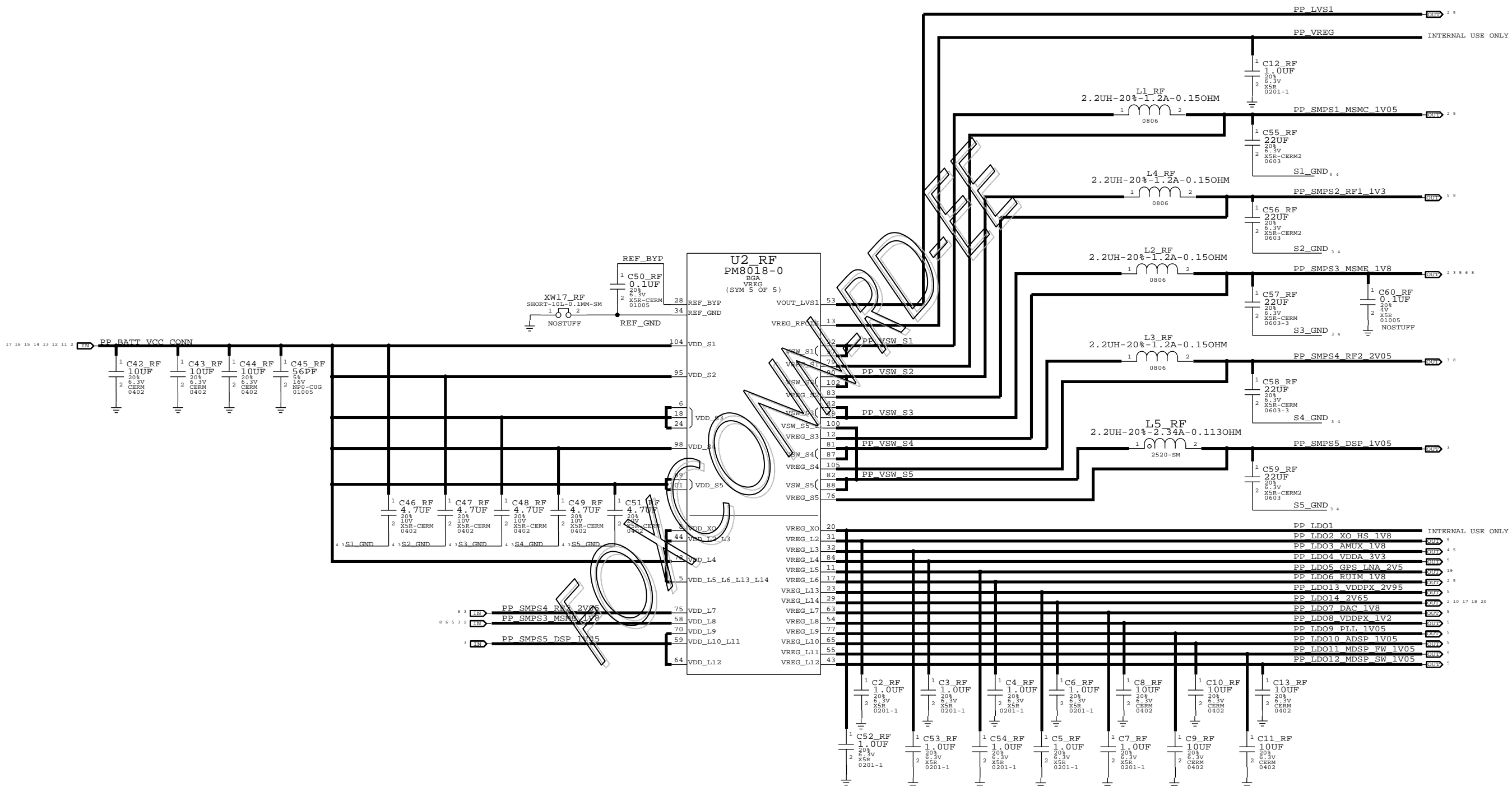
SIM CARD ESD PROTECTION



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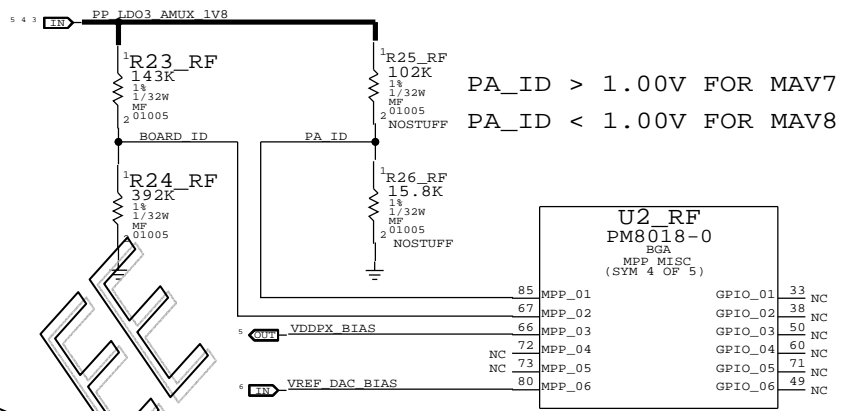
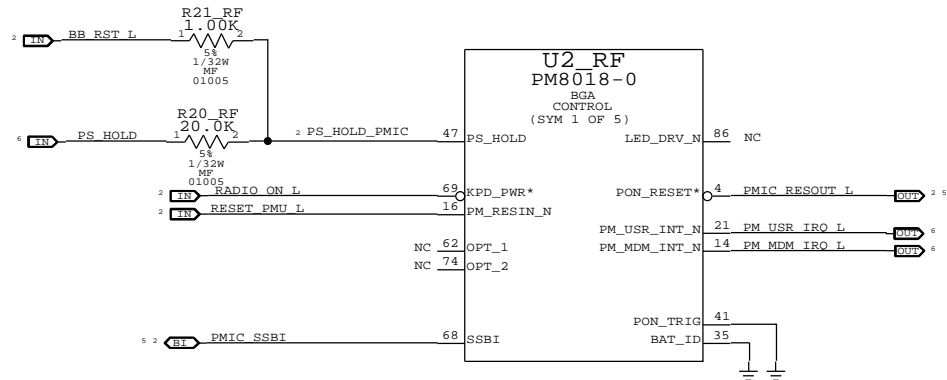
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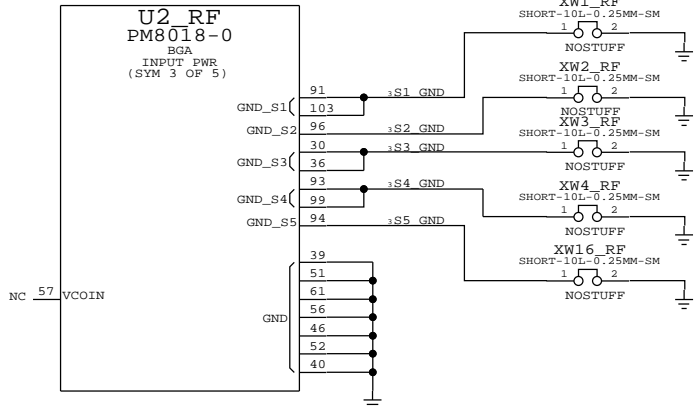


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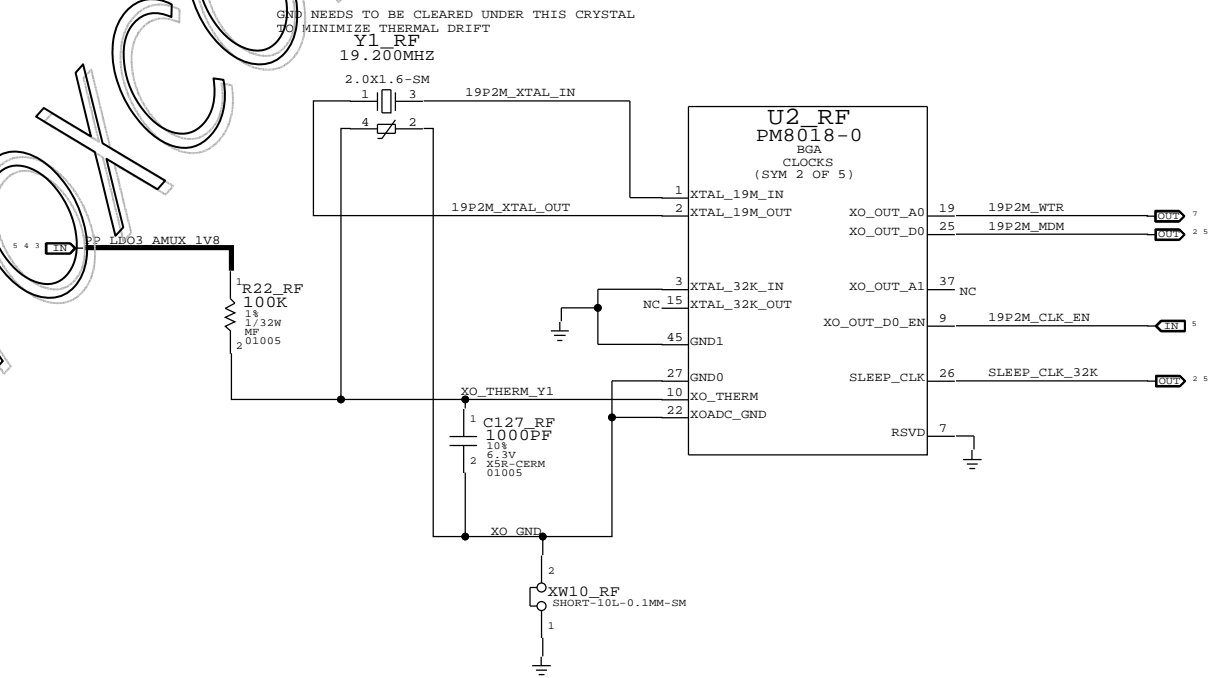
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1.3V	E1C
1.5V	EVT2
1.7V	DVT/PVT



PA THERMISTOR REMOVED TO MATCH N41, AP SECTION
NEEDS ITS OWN THERMISTOR PLACED NEAR THE PA'S.



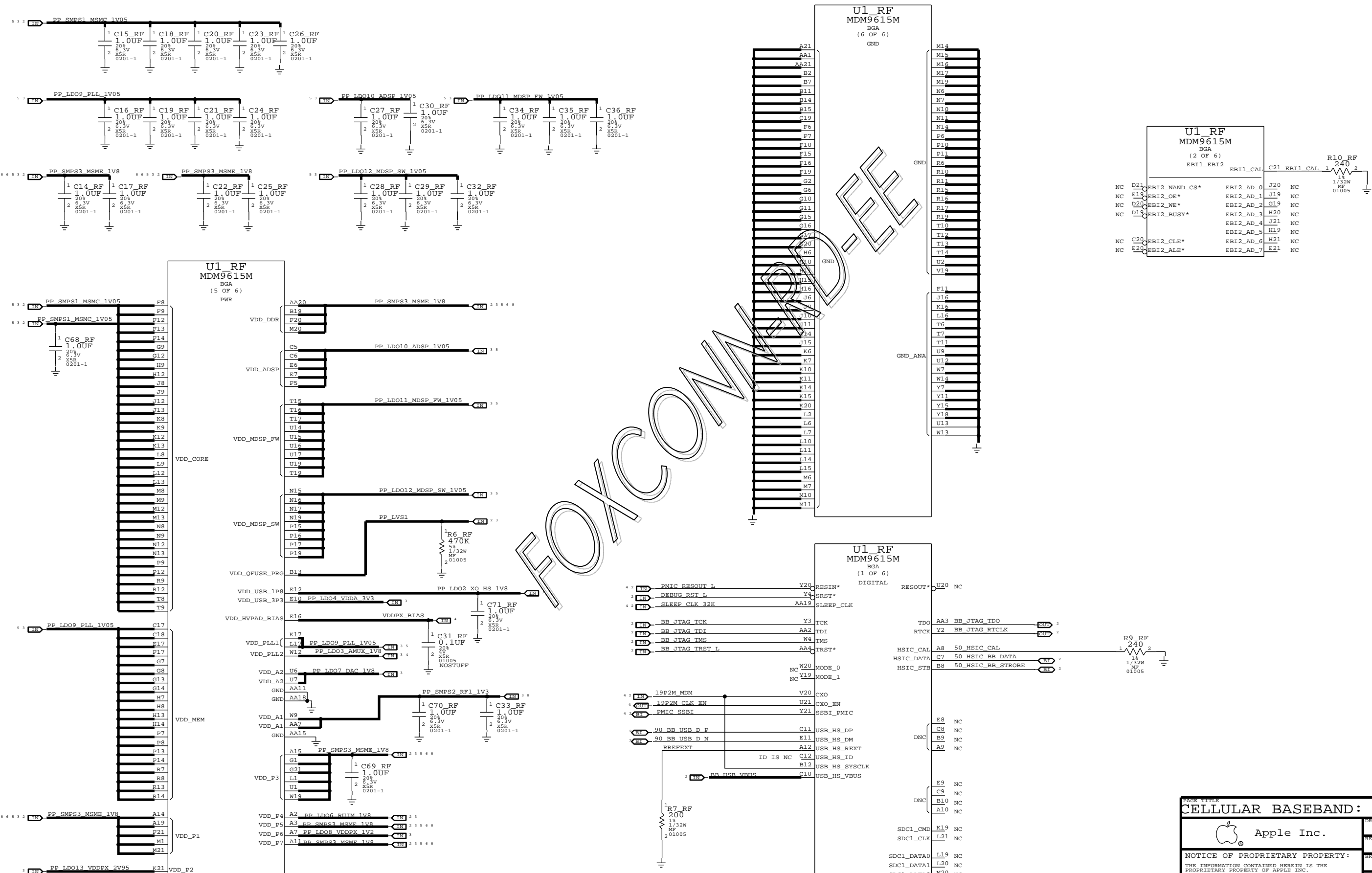
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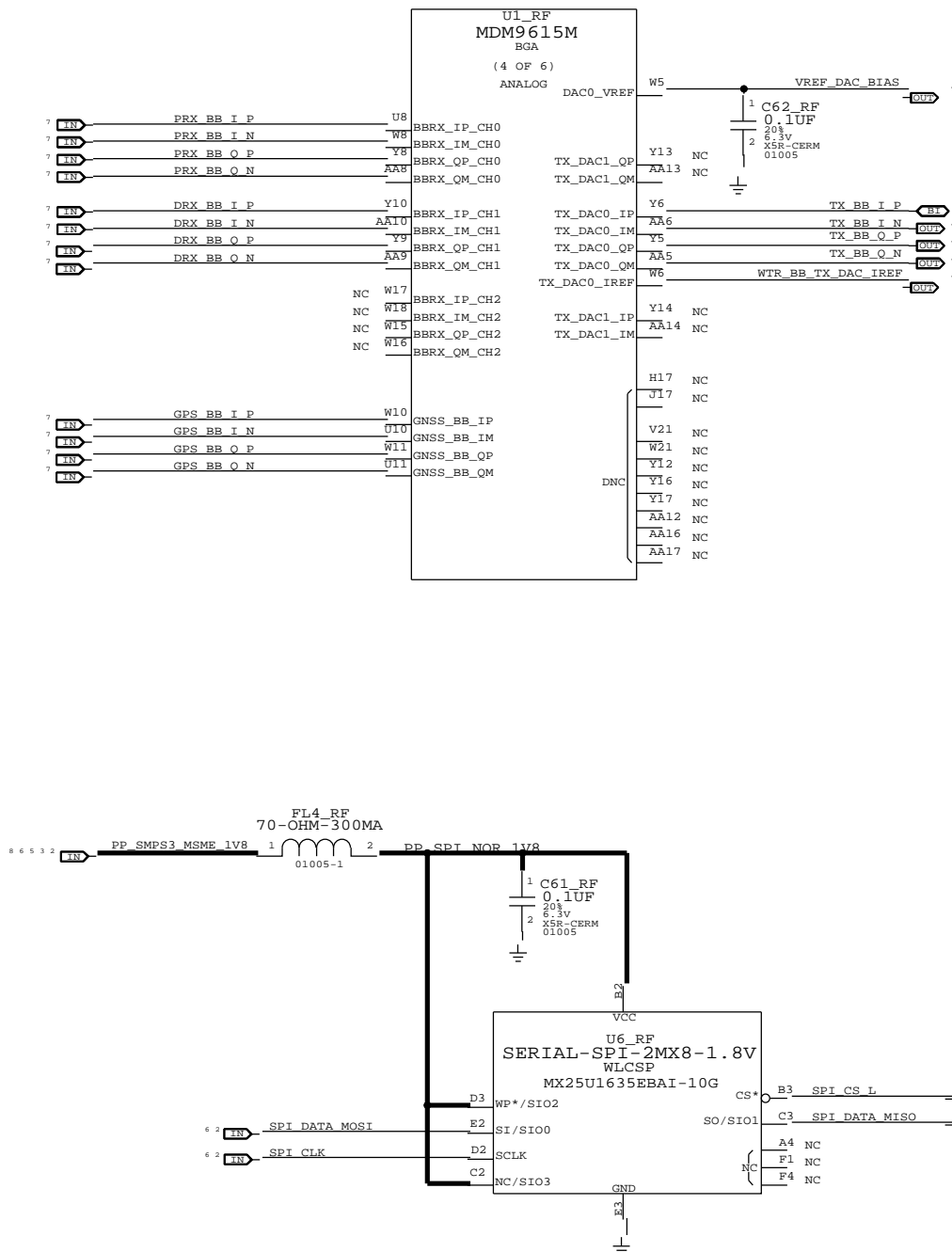
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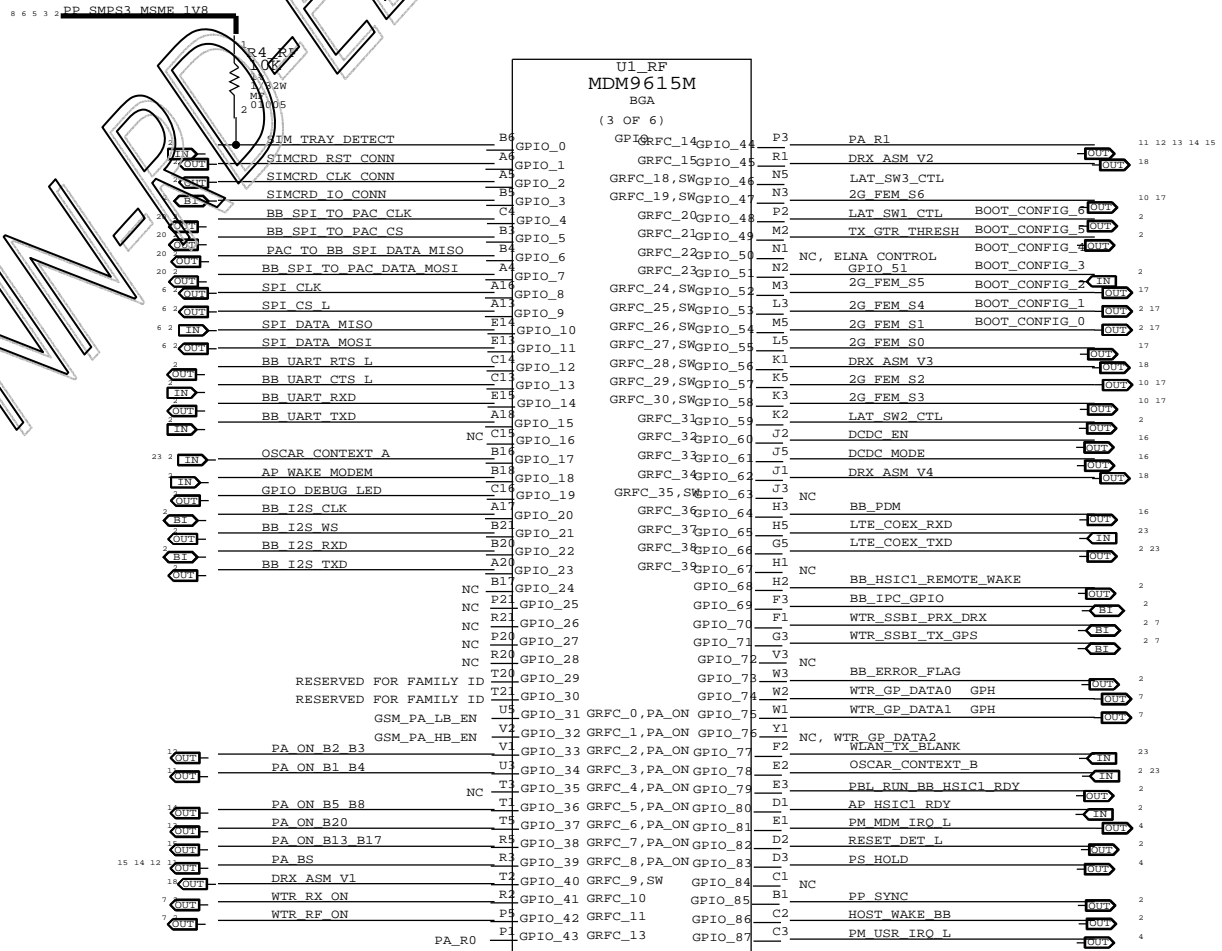
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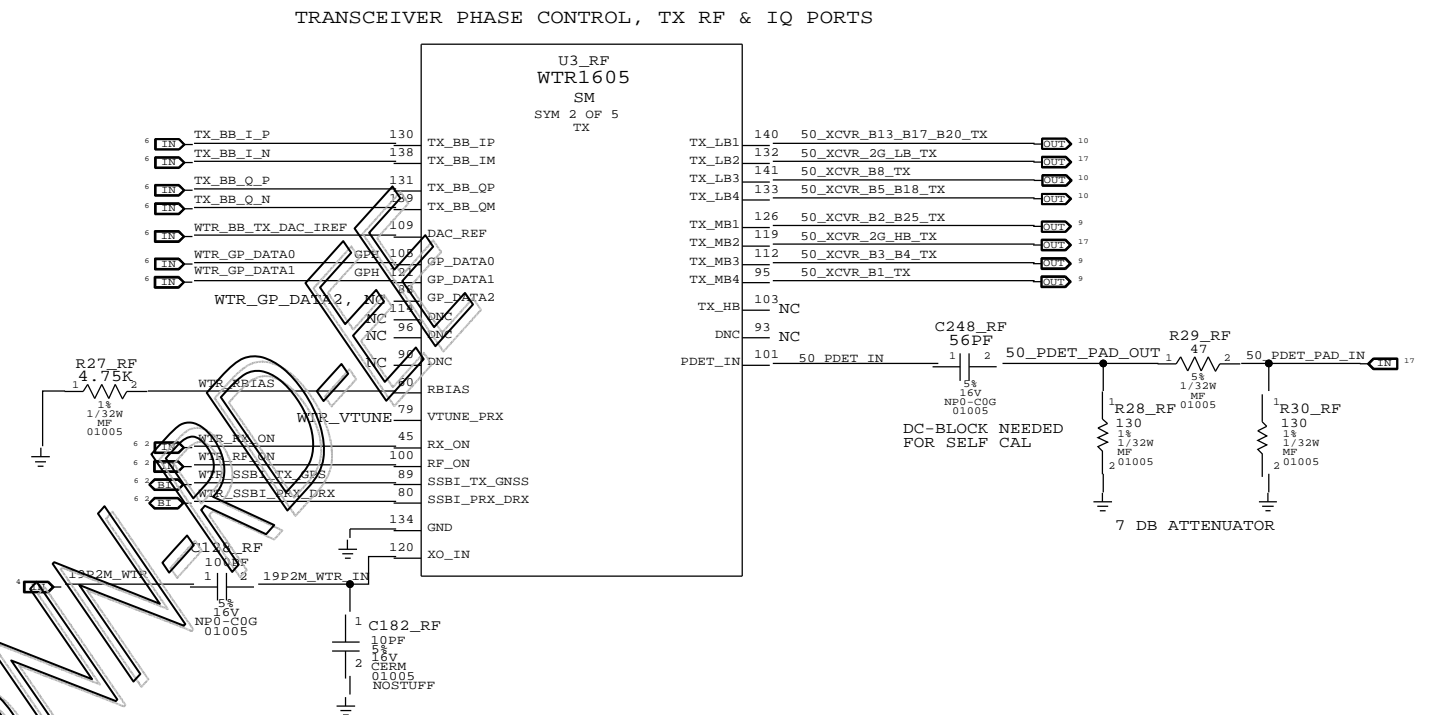
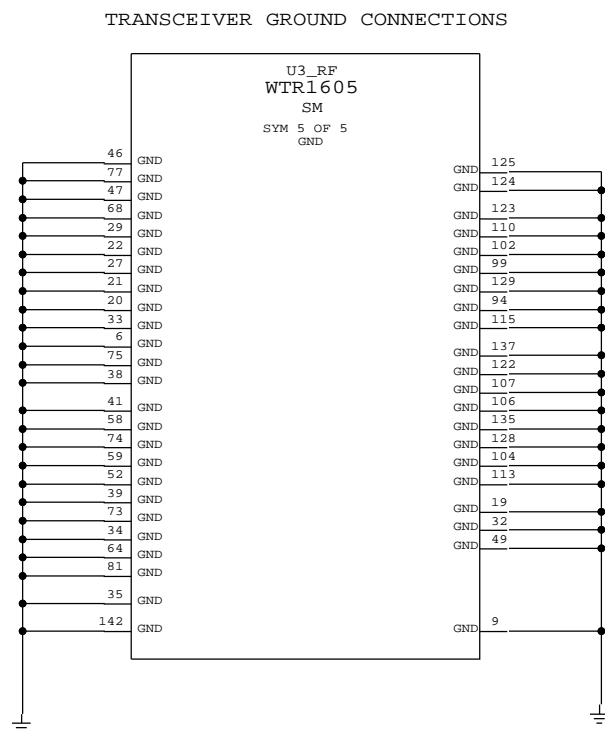
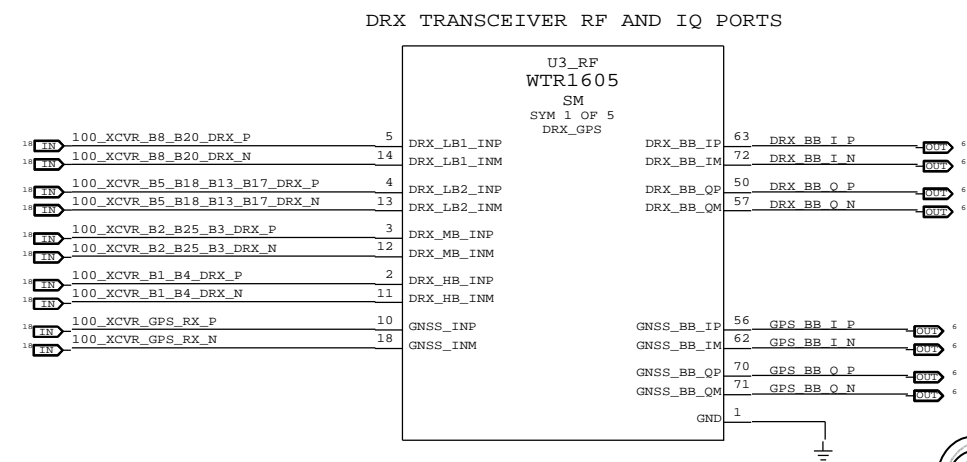
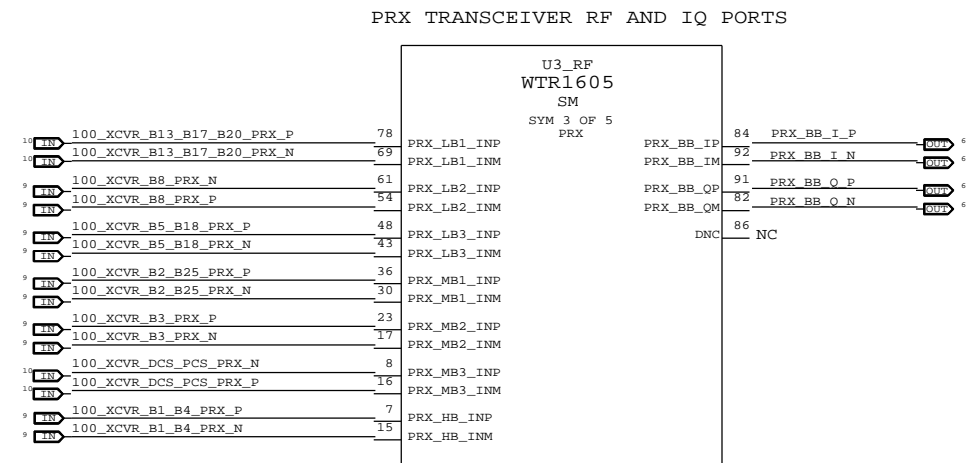


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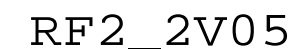
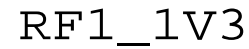


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RF TRANSCEIVER (1 OF 4)



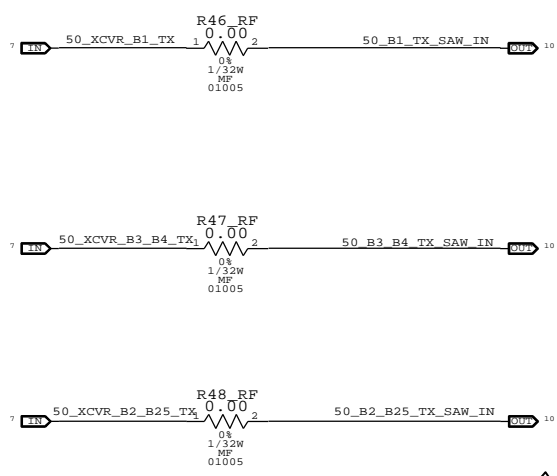
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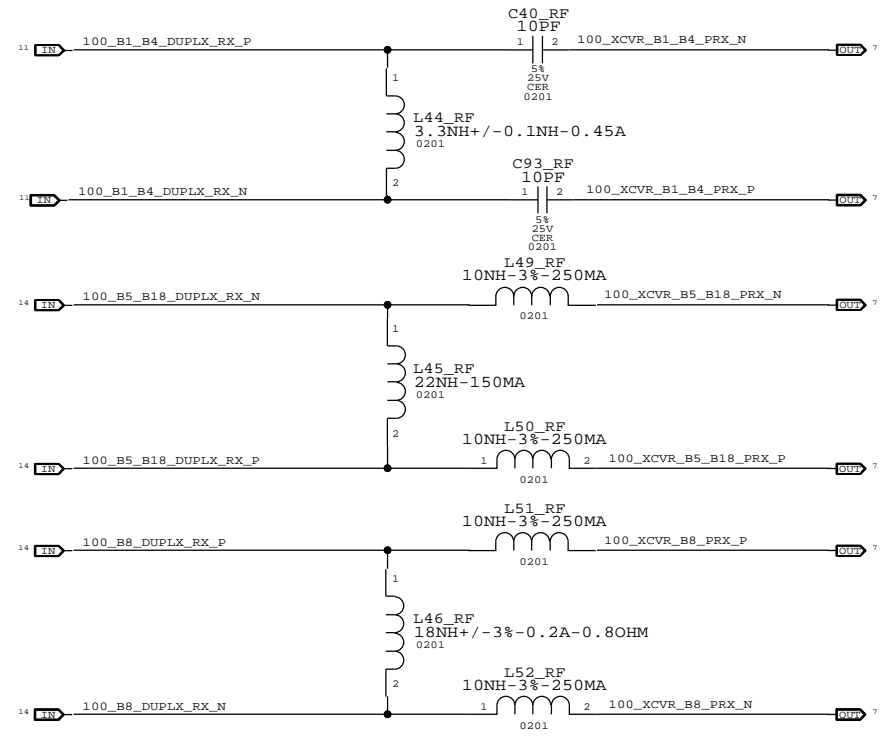
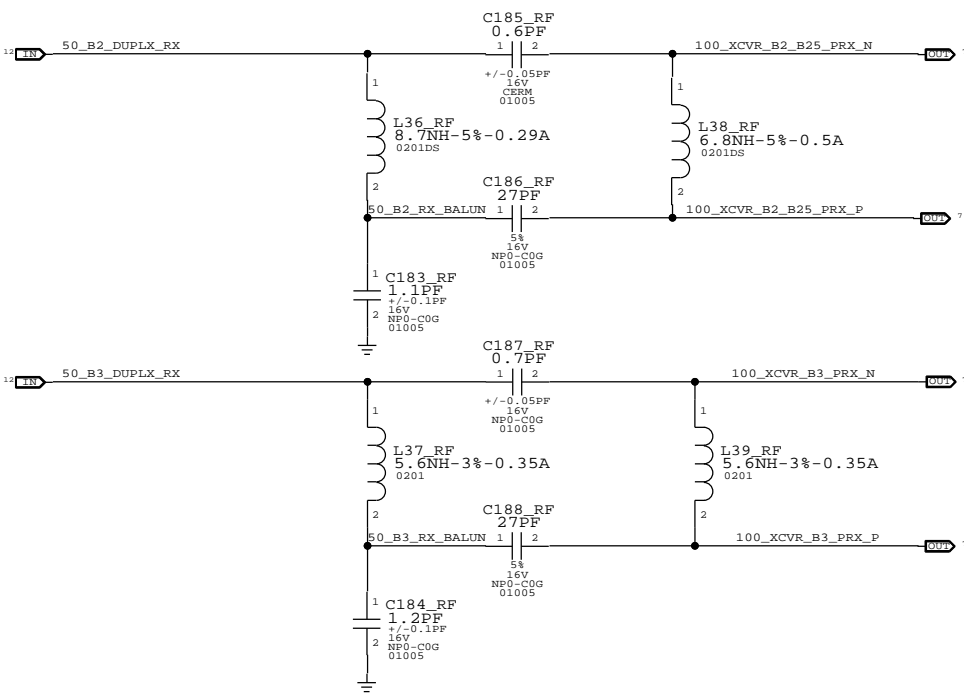
		U3_RF			
		WTR1605			
		SM			
		SRM 4 OF 5			
		PWR			
PP RF1 1V3 PRX FELO1	53	VDD_RF1_P_FELO		VDD_RF2_T_DA	111 PP RF2 2V05 TX DA
PP RF1 1V3 PRX FELO2	42	VDD_RF1_P_FELO		VDD_RF1_T_DA	118 PP RF1 1V3 TX DA
PP RF1 1V3 DRX LBLO	28	VDD_RF1_D_LBLO		VDD_RF1_T_UPC	117 PP RF1 1V3 TX UNCONVERTED
PP RF1 1V3 DRX FE	26	VDD_RF1_D_FE		VDD_RF1_T_LO	116 PP RF1 1V3 TX LQ
PP RF1 1V3 DRX MBLO	25	VDD_RF1_D_MBLO		VDD_RF2_T_BB	108 PP RF2 2V05 TX_BB
PP RF1 1V3 JAM DET	85	VDD_RF1_JDET			
PP RF2 2V05 PRX BB	83	VDD_RF2_P_BB		VDD_RF2_T_VCO	136 PP RF2 2V05 TX VCO
PP RF2 2V05 DRX BB	44	VDD_RF2_D_BB		VDD_RF2_XO	127 PP RF2 2V05 XO FILT
				VDD_RF1_T_SYN	98 PP RF1 1V3 TX SYNTH
PP RF2 2V05 PRX VCO	67	VDD_RF2_P_VCO		VDD_RF2_T_PLL	97 PP RF2 2V05 TX PLL
PP RF1 1V3 PRX VCO	76	VDD_RF1_P_VCO			
PP RF1 1V3 PRX PLL	66	VDD_RF1_P_PLL		VDD_RF1_G_LNA	24 PP RF1 1V3 GPS LNA
PP RF2 2V05 SHDR VCO	51	VDD_RF2_S_VCO		VDD_RF1_G_VCO	37 PP RF1 1V3 GPS VCO
PP RF1 1V3 SHDR VCO	40	VDD_RF1_S_VCO		VDD_RF1_G_PLL	55 PP RF1 1V3 GPS PLL
PP RF1 1V3 SHDR PLL	65	VDD_RF1_S_PLL		VDD_RF1_G_BB	31 PP RF1 1V3 GPS DIG
				VDD_DIO	87 PP RF1 1V8 DIG

TRANSCEIVER TX AND RX MATCHING

TX MATCHING NETWORKS



RX MATCHING NETWORKS

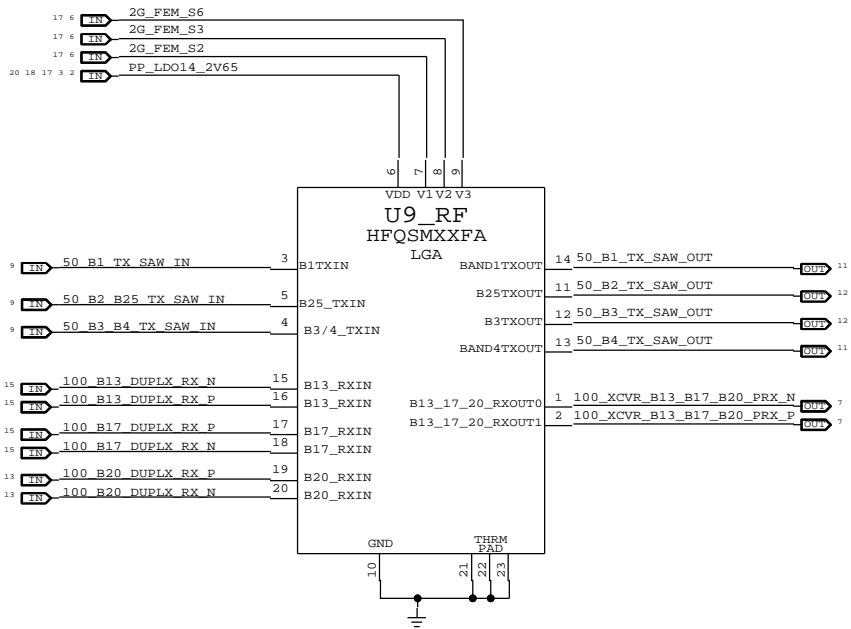


CELLULAR FRONT END: TX AND RX MATCHING

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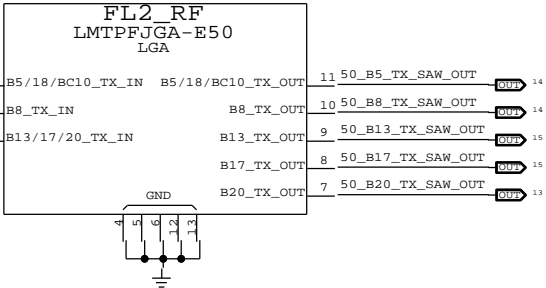
SAW BANKS

HB TX SAW BANK + B13/B17/B20 DP6T SWITCH AND MATCHING

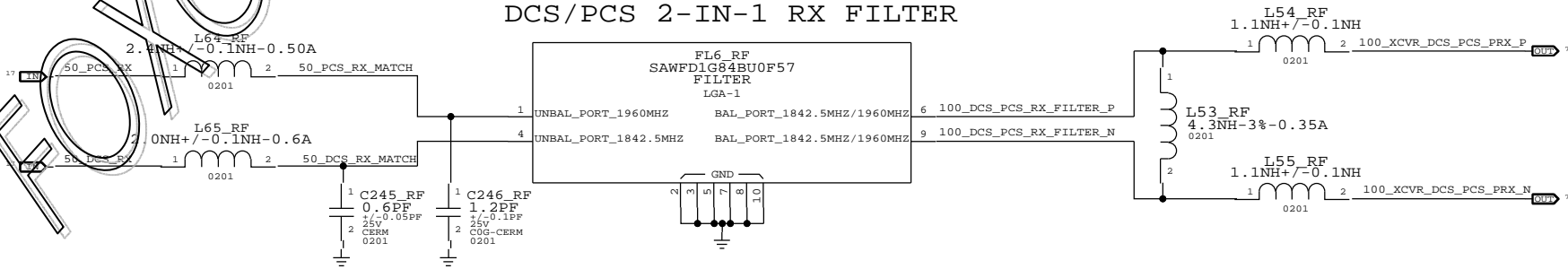



BAND	S6	S3	S2
B3 TX	HIGH	X	X
B4 TX	LOW	X	X
B13 RX	X	HIGH	HIGH
B17 RX	X	HIGH	LOW
B20 RX	X	LOW	HIGH

LB TX SAW BANK

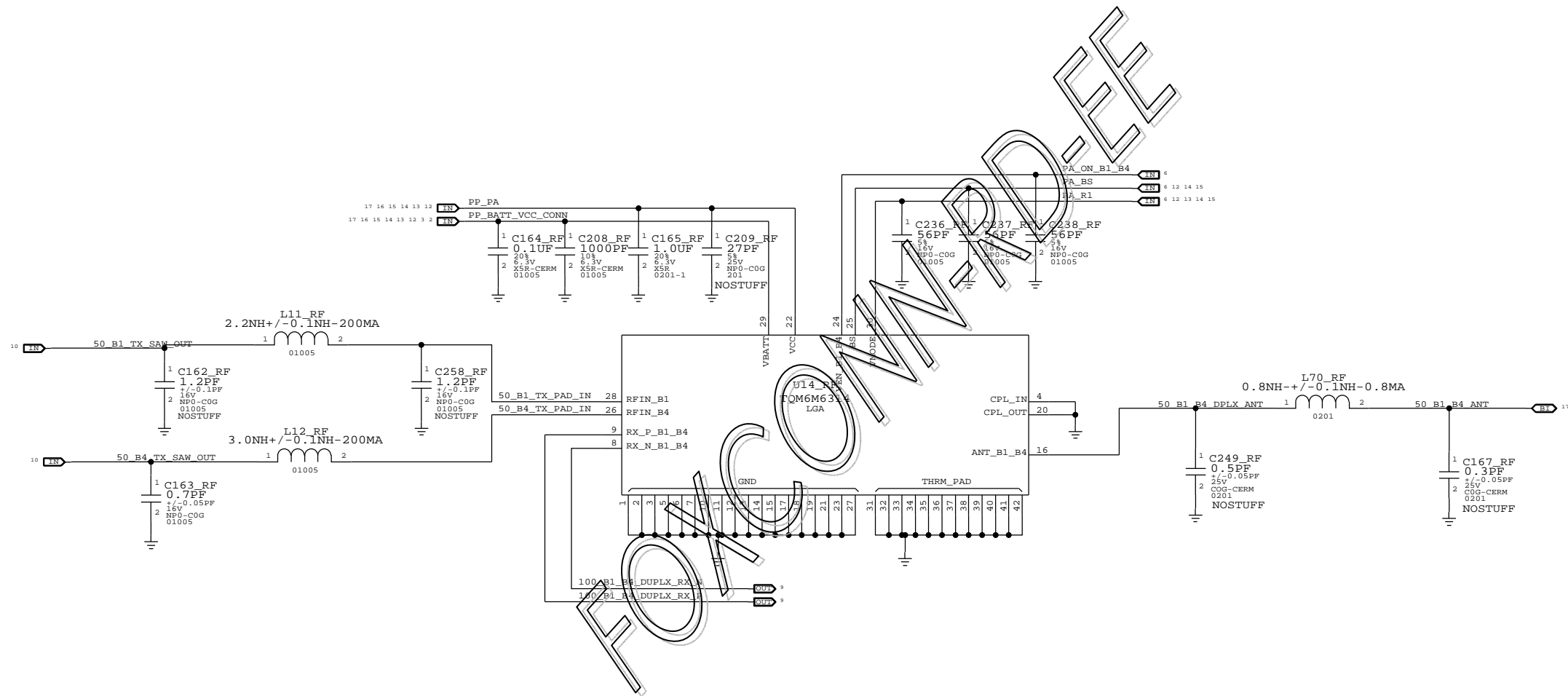


DCS/PCS 2-IN-1 RX FILTER



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BAND 1/4 PAT



BAND	PA	POWER	MODE	PA_BS	PA_ON_B1_B4	PA_R1
=====	=====	=====	=====	=====	=====	=====
POWER DOWN		X		0	0	0
STANDBY		X		X	0	X
B4		HPM		0	1	0
B4		LPM		0	1	1
B1		HPM		1	1	0
B1		LPM		1	1	1

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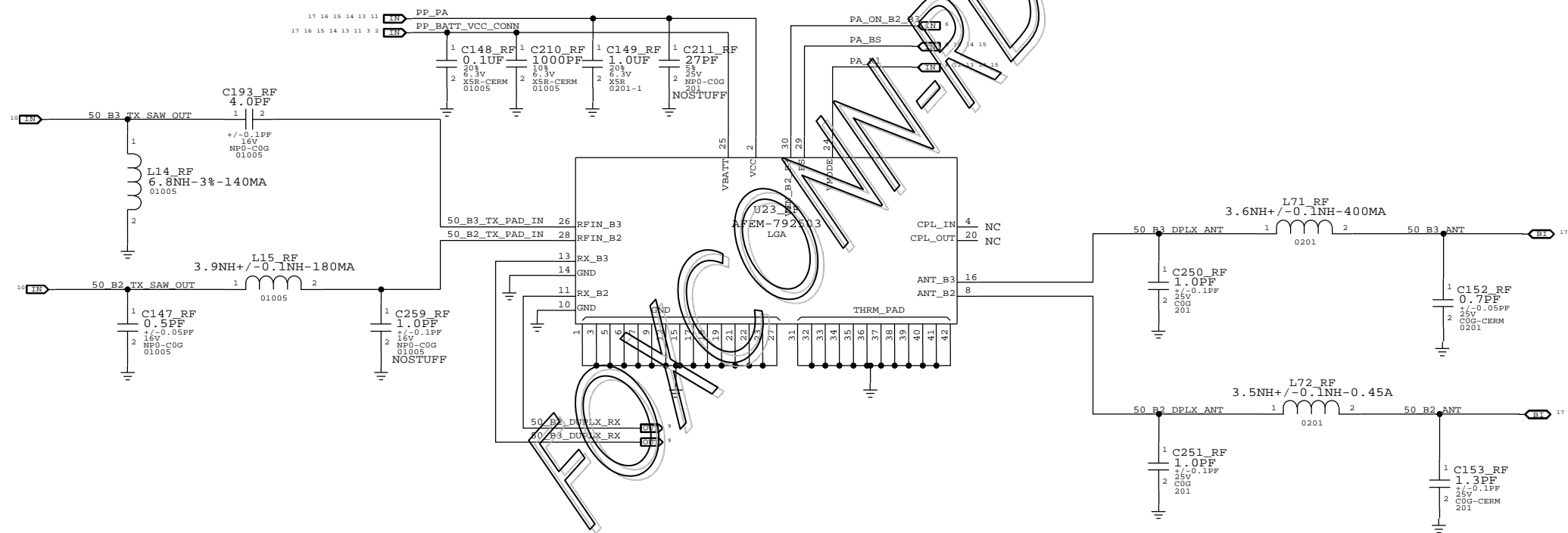
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BAND 2 / 3 PAD



BAND	PA	POWER	MODE	PA_BS	PA_ON_B2_B3	PA_R1
=====	=====	=====	=====	=====	=====	=====
POWER DOWN		X		0	0	0
STANDBY		X		X	0	X
B3		HPM		0	1	0
B3		LPM		0	1	1
B2		HPM		1	1	0
B2		LPM		1	1	1

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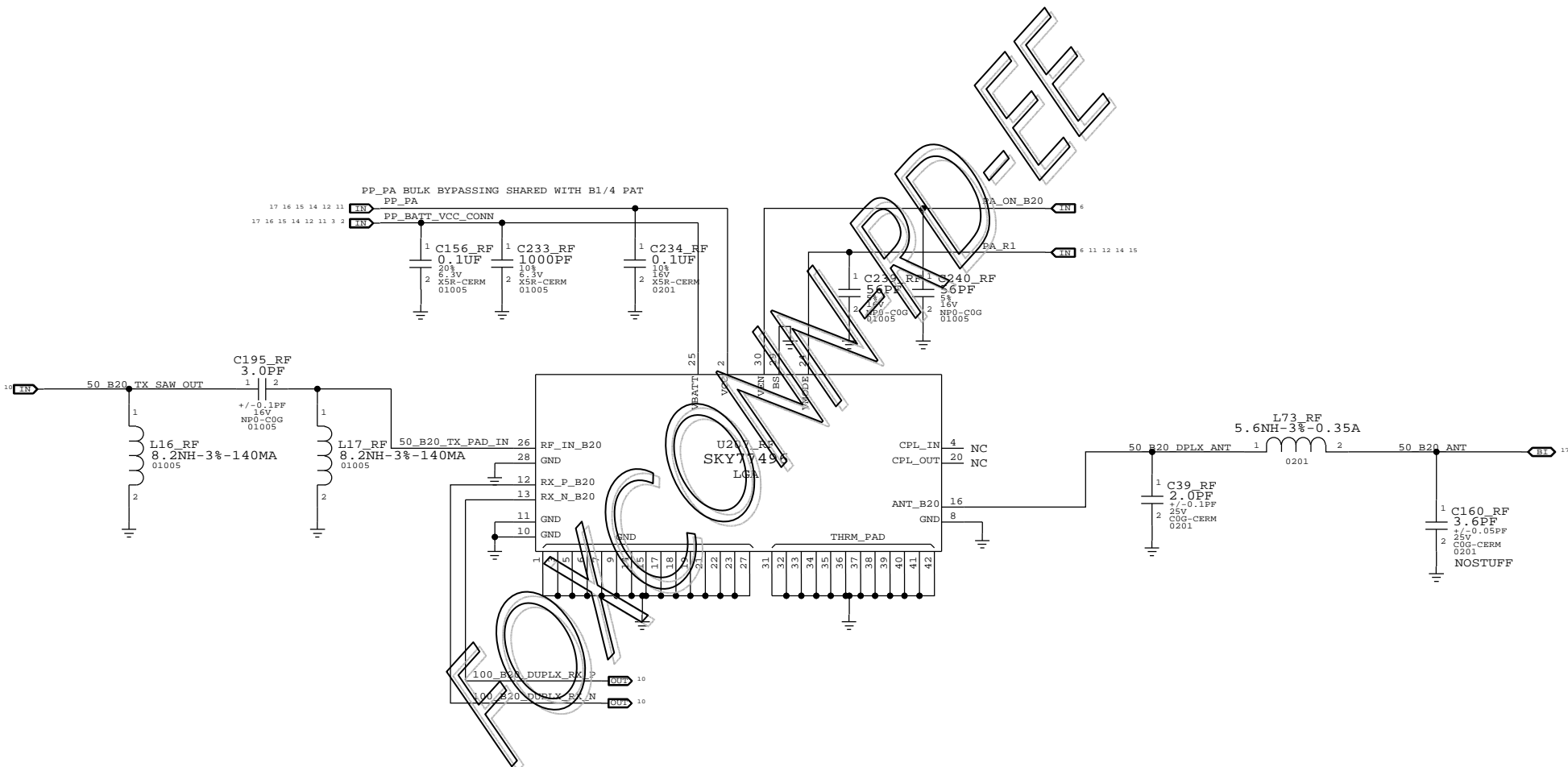
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BAND 20 PAD



BAND	PA POWER MODE	PA_ON_B20	PA_R1
POWER DOWN	LPM	0	0
STANDBY	X	0	X
B20	HPM	1	0
B20	LPM	1	1

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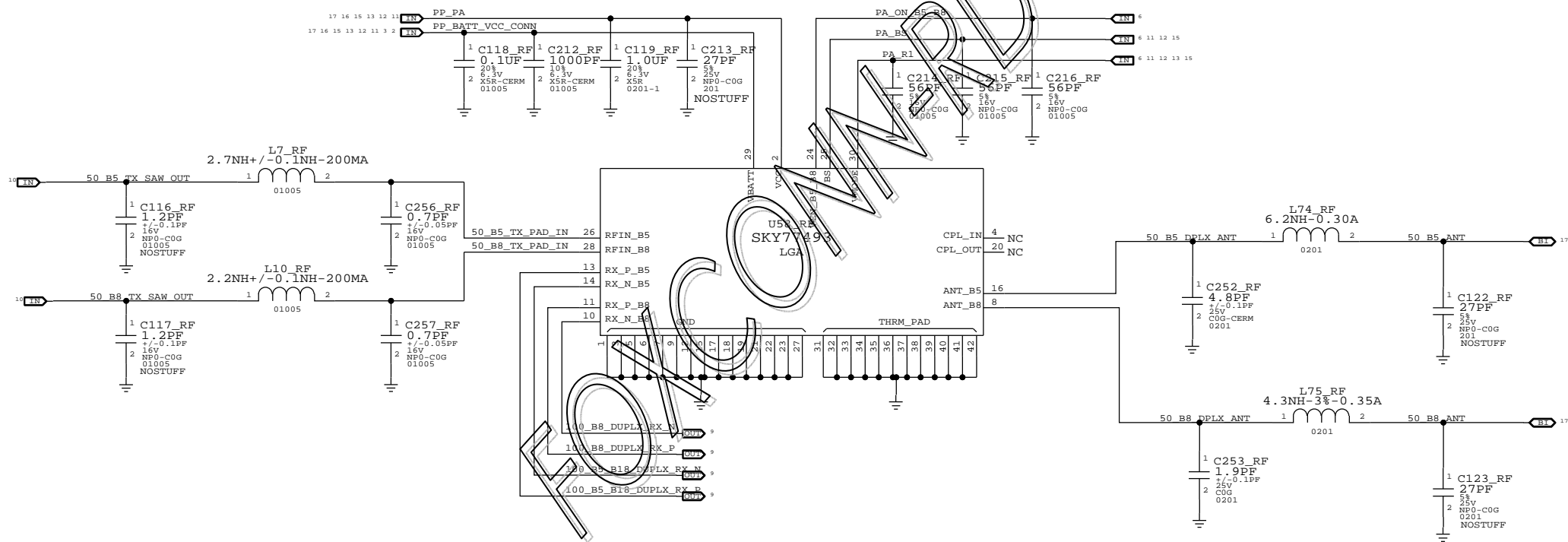
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BAND 5 / 8 PAD



BAND	PA POWER MODE	PA_BS	PA_ON_B5_B8	PA_R1
=====	=====	=====	=====	=====
POWER DOWN	X	0	0	0
STANDBY	X	X	0	X
B5	HPM	0	1	0
B5	LPM	0	1	1
B8	HPM	1	1	0
B8	LPM	1	1	1

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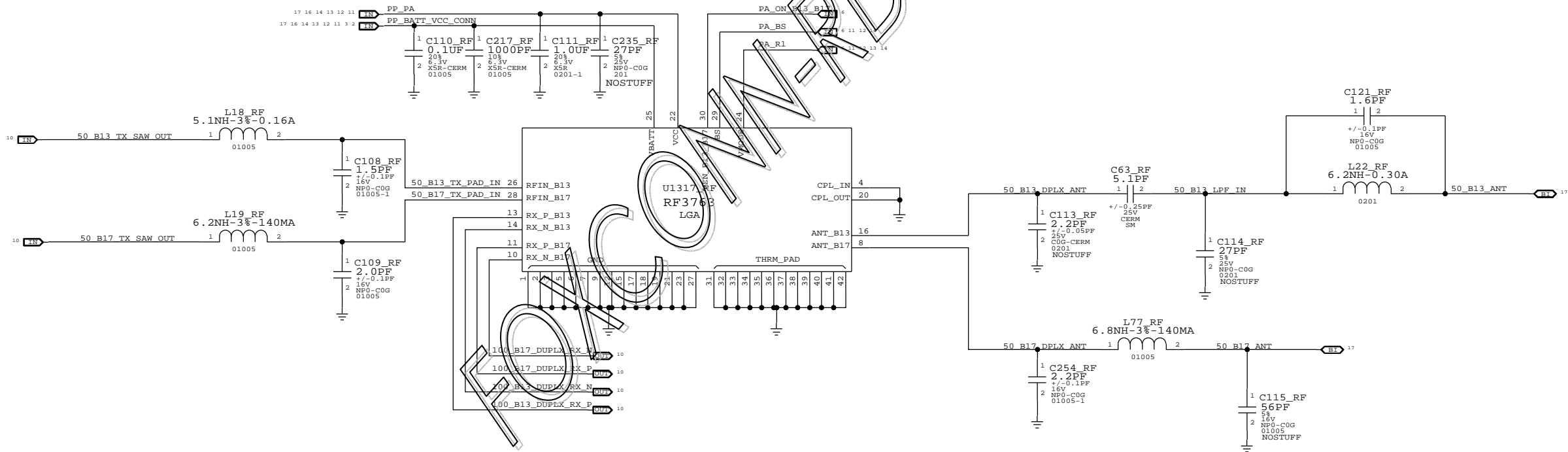
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BAND 13/17 PAD



BAND	PA	POWER	MODE	PA_BS	PA_ON_B13_B17	PA_R1
=====	=====	=====	=====	=====	=====	=====
POWER DOWN		X		0	0	0
STANDBY		X		X	0	X
B17		HPM		0	1	0
B17		LPM		0	1	1
B13		HPM		1	1	0
B13		LPM		1	1	1

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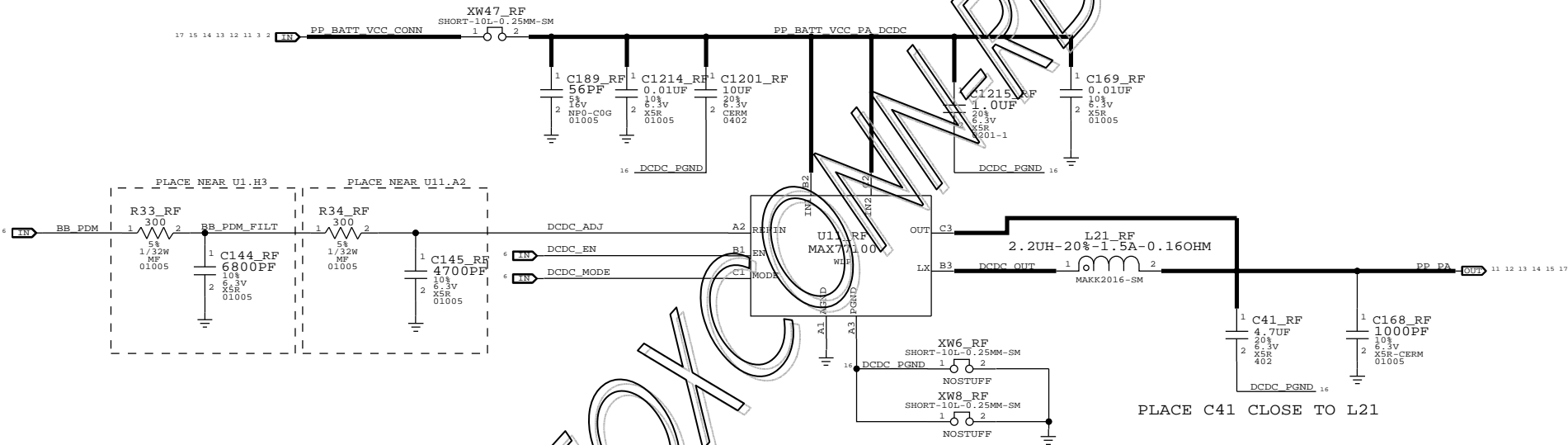
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PA DC/DC CONVERTER



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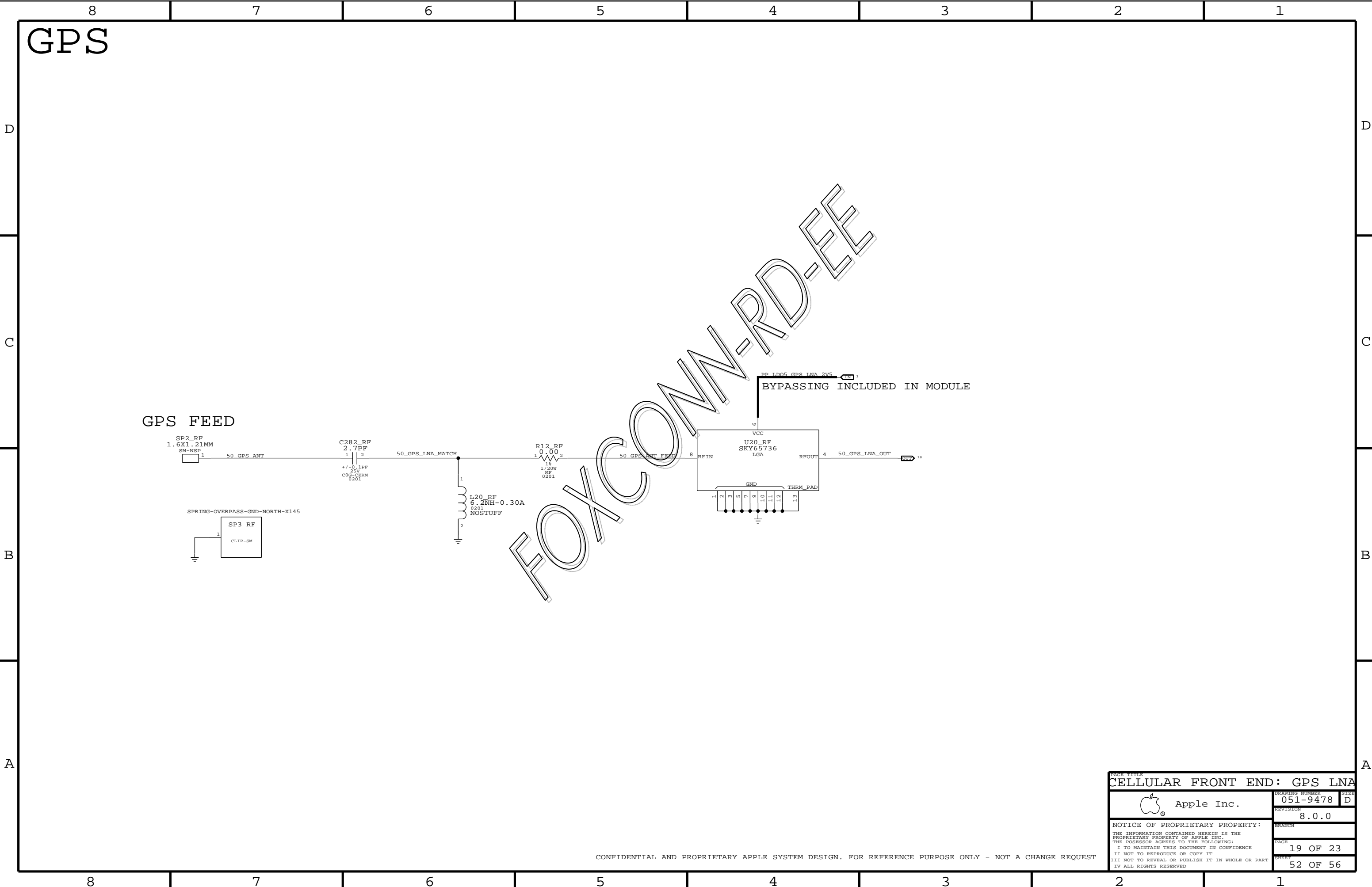
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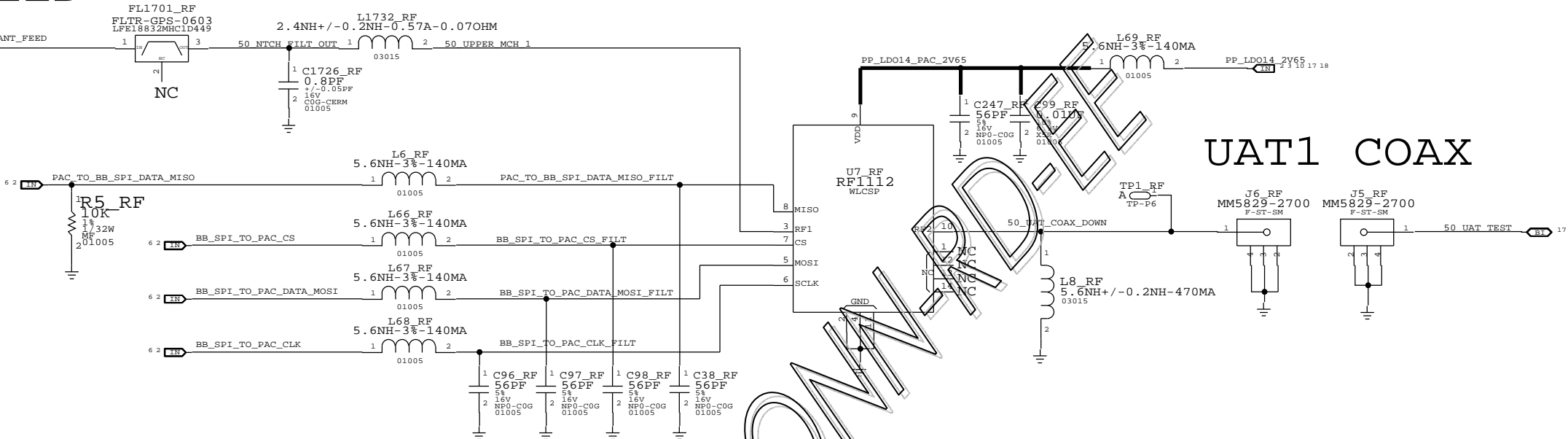
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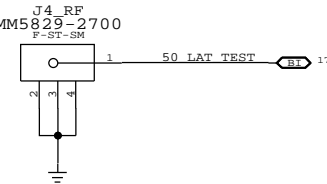
ANTENNA FEEDS

UAT1 FEED




UAT1 COAX


LAT



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2G FEM LOGIC TABLE (1 OF 2)															
D	BAND		S6	S5	S4	S3	S2	S1	S0	TX/PRX	PATH	DRX		PATH	
	LB TX, IDLE, LAT		HIGH	HIGH	HIGH	LOW	LOW	HIGH	HIGH	LAT		UAT			
	LB TX, IDLE, UAT		HIGH	HIGH	HIGH	LOW	LOW	LOW	HIGH	UAT		LAT			
	LB TX, LAT, HPM		HIGH	HIGH	HIGH	LOW	HIGH	HIGH	HIGH	LAT		UAT			
	LB TX, UAT, HPM		HIGH	HIGH	HIGH	LOW	HIGH	LOW	HIGH	UAT		LAT			
	LB TX, LAT, LPM		HIGH	HIGH	HIGH	LOW	HIGH	HIGH	LOW	LAT		UAT			
C	LB TX, UAT, LPM		HIGH	HIGH	HIGH	LOW	HIGH	LOW	LOW	UAT		LAT			
	LB TX, HIGH Z, LAT, HPM		HIGH	HIGH	LOW	LOW	HIGH	HIGH	HIGH	LAT		UAT			
	LB TX, HIGH Z, UAT, HPM		HIGH	HIGH	LOW	LOW	HIGH	LOW	HIGH	UAT		LAT			
	LB TX, HIGH Z, LAT, LPM		HIGH	HIGH	LOW	LOW	HIGH	HIGH	LOW	LAT		UAT			
	LB TX, HIGH Z, UAT, LPM		HIGH	HIGH	LOW	LOW	HIGH	LOW	LOW	UAT		LAT			
	HB TX, IDLE, LAT		HIGH	HIGH	HIGH	HIGH	LOW	HIGH	HIGH	LAT		UAT			
	HB TX, IDLE, UAT		HIGH	HIGH	HIGH	HIGH	LOW	LOW	HIGH	UAT		LAT			
	HB TX, LAT, HPM		HIGH	HIGH	HIGH	HIGH	HIGH	HIGH	HIGH	LAT		UAT			
	HB TX, UAT, HPM		HIGH	HIGH	HIGH	HIGH	HIGH	LOW	HIGH	UAT		LAT			
	HB TX, LAT, LPM		HIGH	HIGH	HIGH	HIGH	HIGH	HIGH	LOW	LAT		UAT			
	HB TX, UAT, LPM		HIGH	HIGH	HIGH	HIGH	HIGH	LOW	LOW	UAT		LAT			
	HB TX, HIGH Z, LAT, HPM		HIGH	HIGH	LOW	HIGH	HIGH	HIGH	HIGH	LAT		UAT			
	HB TX, HIGH Z, UAT, HPM		HIGH	HIGH	LOW	HIGH	HIGH	LOW	HIGH	UAT		LAT			
B	HB TX, HIGH Z, LAT, LPM		HIGH	HIGH	LOW	HIGH	HIGH	HIGH	LOW	LAT		UAT			
	HB TX, HIGH Z, UAT, LPM		HIGH	HIGH	LOW	HIGH	HIGH	LOW	LOW	UAT		LAT			
	GSM850 RX, LAT		HIGH	LOW	HIGH	HIGH	LOW	HIGH	HIGH	LAT		UAT			
	GSM850 RX, UAT		HIGH	LOW	HIGH	HIGH	LOW	LOW	HIGH	UAT		LAT			
	GSM900 RX, LAT		HIGH	LOW	HIGH	HIGH	HIGH	HIGH	HIGH	LAT		UAT			
	GSM900 RX, UAT		HIGH	LOW	HIGH	HIGH	HIGH	LOW	HIGH	UAT		LAT			
	GSM1900 RX, LAT		LOW	HIGH	LOW	HIGH	HIGH	HIGH	HIGH	LAT		UAT			
	GSM1900 RX, UAT		LOW	HIGH	LOW	HIGH	HIGH	LOW	HIGH	UAT		LAT			
A	GSM1800 RX, LAT		HIGH	LOW	LOW	HIGH	HIGH	HIGH	HIGH	LAT		UAT			
	GSM1800 RX, UAT		HIGH	LOW	LOW	HIGH	HIGH	LOW	HIGH	UAT		LAT			
	TERMINATED, UAT		HIGH	LOW	HIGH	LOW	HIGH	HIGH	HIGH	UAT		LAT			
	TERMINATED, LAT		HIGH	LOW	HIGH	LOW	HIGH	LOW	HIGH	LAT		UAT			
LAT = LOWER ANTENNA UAT = UPPER ANTENNA															
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	2G FEM LOGIC TABLE - DEV2 (2 OF 2)									
	BAND	S6	S5	S4	S3	S2	S1	S0	TX/PRX PATH	DRX PATH
D	B1/BC6, LAT	LOW	LOW	LOW	LOW	HIGH	HIGH	HIGH	LAT	UAT
	B1/BC6, UAT	LOW	LOW	LOW	LOW	HIGH	LOW	HIGH	UAT	LAT
	B2/B25/BC1, LAT	LOW	HIGH	LOW	LOW	HIGH	HIGH	HIGH	LAT	UAT
	B2/B25/BC1, UAT	LOW	HIGH	LOW	LOW	HIGH	LOW	HIGH	UAT	LAT
C	B3, LAT	HIGH	LOW	LOW	LOW	HIGH	HIGH	HIGH	LAT	UAT
	B3, UAT	HIGH	LOW	LOW	LOW	HIGH	LOW	HIGH	UAT	LAT
	B4/BC15, LAT	LOW	LOW	LOW	LOW	HIGH	HIGH	HIGH	LAT	UAT
	B4/BC15, UAT	LOW	LOW	LOW	LOW	HIGH	LOW	HIGH	UAT	LAT
	B5/B6/B18/BC0/BC10, LAT	LOW	LOW	HIGH	LOW	HIGH	HIGH	HIGH	LAT	UAT
	B5/B6/B18/BC0/BC10, UAT	LOW	LOW	HIGH	LOW	HIGH	LOW	HIGH	UAT	LAT
	B8, LAT	LOW	LOW	LOW	HIGH	HIGH	HIGH	HIGH	LAT	UAT
	B8, UAT	LOW	LOW	LOW	HIGH	HIGH	LOW	HIGH	UAT	LAT
	B13, LAT	LOW	HIGH	HIGH	HIGH	HIGH	HIGH	HIGH	LAT	UAT
	B13, UAT	LOW	HIGH	HIGH	HIGH	HIGH	LOW	HIGH	UAT	LAT
	B17, LAT	LOW	HIGH	HIGH	HIGH	LOW	HIGH	HIGH	LAT	UAT
	B17, UAT	LOW	HIGH	HIGH	HIGH	LOW	LOW	HIGH	UAT	LAT
	B20, LAT	LOW	HIGH	HIGH	LOW	HIGH	HIGH	HIGH	LAT	UAT
	B20, UAT	LOW	HIGH	HIGH	LOW	HIGH	LOW	HIGH	UAT	LAT
	OFF	LOW	LOW	HIGH	HIGH	X	X	X	X	X
	STANDBY	LOW	LOW	LOW	LOW	LOW	LOW	LOW	X	X
B	LAT = LOWER ANTENNA									
	UAT = UPPER ANTENNA									
	OFF = LOWEST POWER STATE WITHOUT REMOVING LDO14_2V65 POWER									
	STANDBY = ADDED TO SUPPORT EXISTING SW ARCHITECTURE. NOT TO BE USED AS A LOW POWER STATE.									
A										
	8	7	6	5	4	3	2	1		

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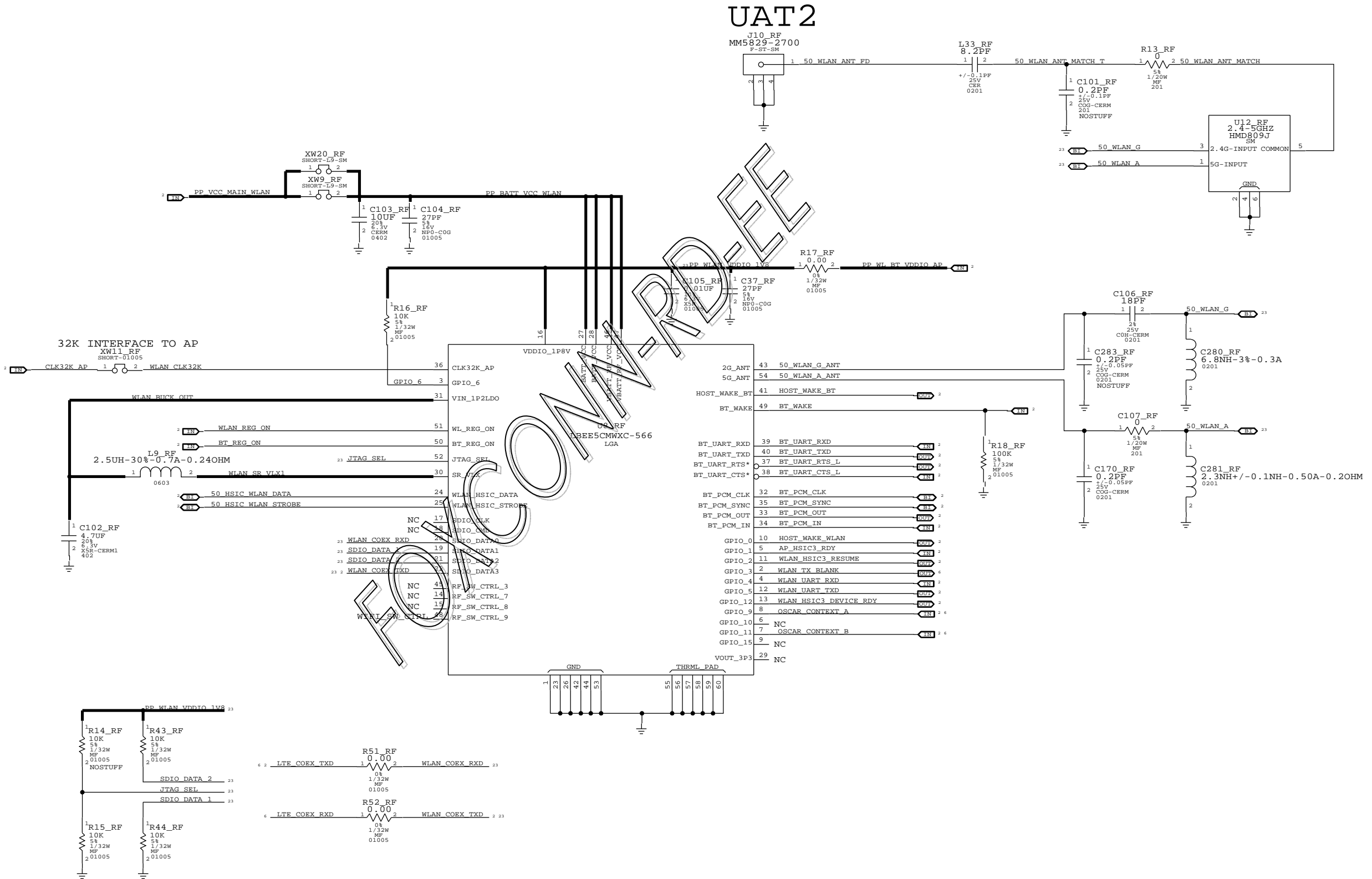
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
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WLAN / BT



PULL-UP ON GPIO6, SDIO_DATA_2 & PULL-DOWN ON SDIO_DATA_1 REQUIRED FOR HSIC BOOTSTRAPPING

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