1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ECN	DESCRIPTION OF REVISION	CK APPD DATE
8	0006532879	ENGINEERING RELEASED	2016-07-06

D11 MLB - PVT

LAST_MODIFICATION=Wed Jul 6 08:55:26 2016

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Schematic & PCB Callouts

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
051-00482	1	SCH,MLB,D11-12	SCH	CRITICAL	?
820-00229	1	PCBF,MLB,D11-12	PCB	CRITICAL	?

SCH 051-00482
BRD 820-00229
MCO 056-01585

System Block Diagram:

<rdar://problem/16684269>

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NAND BOM Options

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
335S00169	1	NAND,8,32GB,16nm,MLC	U1701	CRITICAL	NAND_32G
335S00182	1	NAND,8,128GB,15nm,TLC	U1701	CRITICAL	NAND_128G
335S00183	1	NAND,T,256GB,3Dx3,TLC	U1701	CRITICAL	NAND_256G
138S0867	5	[illegible]	[illegible]	CRITICAL	NAND_32G
138S00003	5	[illegible]	[illegible]	CRITICAL	NAND_128G
138S00003	5	[illegible]	[illegible]	CRITICAL	NAND_256G

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
335S00201	335S00169	ALTERNATE	U1701	T,15nm,MLC,32GB
335S00188	335S00169	ALTERNATE	U1701	S,16nm,MLC,32GB
335S00195	335S00182	ALTERNATE	U1701	SS,15nm,TLC,128GB
335S00186	335S00182	ALTERNATE	U1701	T,15nm,TLC,128GB
335S00179	335S00182	ALTERNATE	U1701	SD,15nm,TLC,128GB
335S00148	335S00183	ALTERNATE	U1701	SD,3Dx2,TLC,256GB
335S00198	335S00183	ALTERNATE	U1701	SS,3Dx3,TLC,256GB

#22686038:See Radar

Magnesium Alternates

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
338S00173	338S00203	ALTERNATE	U2402	[illegible]

UT LDO Alternates

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
353S00889	353S00015	ALTERNATE	U2501	[illegible]

Mamba LDO Alternates

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
353S00932	353S00576	ALTERNATE	U3801	[illegible]

I2C5 Alternate

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
335S00234	335S00233	ALTERNATE	U5101	[illegible]

Active Diode Alternate

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
376S00186	376S00067	ALTERNATE	Q2101	DIODES INC. ACT DIODE

DDR PLL Alternate

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
155S00095	155S00868	ALTERNATE	FL1501	[illegible]

Power Inductor Alternates

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:	
152S00118	152S00075	ALTERNATE	ALL	[illegible]	
152S00077	152S00397	ALTERNATE	ALL	[illegible]	updated 11/12
152S00121	152S00081	ALTERNATE	ALL	[illegible]	
152S00123	152S21036	ALTERNATE	ALL	[illegible]	
152S00482	152S00366	ALTERNATE	ALL	[illegible]	updated 11/12
152S00297	152S21845	ALTERNATE	ALL	[illegible]	reverted 11/13
152S00365	152S00297	ALTERNATE	ALL	[illegible]	
152S00398	152S00204	ALTERNATE	ALL	[illegible]	For Chestnut inductor: so it doesn't interfere with PMU inductor Buck 7 alts
152S00120	152S00077	ALTERNATE	ALL	[illegible]	[illegible]
152S00117	152S00074	ALTERNATE	[illegible]	[illegible]	

Global R/C Alternates

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
118S0764	118S0717	ALTERNATE	ALL	[illegible]
138S0702	138S0457	ALTERNATE	ALL	[illegible]
138S00008	138S0835	ALTERNATE	ALL	[illegible]
138S0648	138S0652	ALTERNATE	ALL	[illegible]
132S0400	132S0436	ALTERNATE	ALL	[illegible]
138S00024	138S0986	ALTERNATE	ALL	[illegible]
138S0706	138S0739	ALTERNATE	ALL	[illegible]
138S0945	138S0739	ALTERNATE	ALL	[illegible]
132S0436	132S0460	ALTERNATE	ALL	[illegible]

Global Ferrite Alternates

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
155S00067	155S0581	ALTERNATE	ALL	[illegible]
155S0581	155S00067	ALTERNATE	ALL	[illegible]
155S00012	155S00168	ALTERNATE	ALL	[illegible]
155S00194	155S0610	ALTERNATE	ALL	[illegible]
155S00280	155S0610	ALTERNATE	ALL	[illegible]
152S00489	152S00456	ALTERNATE	ALL	[illegible]

Global Varistor Alternates

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
377S0168	377S0140	ALTERNATE	ALL	[illegible]

Acc Buck Alternates

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
371S00087	371S00064	ALTERNATE	U2703	[illegible]
152S00958	152S00557	ALTERNATE	L2703	[illegible]
376S00166	376S00164	ALTERNATE	[illegible]	[illegible]

Load Switch OMIT TABLE

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
353S01007	1	[illegible]	U2710,MTCSM_RF	CRITICAL	

N&V 15uF Cap Alternates

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
138S00005	138S00003	ALTERNATE	[illegible]	[illegible]
138S00005	138S00003	ALTERNATE	[illegible]	[illegible]
138S00005	138S00003	ALTERNATE	[illegible]	[illegible]
138S00005	138S00003	ALTERNATE	[illegible]	[illegible]
138S00005	138S00003	ALTERNATE	[illegible]	[illegible]
138S00005	138S00003	ALTERNATE	[illegible]	[illegible]
138S00005	138S00003	ALTERNATE	[illegible]	[illegible]
138S00005	138S00003	ALTERNATE	[illegible]	[illegible]
138S00005	138S00003	ALTERNATE	[illegible]	[illegible]
138S00005	138S00003	ALTERNATE	[illegible]	[illegible]
138S00048	138S00003	ALTERNATE	ALL	[illegible]

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D11 EEEE CALLOUTS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
825-6838	1	[illegible]	EEEE_GY2T	CRITICAL	EEEE_D11_BEST_JP
825-6838	1	[illegible]	EEEE_GY2V	CRITICAL	EEEE_D11_SUPREME_JP
825-6838	1	[illegible]	EEEE_GY2W	CRITICAL	EEEE_D11_EXTREME_JP
825-6838	1	[illegible]	EEEE_H3RN	CRITICAL	EEEE_D11_BEST_ROW
825-6838	1	[illegible]	EEEE_H3RP	CRITICAL	EEEE_D11_SUPREME_ROW
825-6838	1	[illegible]	EEEE_H3RQ	CRITICAL	EEEE_D11_EXTREME_ROW
825-6838	1	[illegible]	EEEE_HRC1	CRITICAL	EEEE_D11_BEST_CH
825-6838	1	[illegible]	EEEE_HRCR	CRITICAL	EEEE_D11_SUPREME_CH
825-6838	1	[illegible]	EEEE_HRC3	CRITICAL	EEEE_D11_EXTREME_CH

CAYMAN DDR Alternates

339S00258	339S00257	ALTERNATE	ALL	DDR-E, 3G, H1

CAYMAN OMIT TABLE

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
339S00257	1	CAYMAN, DDR-E, 3G, H1	U0703	CRITICAL	

2.2uF CAP Alts

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
138S00069	138S0032	ALTERNATE	ALL	[illegible]
138S0831	138S0032	ALTERNATE	ALL	[illegible]

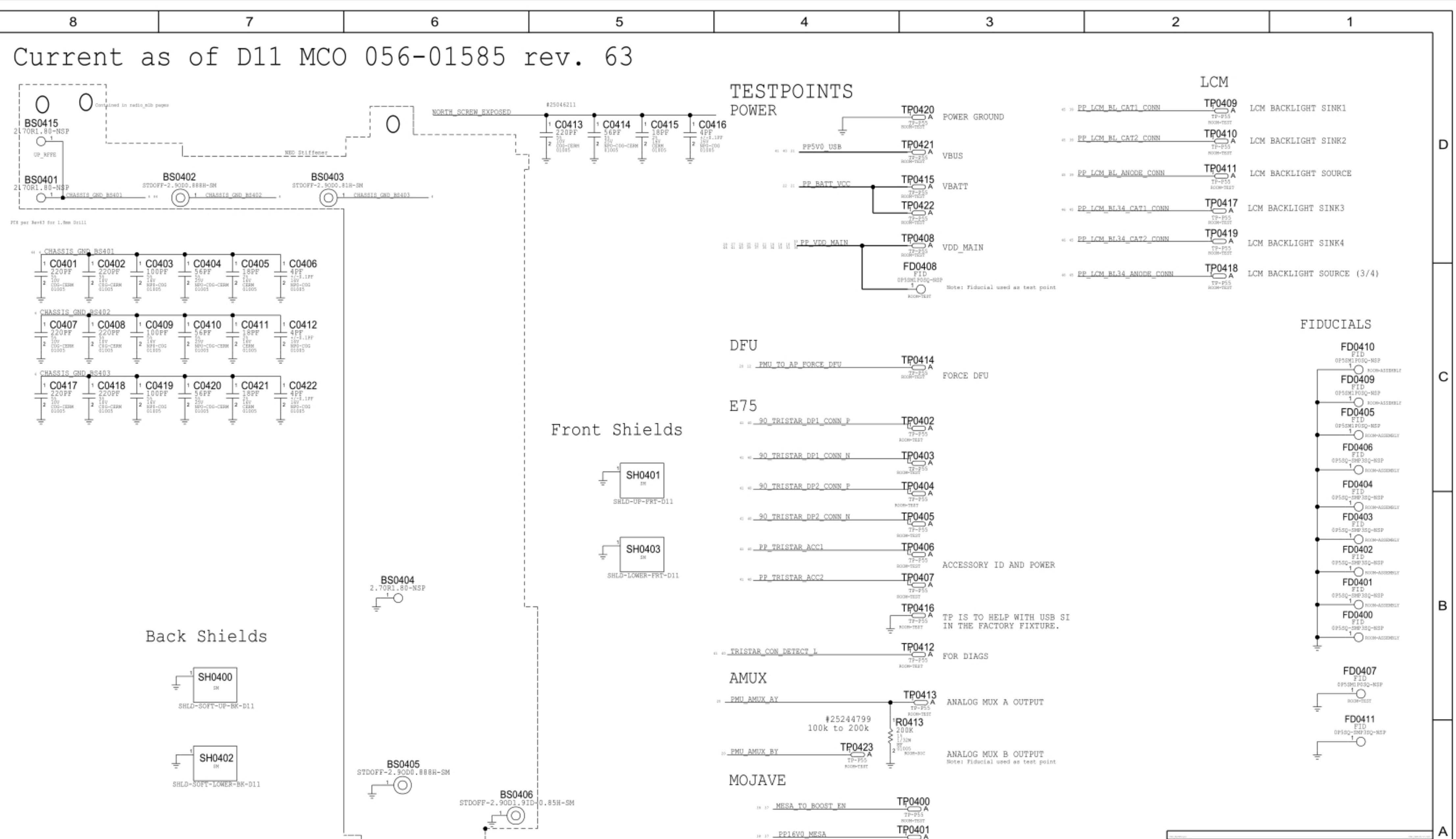
SIP Alternates

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
339M00009	339M00003	ALTERNATE	ALL	TRINITY BLUE,STATS
339M0000?	339M00002	ALTERNATE	ALL	NHG,STATS

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Current as of D11 MCO 056-01585 rev. 63

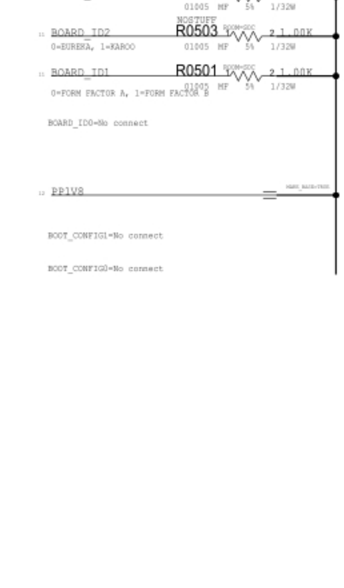


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BOOTSTRAPPING:BOARD REV
BOARD ID
BOOT CONFIG



BOARD_REV[3:0]
FLOAT=LOW, PULLUP=HIGH
1111 Pre-Proto w/D520 (non enclosure)
1110 PROTO1
1101 PROTO2
1100 PROTO2.5
1011 EVT
xxxx SPARE
1000 CARRIER
xxxx SPARE
SELECTED --> 0010 DVT
xxxx SPARE
0000 PVT

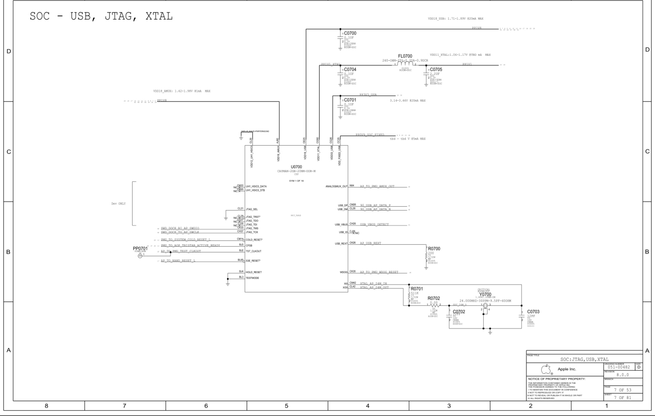
BOARD_ID[4:0]
FLOAT=LOW, PULLUP=HIGH
01000 D10 MLB
01001 D10 DEV
SELECTED --> 01010 D11 MLB
01011 D11 DEV
01100 D101 MLB
01101 D101 DEV
01110 D111 MLB
01111 D111 DEV
0=MLB, 1=DEV
0=FORM FACTOR A, 1=FORM FACTOR B
0=EUREKA, 1=KAROO

BOOT_CONFIG[2:0]
FLOAT=LOW, PULLUP=HIGH
000 SPI0
001 SPI0 TEST MODE
010 NVMES_X2
011 NVMES X2 TEST
SELECTED --> 100 NVMES X1
101 NVMES X1 TEST
110 SLOW SPI0 TEST
111 FAST SPI0 TEST

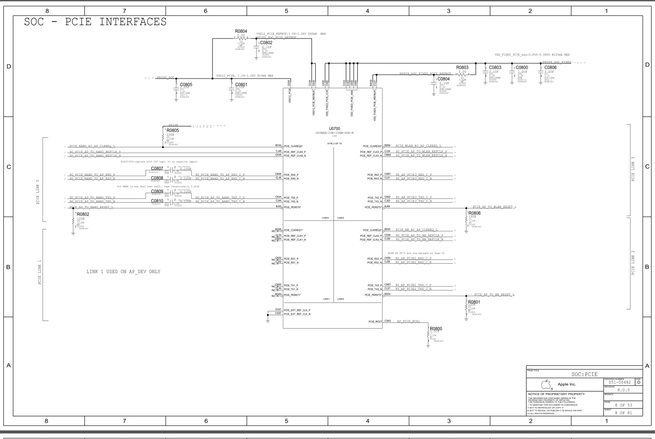
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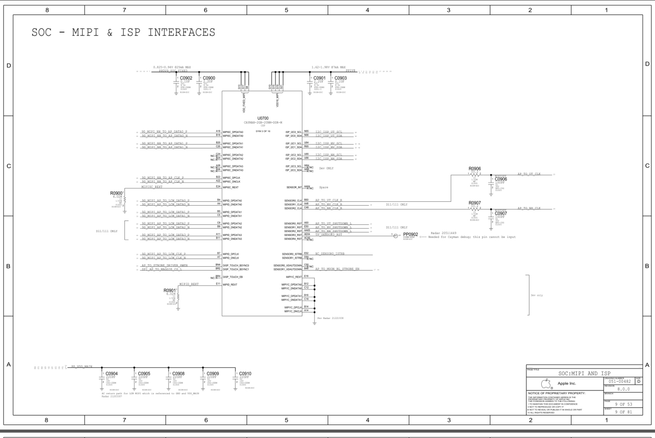
SOC - USB, JTAG, XTAL

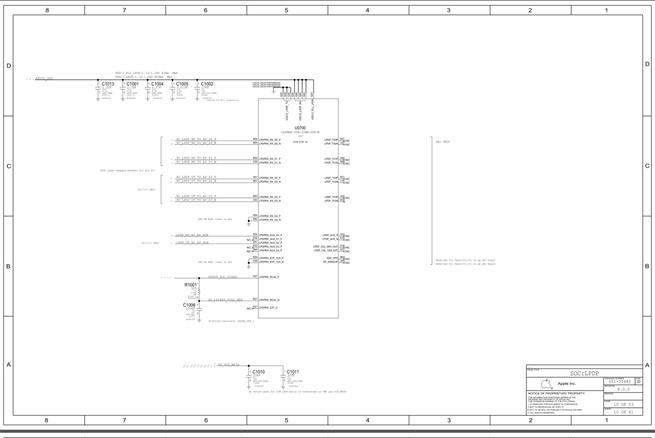


SOC - PCIE INTERFACES

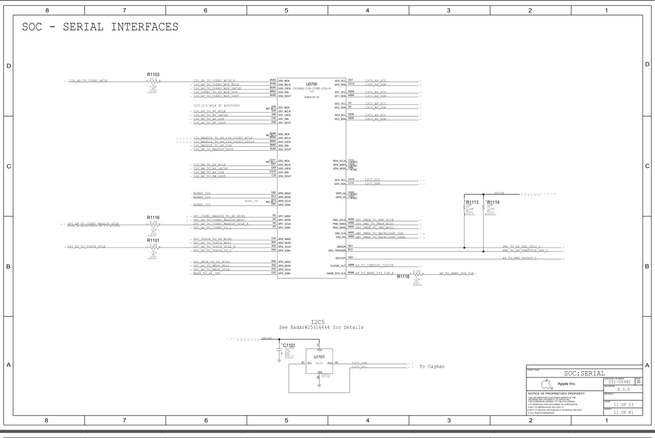


SOC - MIPI & ISP INTERFACES

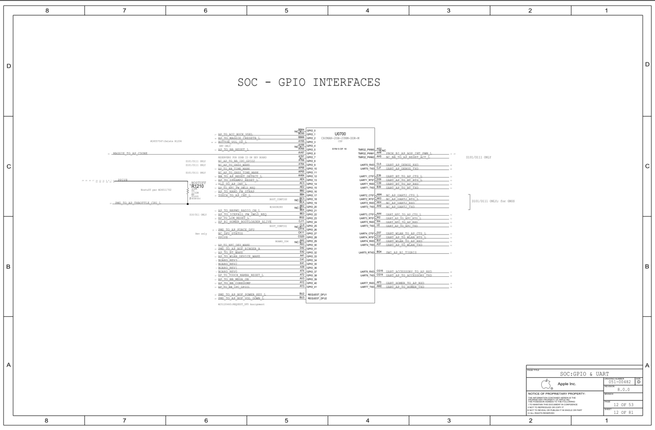




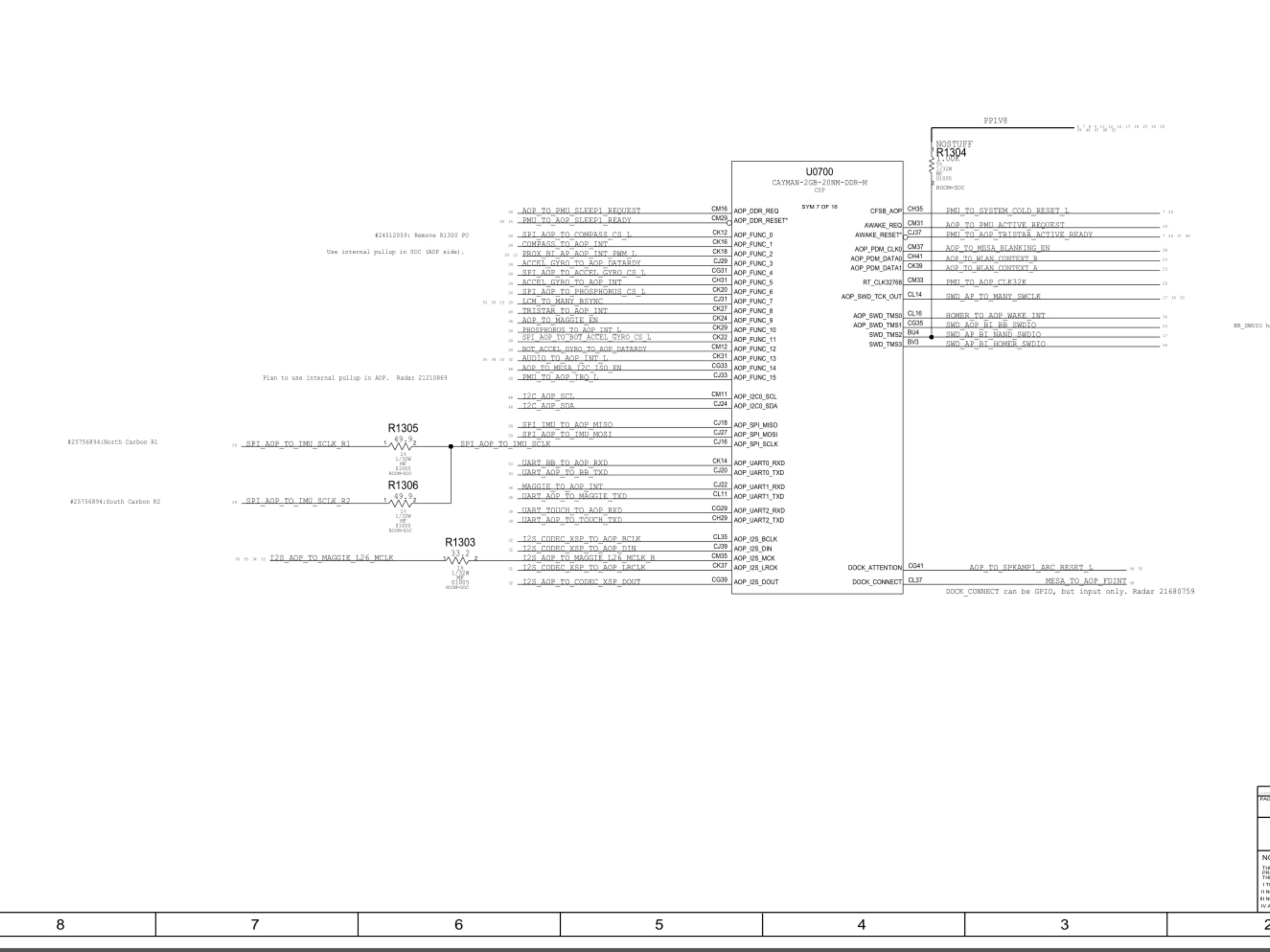
SOC - SERIAL INTERFACES



SOC - GPIO INTERFACES



SOC - AOP



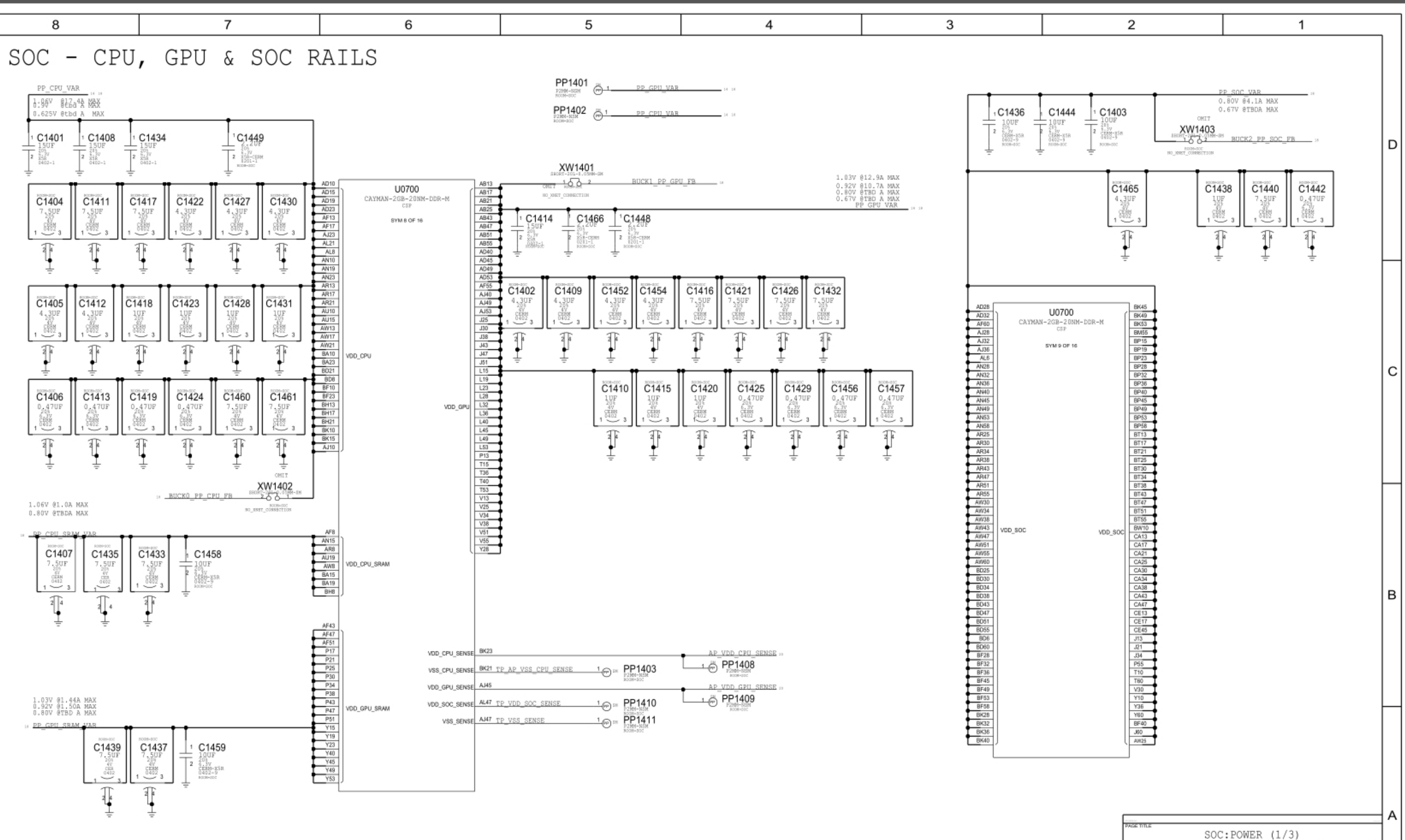
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SOC - CPU, GPU & SOC RAILS

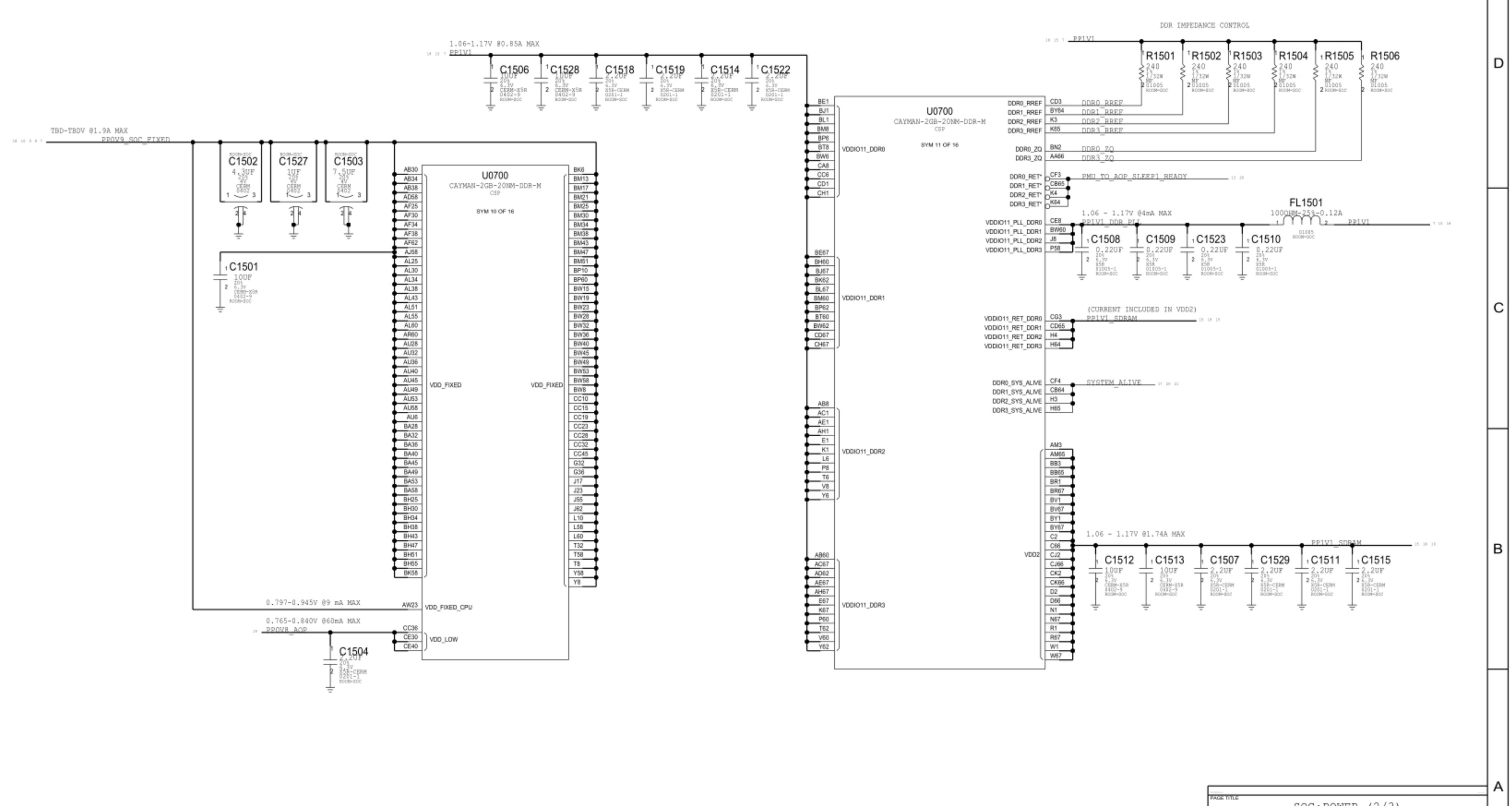


SOC:POWER (1/3)

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SOC - POWER SUPPLIES

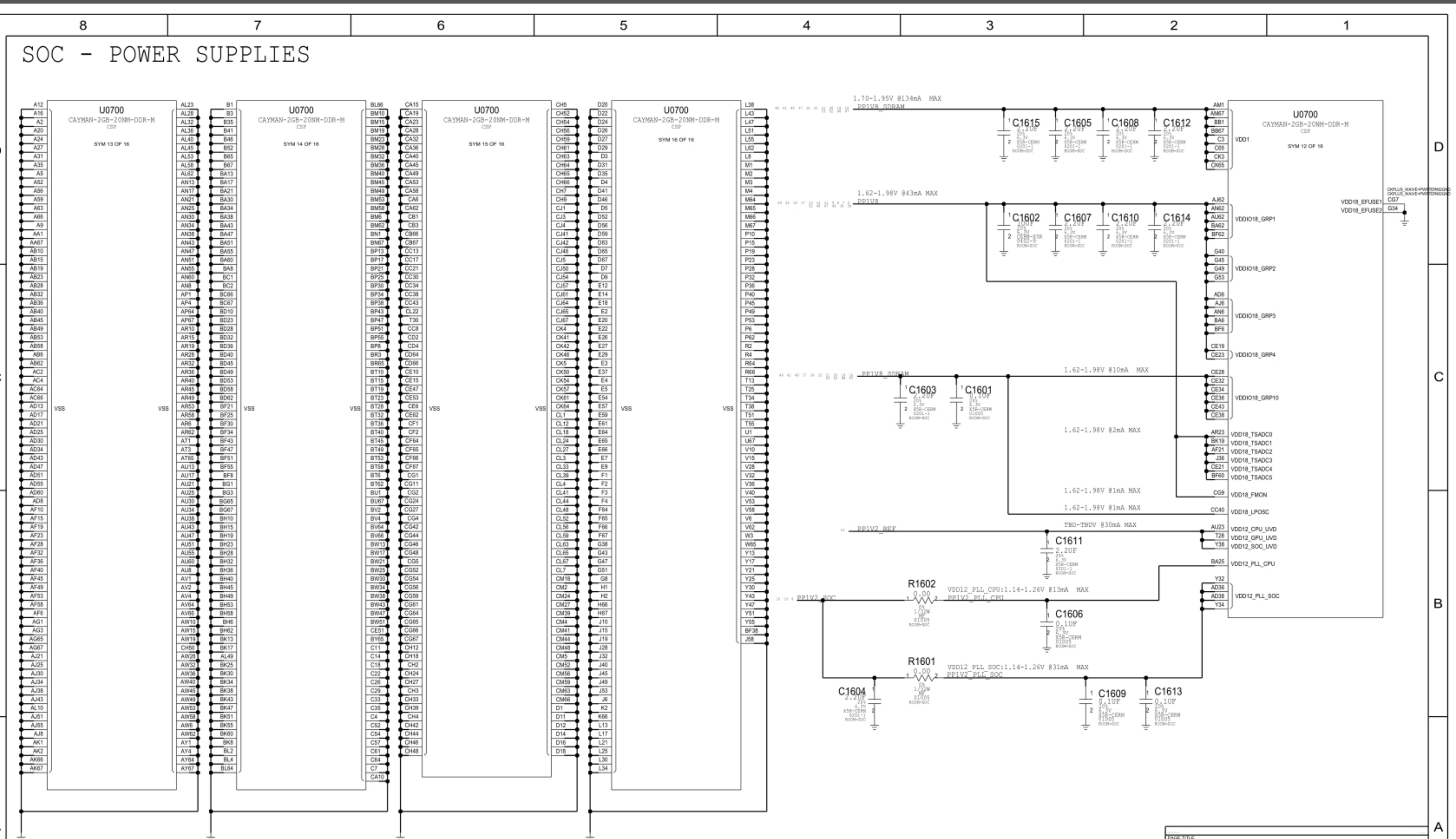


SOC:POWER (2/3)

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SOC - POWER SUPPLIES

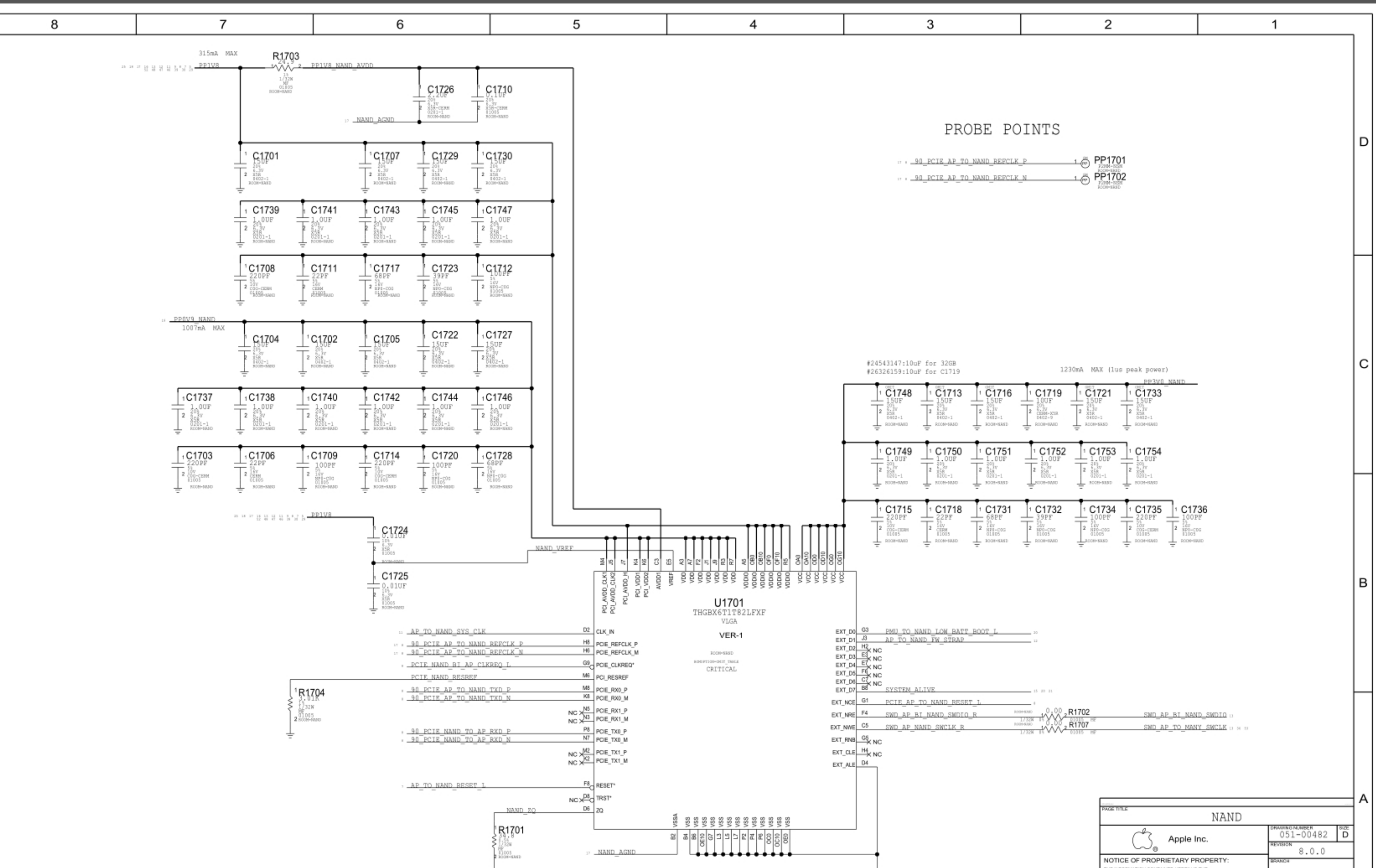


SOC:POWER (3/3)

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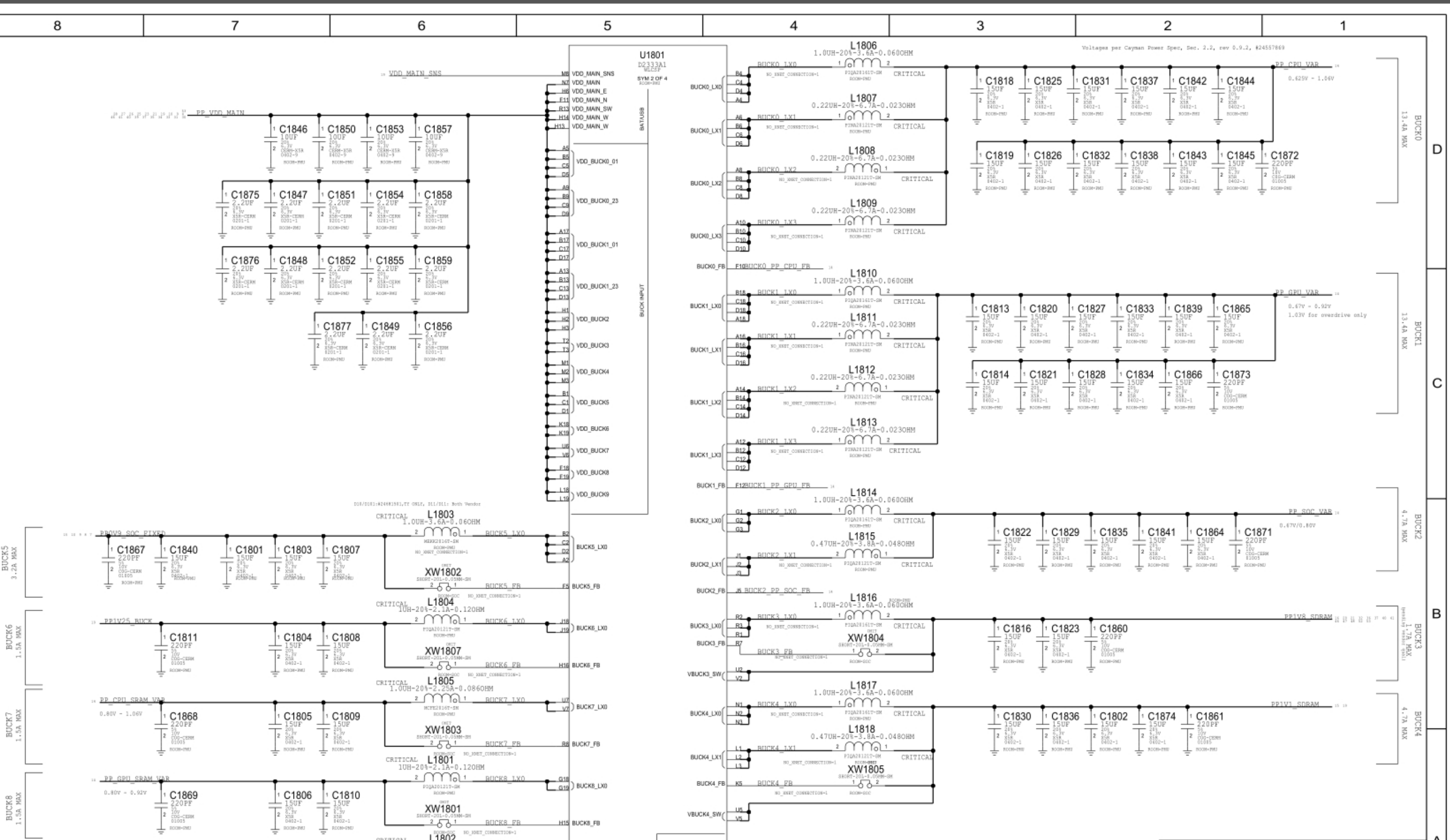
PROBE POINTS



NAND

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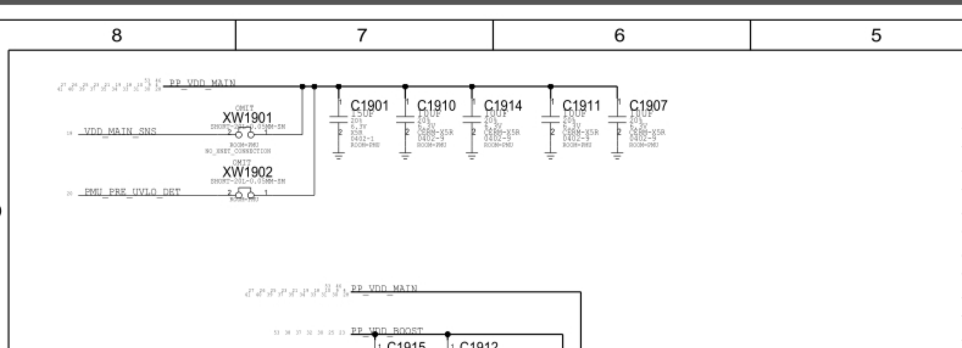
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SYSTEM POWER:PMU (1/3)

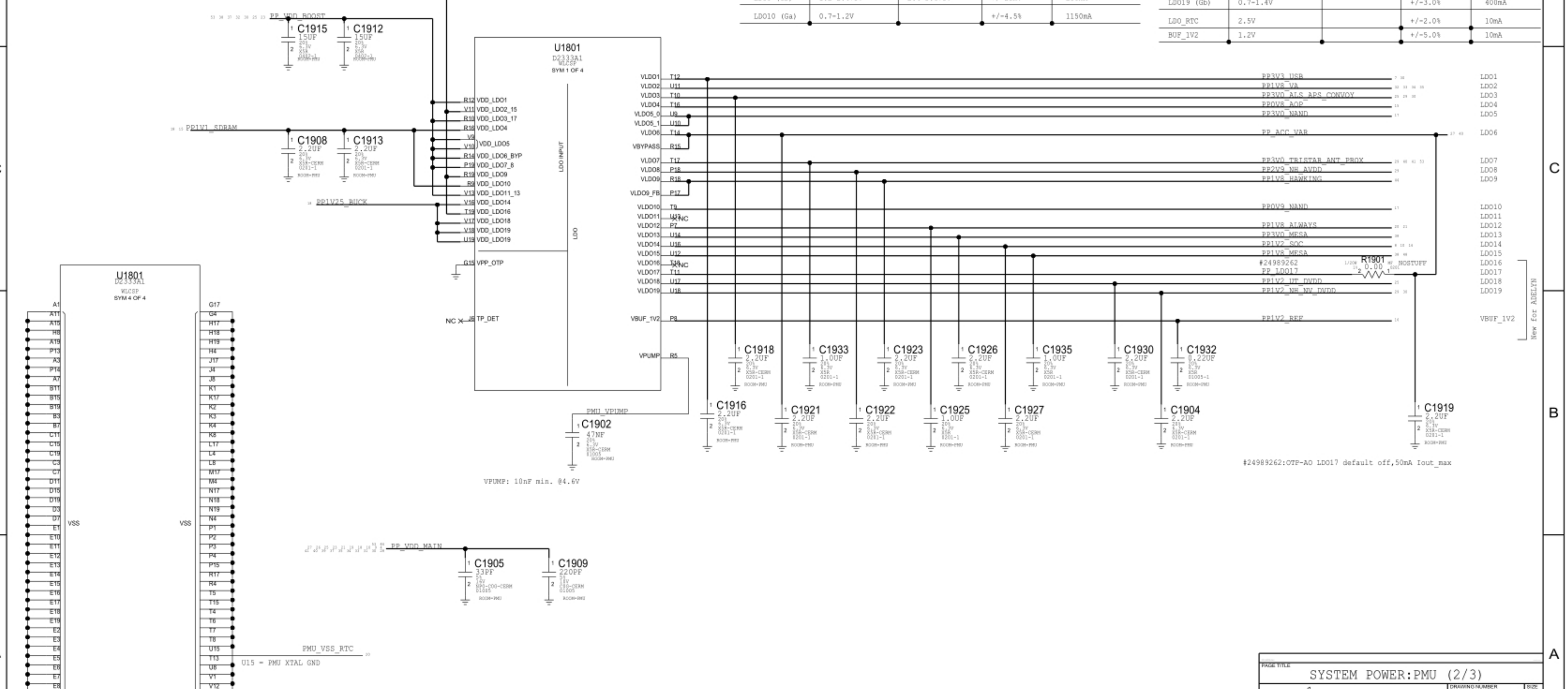
Apple Inc.

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ADELYN LDO SPECS

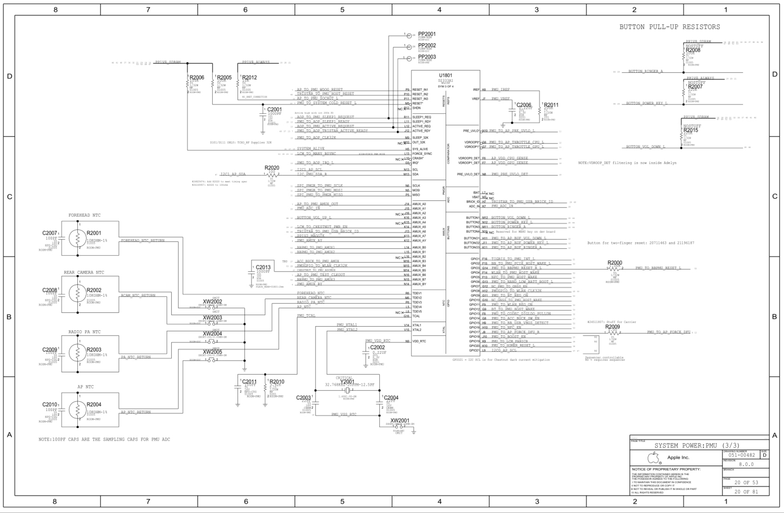
LDO#	ADJ.RANGE, LOW	ADJ.RANGE, HI	ACCURACY	MAX.CURRENT
LDO1 (Ca)	1.2-2.475V	2.4-3.675V	+/-1.4%	50mA
LDO2 (Ca)	1.2-2.475V	2.4-3.675V	+/-2.5%	50mA
LDO3 (Ca)	1.2-2.475V	2.4-3.675V	+/-2.5%	50mA
LDO4 (D)	0.7-1.2V		+/-2.5%	60mA
LDO5 (F)	2.5-3.6V(tbc)		+/-75mV	1000mA
LDO6 (Cb)	1.2-2.475V	2.4-3.675V	+/-2.5%	350mA
LDO7 (Cb)	1.2-2.475V	2.4-3.675V	+/-30mV	250mA
LDO8 (Cb)	1.2-2.475V	2.4-3.675V	+/-30mV	250mA
LDO9 (Cb)	1.2-2.475V	2.4-3.675V	+/-25mV	250mA
LDO10 (Ga)	0.7-1.2V		+/-4.5%	1150mA
LDO11 (Cb)	1.2-2.475V	2.4-3.675V	+/-30mV	250mA
LDO12 (Ca)	1.8V		+/-5%	10mA
LDO13 (Cb)	1.2-2.475V	2.4-3.675V	+/-30mV	250mA
LDO14 (Gb)	0.7-1.4V		+/-3.0%	400mA
LDO15 (Ca)	1.2-2.475V	2.4-3.675V	+/-2.5%	50mA
LDO16 (Cb)	1.2-2.475V	2.4-3.675V	+/-30mV	250mA
LDO17 (Ca)	1.2-2.475V	2.4-3.675V	+/-2.5%	50mA
LDO18 (Gb)	0.7-1.4V		+/-3.0%	400mA
LDO19 (Gb)	0.7-1.4V		+/-3.0%	400mA
LDO_RTC	2.5V		+/-2.0%	10mA
BUF_1V2	1.2V		+/-5.0%	10mA

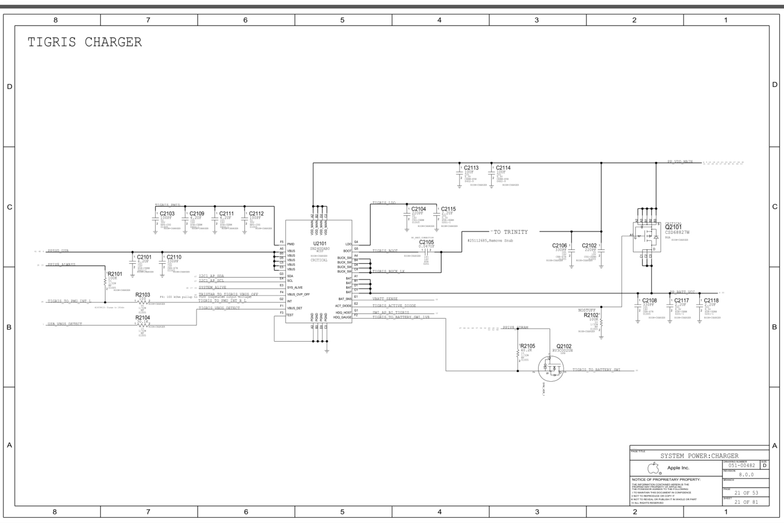


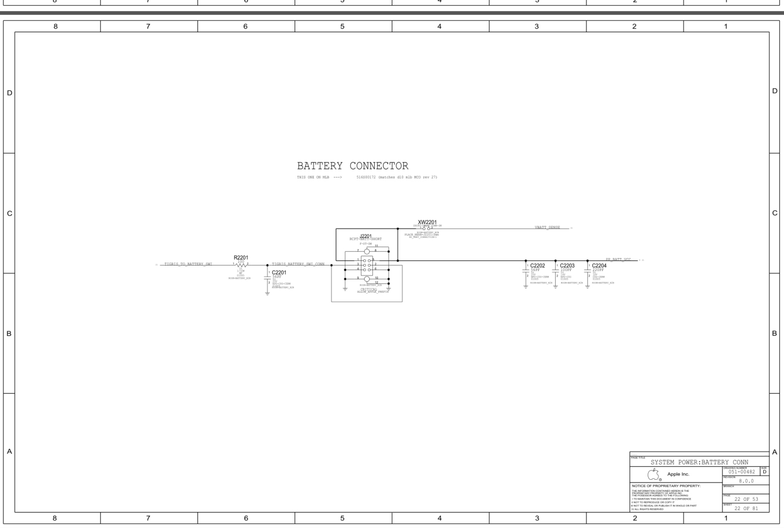
SYSTEM POWER:PMU (2/3)

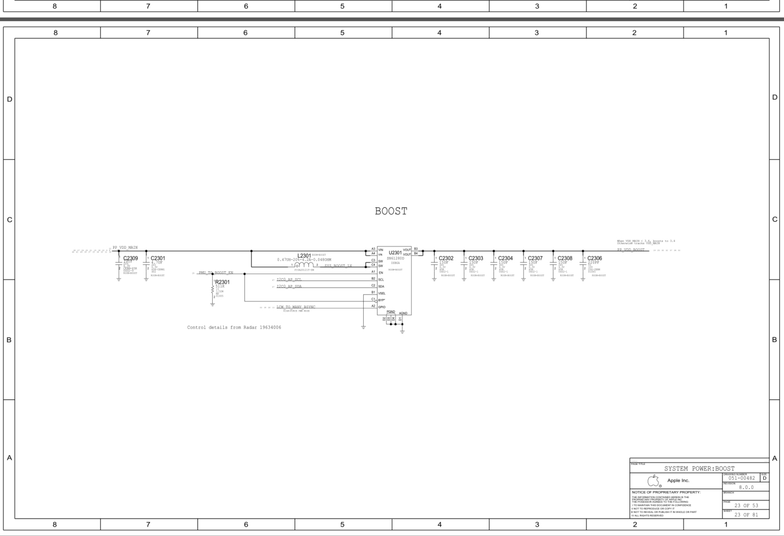
Apple Inc.

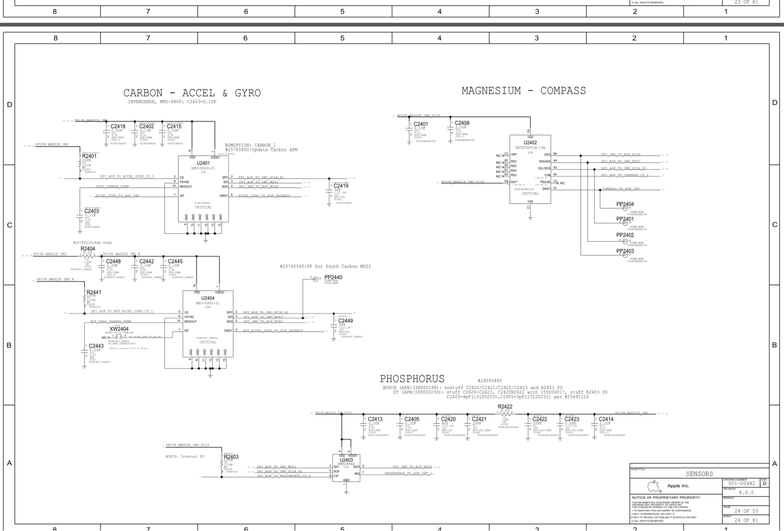
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BUTTON PULL-UP RESISTORS
FOR CHARGER NTC
REAR-CAMERA NTC
RADIO PA NTC
AP NTC
NOTE:100PF CAPS ARE THE SAMPLING CAPS FOR PMU ADC
SYSTEM POWER:PMU (3/3)
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TIGRIS CHARGER
TO TRINITY
SYSTEM POWER:CHARGER
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BATTERY CONNECTOR
SYSTEM POWER:BATTERY CONN
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BOOST
SYSTEM POWER:BOOST
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CARBON - ACCEL & GYRO
INVENSENSE, MPU-6500; C2403=0.1UF
MAGNESIUM - COMPASS
PHOSPHORUS
SENSORS
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UTAH POWER

IO FILTERS

LPDF FILTERS

Apple Inc.	051-00441	D
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Title: B2B FILTERS: UTAH

STROBE DRIVERS INSIDE NEO SIP MODULE

Apple Inc.	051-00441	D
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Title: CAMERA|STROBE DRIVER

ACCESSORY BUCK

Apple Inc.	051-00441	D
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Title: Accessory| Buck Circuits

NEW HAMPSHIRE POWER

PROX, ALS, & CONVOY POWER

NEW HAMPSHIRE I/O

SPEAKER2

CONVOY I/O

MIC3

PROX/ALS I/O

Apple Inc.	051-00441	D
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Title: B2B|FOREMAN

NEVADA POWER

NEVADA CONNECTOR

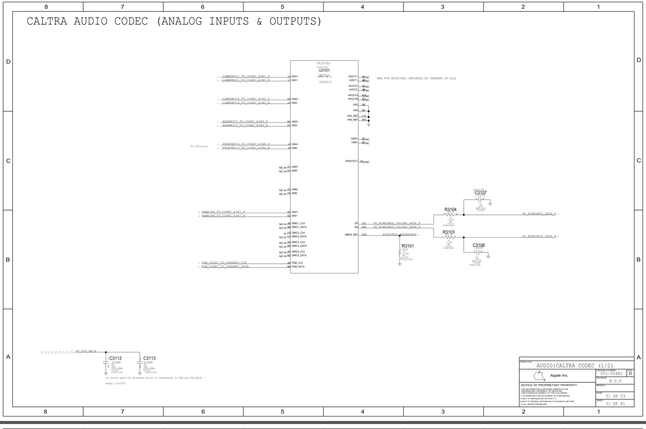
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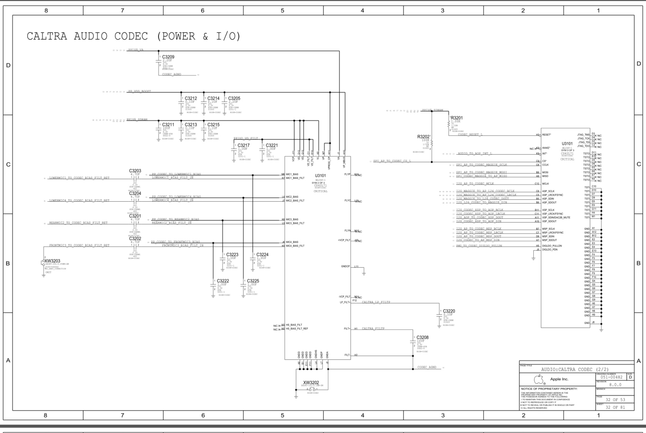
GPIO FILTERS

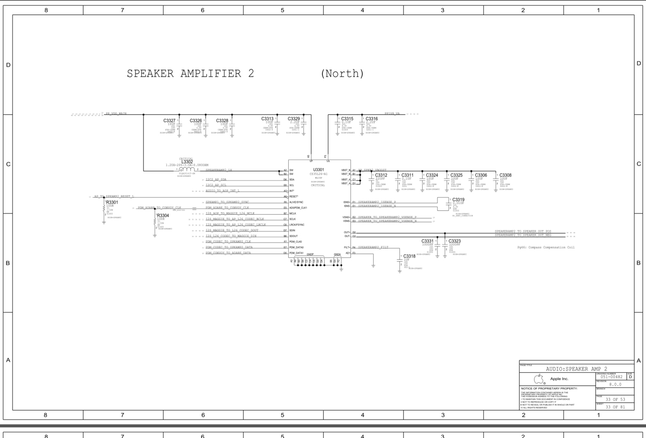
LPDF FILTERS

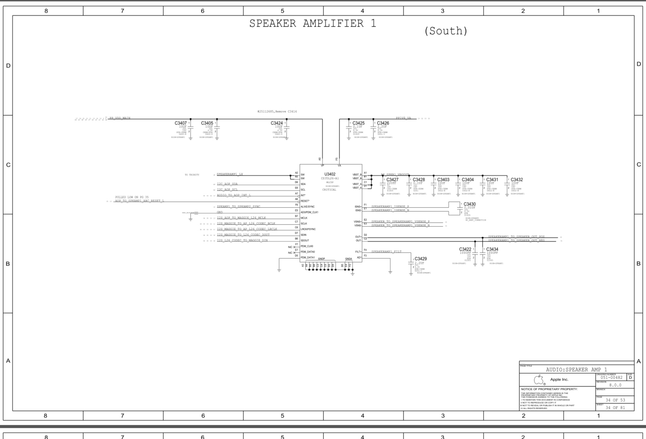
B2B|NEVADA

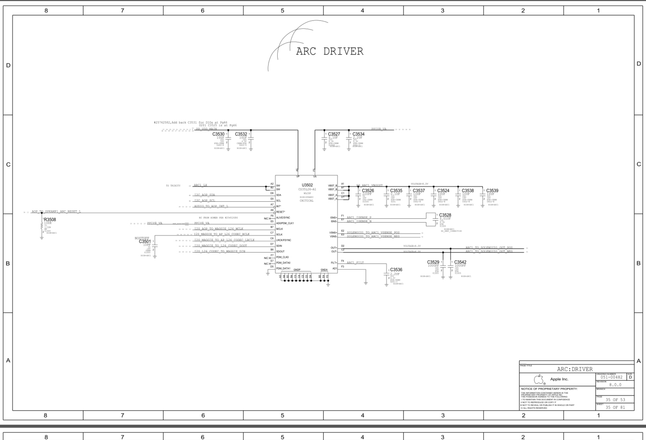
Apple Inc.	051-00441	D
	REVISION	0.0.0
NOTICE OF PROPRIETARY PROPERTY:	SHEET	19 OF 53
	19 OF 81	

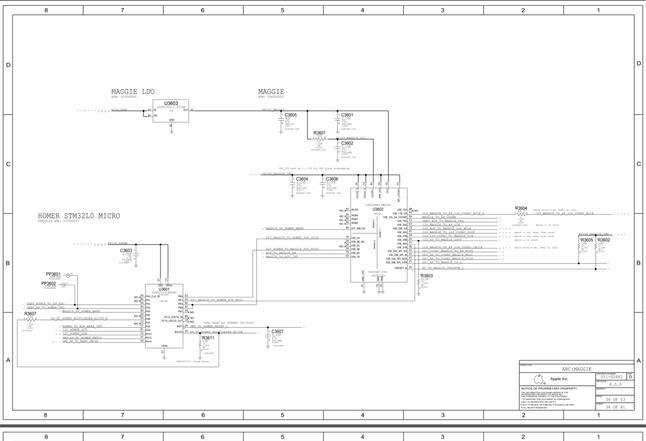

CALTRA AUDIO CODEC (ANALOG INPUTS & OUTPUTS)


CALTRA AUDIO CODEC (POWER & I/O)


SPEAKER AMPLIFIER 2
(North)

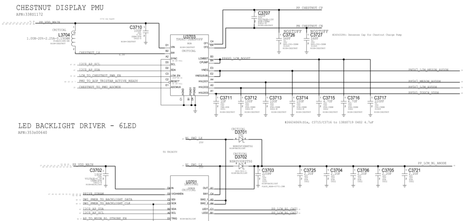

SPEAKER AMPLIFIER 1
(South)


ARC DRIVER

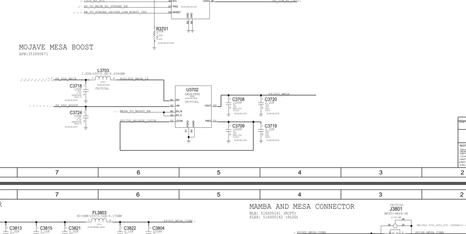

MAGGIE LDO
MAGGIE
HOMER STM32L0 MICRO

DISPLAY & TOUCH - POWER SUPPLIES

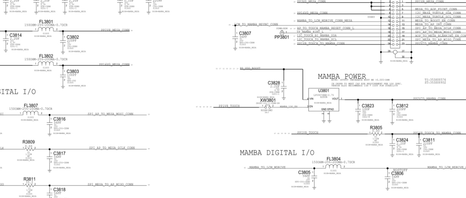
CHESTNUT DISPLAY PMU



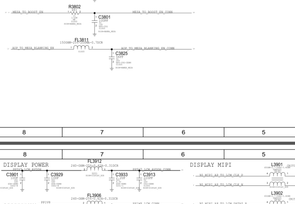
LED BACKLIGHT DRIVER - 6LED



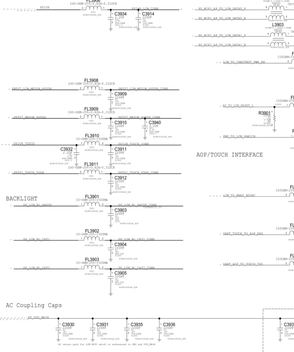
MOJAVE MESA BOOST



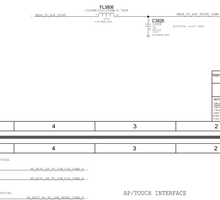
MESA POWER



MESA DIGITAL I/O



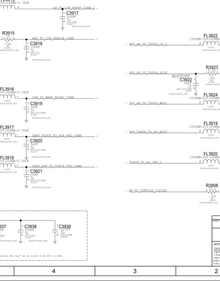
MAMBA AND MESA CONNECTOR



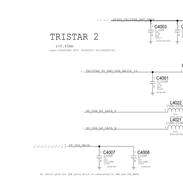
MAMBA POWER



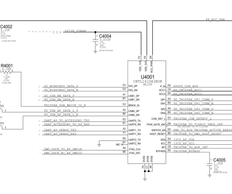
MAMBA DIGITAL I/O



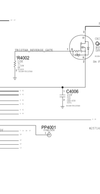
DISPLAY POWER



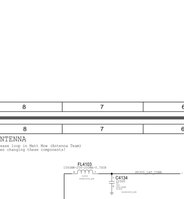
DISPLAY MIPI



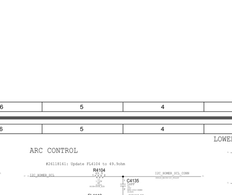
AP/TOUCH INTERFACE



BACKLIGHT



AOP/TOUCH INTERFACE



AC Coupling Caps



TRISTAR 2

ANTENNA

Please log in Mark Now (Antenna Team)
when changing these components

ARC CONTROL

ARC1

Antenna GPIO

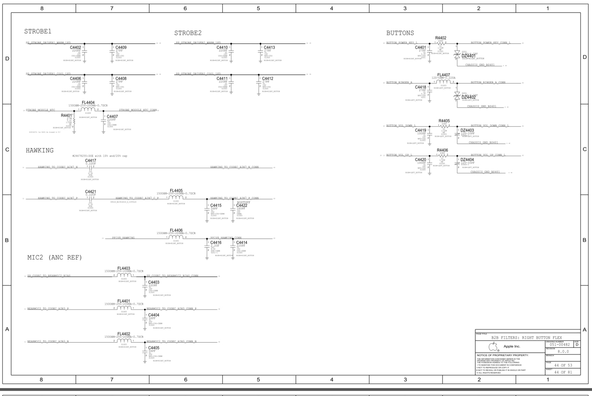
LOWER MIC1/4

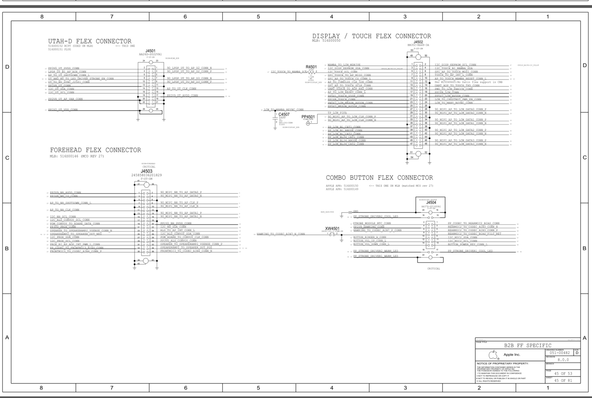
SPEAKER1

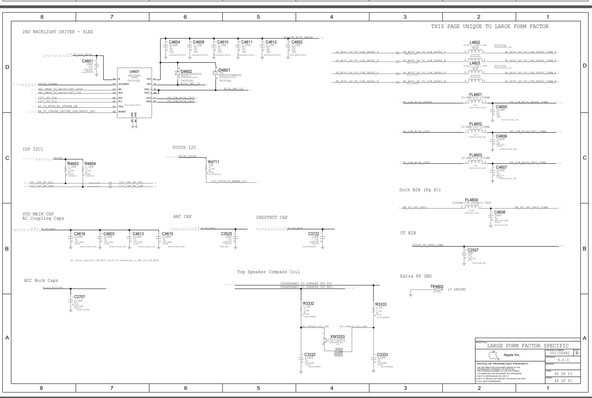
DOCK FLEX CONNECTOR

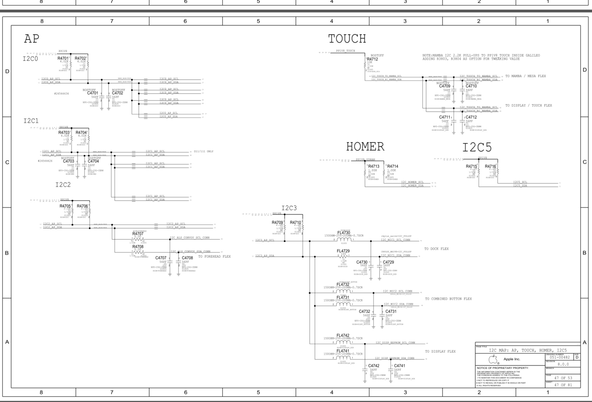
TRISTAR

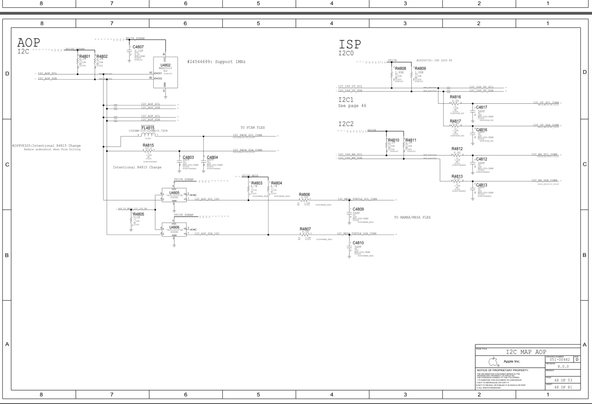
USB AC Coupling

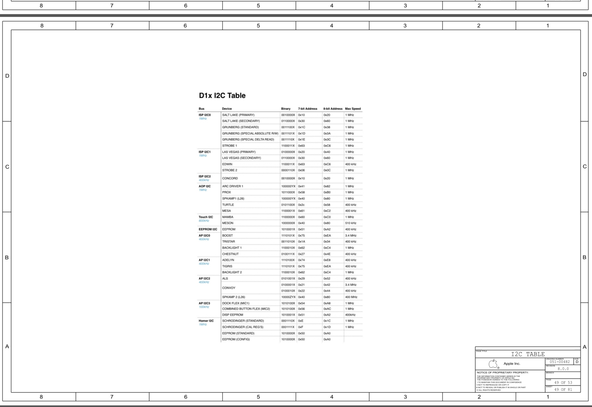












D1x I2C Table

Bus	Device	Binary	7-bit Address	8-bit Address	Max Speed
AP I2C0 (Master)	SALT LAKE (PRIMARY)	0010000X	0x10	0x20	* MHz
	SALT LAKE (SECONDARY)	0010001X	0x08	0x80	* MHz
	GRUNNING (STANDARD)	0011100X	0x1C	0x38	* MHz
	GRUNNING (SPECIAL ABSOLUTE TRK)	0011101X	0x1D	0x3A	* MHz
	GRUNNING (SPECIAL RELTA RUN)	0011110X	0x1E	0x3C	* MHz
	STROBE 1	1100011X	0x63	0xC6	* MHz
AP I2C1 (Master)	LIS VEGAS (PRIMARY)	0100000X	0x20	0x40	* MHz
	LIS VEGAS (SECONDARY)	0100001X	0x08	0x80	* MHz
	EDWIN	1100011X	0x63	0xC6	400 kHz
	STROBE 2	0000101X	0x05	0x0A	* MHz
AP I2C2 (Master)	CONCORD	0010000X	0x10	0x20	* MHz
AOP I2C (Master)	ARC DRIVER 1	1000007X	0x41	0x82	* MHz
	PINOI	1011002X	0x58	0xB0	* MHz
	OPHAMP (LIS)	1000007X	0x40	0x80	* MHz
	TURPLE	0101700X	0x2e	0x5B	400 kHz
	MAGNO	1100003X	0x60	0xC0	400 kHz
Touch SE	MAGMA	1100003X	0x60	0xC0	400 kHz
	MESON	1000500X	0x28	0x50	400 kHz
EEPROM HE	EEPROM	1010001X	0x50	0xA0	400 kHz
AP SE0 (Master)	BOOST	1110101X	0x75	0xEA	3.4 MHz
	PINTAN	0011010X	0x1A	0x34	400 kHz
	BACKLIGHT ?	1100013X	0x62	0xC4	* MHz
	CHESTNUT	0100011X	0x23	0x46	400 kHz
AP SE1 (Master)	KEELIN	1110100X	0x74	0xE8	400 kHz
	PINTAN	0011010X	0x1A	0x34	400 kHz
	BACKLIGHT 2	1100013X	0x62	0xC4	* MHz
AP SE2 (Master)	ALS	0101007X	0x28	0x50	400 kHz
		0100001X	0x21	0x42	3.4 MHz
	CONDOR	0100010X	0x22	0x44	400 kHz
	OPHAMP (LIS)	1000007X	0x40	0x80	400 kHz
AP SE3 (Master)	DOCK FLEX (MIC1)	1010120X	0x54	0xA8	* MHz
	DOCK FLEX (MIC2 / FLEX (MIC2)	1010121X	0x55	0xAA	* MHz
	CHIP EEPROM	1010001X	0x50	0xA0	400kHz
Homer SE (Slave)	SCHRODINGER (STANDARD)	0001110X	0x0E	0x1C	* MHz
	SCHRODINGER (CAL REG'S)	0001111X	0x0F	0x1E	* MHz
	EEPROM (STANDARD)	1010000X	0x50	0xA0	400kHz
	EEPROM (CONFIG)	1010000X	0x50	0xA0	* MHz

This page controls elements different accross all MLBs

PCIe Lanes

PP0R01
PP0R02

C0R05 C0R07 C0R13 C0R19 C0R11 C0R14 C0R17

R0R07
R0R08

MLB UNIQUE
Apple Inc.
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Wifi/BT

Cellular

FF SPECIFIC

NFC

CELL,WIFI,NFC
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1. ALL RESISTANCE VALUES ARE IN OHMS, E-7 SHIFT Y: 0%
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ

REV | ECN | DESCRIPTION OF REVISION | DR BY | DATE

STOCKHOLM MLB
JUNE 22, 2016

ALTERNATES

PART NUMBER	ALTERNATE PART NUMBER	REFERENCE DESIGNATOR(S)	DESCRIPTION	BOM OPTION
1337S0136	1337S0137	C7100_AF	0.039UF 10% 6.3V X5R	

STATE	REV	DESCRIPTION	REFERENCE DESIGNATOR	SPECIAL	BOM OPTION
PRODUCTION	A	0.039UF 10% 6.3V X5R-CERM-0201	C7100_AF		

BOM OPTIONS

STATE	REV	DESCRIPTION	REFERENCE DESIGNATOR	BOM OPTION
PRODUCTION	A	100PF 5% 50V C0G	C7100_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7101_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7102_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7103_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7104_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7105_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7106_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7107_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7108_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7109_AF	STUFF

STATE	REV	DESCRIPTION	REFERENCE DESIGNATOR	BOM OPTION
PRODUCTION	A	100PF 5% 50V C0G	C7110_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7111_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7112_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7113_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7114_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7115_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7116_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7117_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7118_AF	STUFF
PRODUCTION	A	100PF 5% 50V C0G	C7119_AF	STUFF

在维基

pspi
Apple Inc.
051-00482 C
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STOCKHOLM

5V BOOSTER

NFC CONTROLLER

NFC FRONT END

NFC LOAD SWITCH

NFC
Apple Inc.
051-00482 C
8.0.0
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1. ALL RESISTANCE VALUES ARE IN OHMS, E-7 SHIFT Y: 0%
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ

D11 RADIO MLB_FF
FEB 05, 2016

PDF PAGE	CONTENTS
1	METROCIRC

LMBRF:

STATE	REV	DESCRIPTION	REFERENCE DESIGNATOR	SPECIAL	BOM OPTION
PRODUCTION	A	339S0146 PCB ASSEMBLY	339S0146	SPECIAL	BOM OPTION

NOLMBRF:

STATE	REV	DESCRIPTION	REFERENCE DESIGNATOR	SPECIAL	BOM OPTION
PRODUCTION	A	339S0146 PCB ASSEMBLY	339S0146	SPECIAL	BOM OPTION

METROCIRC

D11 NORTH-SOUTH METROCIRC 339S00147

D11 EAST-WEST METROCIRC 339S00146

WIFI LOWER ANTENNA FEED

COAX

METROCIRC 1/2
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SHT 2 OF 3
57 OF 81

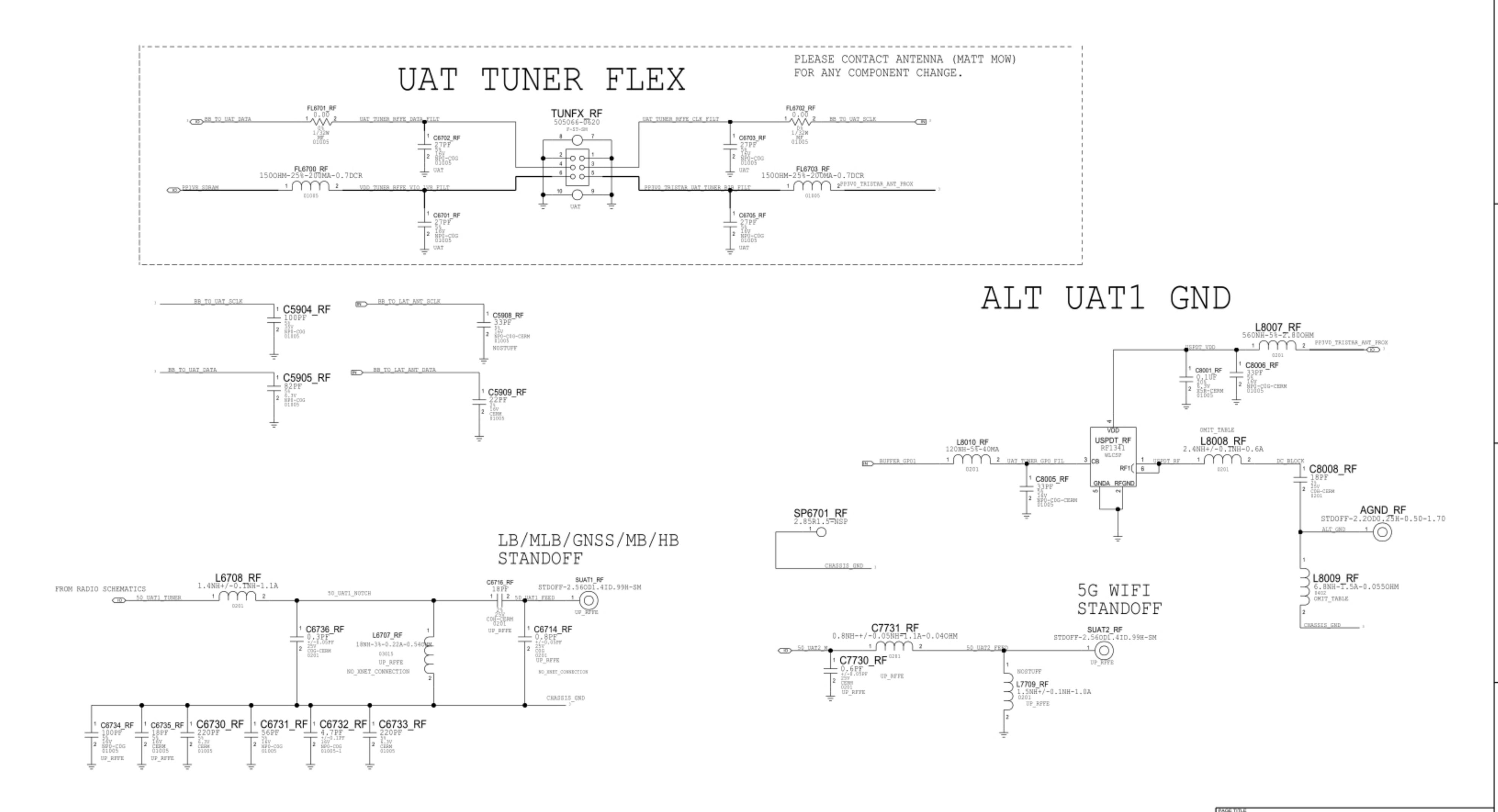
UAT TUNER FLEX

PLEASE CONTACT ANTENNA (MATT MOW) FOR ANY COMPONENT CHANGE.

ALT UAT1 GND

LB/MLB/GNSS/MB/HB STANDOFF

5G WIFI STANDOFF



UAT MATCH AND TUNER [3]

Apple Inc. — 051-00482 — D — 8.0.0 — 3 OF 3 — 58 OF 81

NOTICE OF PROPRIETARY PROPERTY

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

REV	ECN	DESCRIPTION OF REVISION	DATE
8	0006532879	ENGINEERING RELEASED	2016-07-06

D1X WIFI_MLB (PERENNIAL)
FEBRUARY 1, 2016

PDF PAGE	CSA PAGE	CONTENTS
2	76	PERENNIAL
3	77	WIFI FRONT-END

BOM OPTIONS:

POWER

CLOCKS

CONTROL

WLAN PCIE

WLAN UART

BLUETOOTH UART

AOP

AUDIO

D10_JP:

NOSTUFF:C7729_RF,C7711_RF,C7709_RF,C7710_RF, C7707_RF, C7708_RF C7700_RF, C7703_RF,C7704_RF

D10_ROW:

NOSTUFF:C7729_RF,C7711_RF,C7709_RF,C7710_RF, C7707_RF, C7708_RF C7700_RF, C7703_RF,C7704_RF

D101_WIFI:

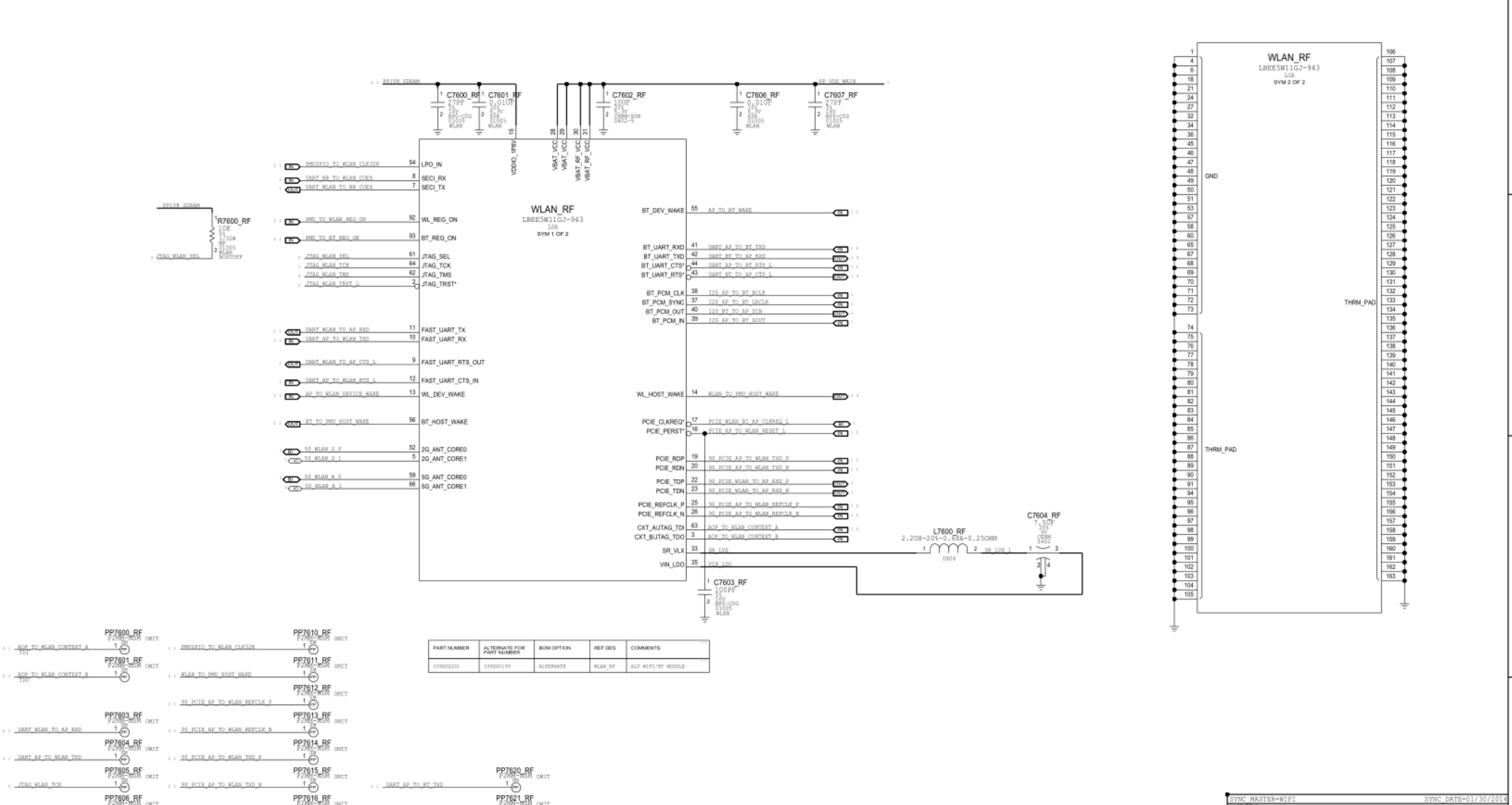
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D11_JP:

D11_ROW:

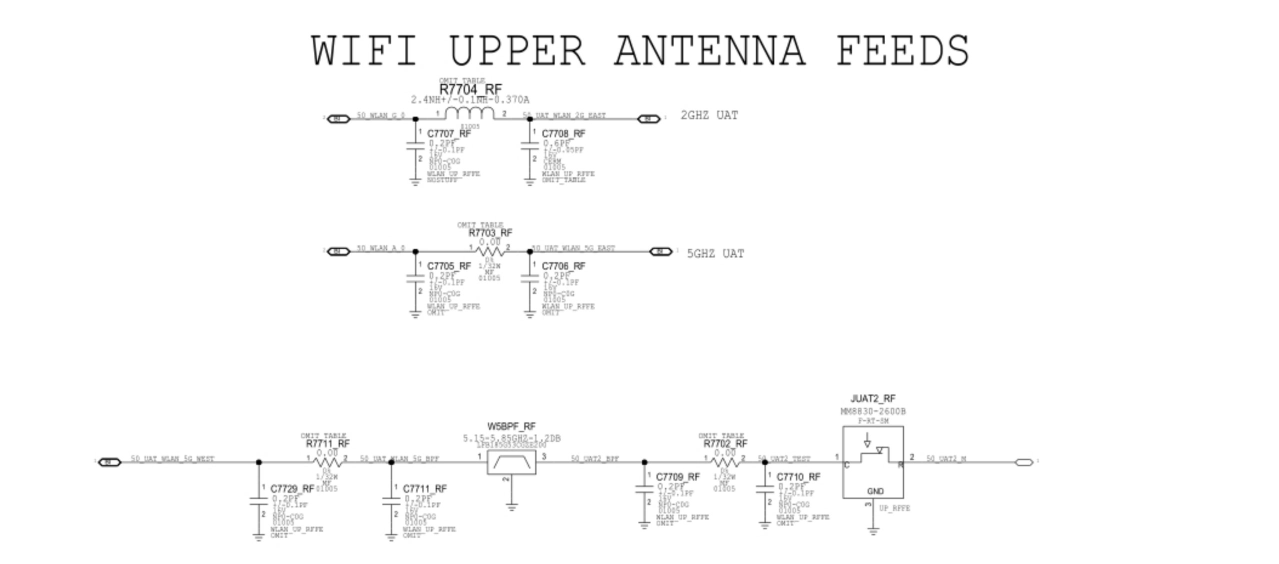
D111_WIFI:

WIFI/BT



PERENNIAL

WIFI UPPER ANTENNA FEEDS



WIFI FRONT-END [77]

81 ICEFALL, SIM, DEBUG_CONN

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.
2. ALL CAPACITANCE VALUES ARE IN MICROFARADS.
3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

MAV16 RADIO_MLB

LAST_MODIFICATION=Wed Jun 29 12:06:01 2016

PAGE	CSA	CONTENTS	SYNC	DATE
62	1	page1	<SYNC_MASTER1>	<SYNC_DATE1>
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64	3	PMU: CONTROL AND CLOCKS	<SYNC_MASTER3>	<SYNC_DATE3>
65	4	PMU: SWITCHERS AND LDOS	<SYNC_MASTER4>	<SYNC_DATE4>
66	5	BASEBAND: POWER2	<SYNC_MASTER5>	<SYNC_DATE5>
67	6	BASEBAND: CONTROL		
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77	16	PMU: ET MODULATOR		
78	17	TEST POINTS & BOOT CONFIG		
79	18	TDD TRANSMIT		
80	19	FDD TRANSMIT		

SCH: 951-00964

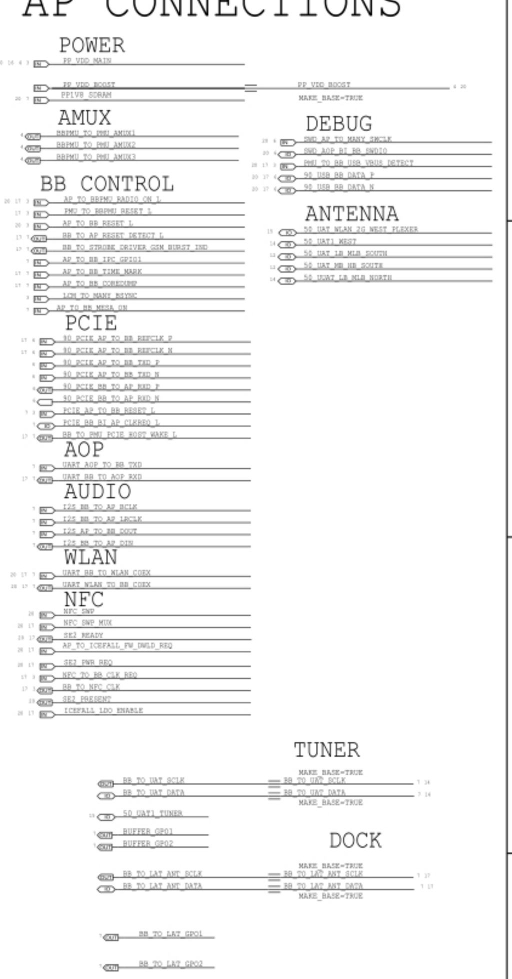
BOM: 939-00826

ALTERNATES

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
197S0565	197S0593	ALTERNATE	Y5501_RF	1902 MHZ XTAL
197S0598	197S0593	ALTERNATE	Y5501_RF	1902 MHZ XTAL
138S00101	138S00095	ALTERNATE	ALL	TRANSITION CAP
335S00013	335S0894	ALTERNATE	EPROM_RF	IC EEPROM
353S00253	353S00321	ALTERNATE	SWPMX_RF	IC SWITCH SPDT
155S00235	155S00234	ALTERNATE	UPPDI_RF	NOLMBRF

ALL:C5616_RF-C5618_RF,C5632_RF-C5634_RF

AP CONNECTIONS



BOM OPTIONS:

LMBRF:

NOLMBRF:

D10 SPECIFIC:

D11 SPECIFIC:

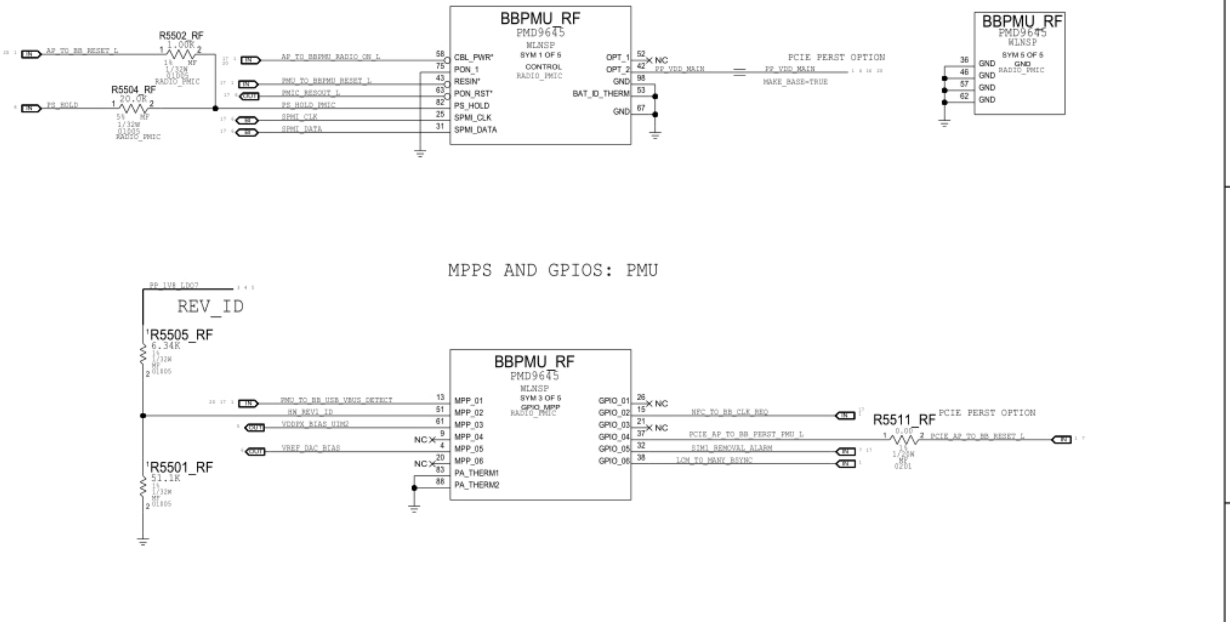
PMU: CONTROL AND CLOCKS

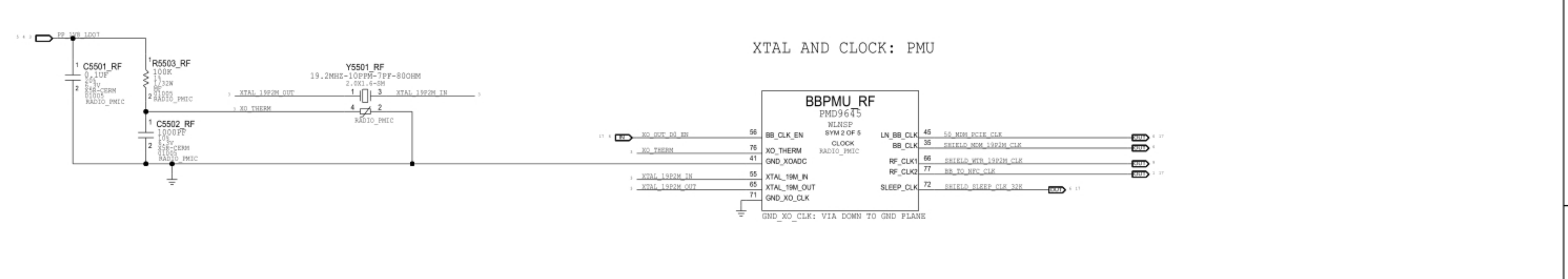
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0.10V	887K	51.1K	DEV1
0.20V	422K	51.1K	DEV2
0.30V	255K	51.1K	DEV 2.1
0.40V	180K	51.1K	DEV 3
0.50V	124K	51.1K	T181
0.60V	102K	51.1K	PP/P1
0.70V	82.5K	51.1K	DEV4
0.80V	63.4K	51.1K	P2
0.90V	51.1K	51.1K	DEV5
1.00V	51.1K	63.4K	EVT
1.10V	51.1K	82.5K	EVT DOE
1.20V	50.0K	100K	EVT ALT/CARBON
1.30V	39.0K	100K	DEV6
1.40V	14.7K	51.1K	CARRIER
1.50V	40.2K	200K	DVT
1.60V	6.34K	51.1K	PVT

RESET AND CONTROL: PMU

MPPS AND GPIOS: PMU

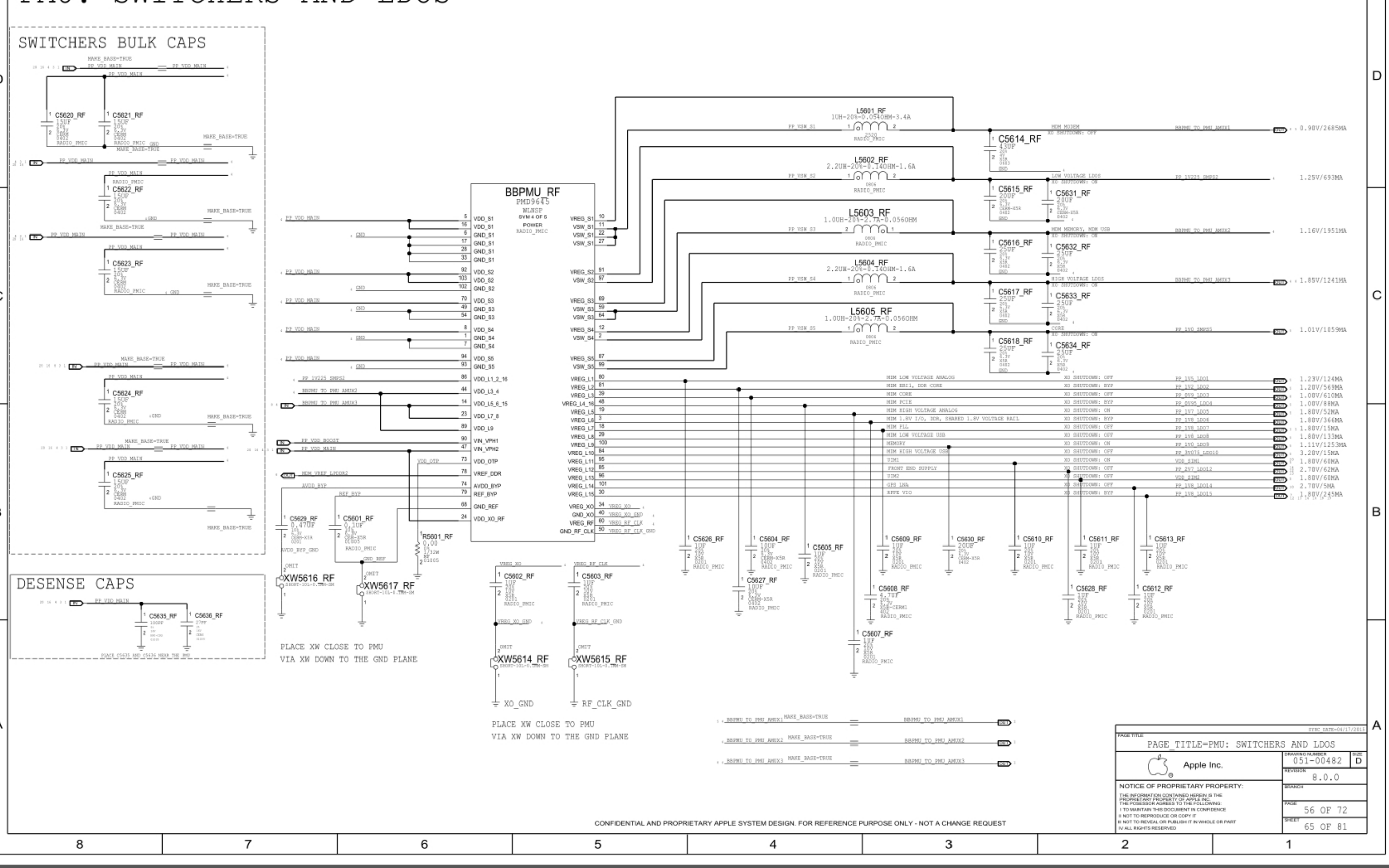
XTAL AND CLOCK: PMU



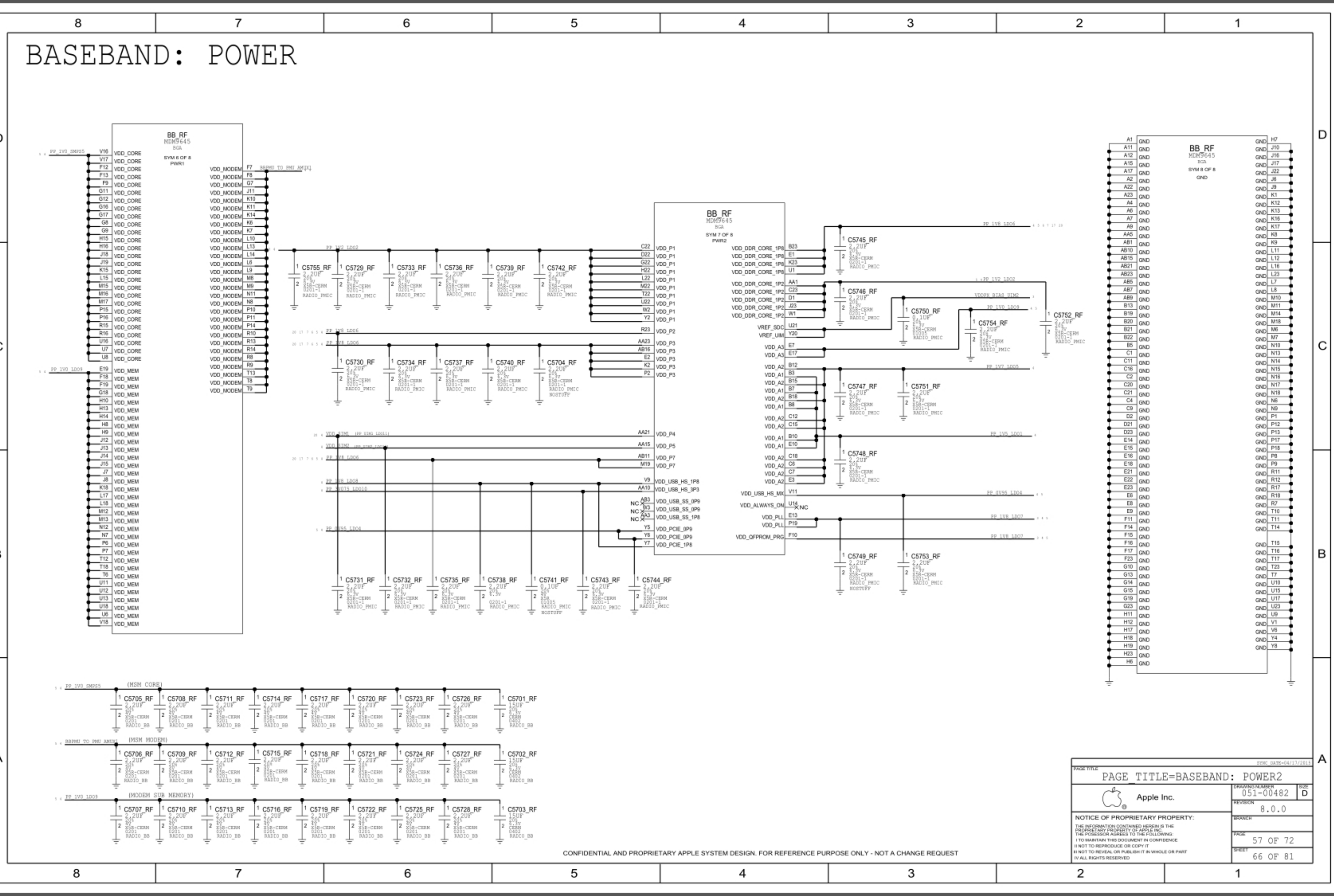


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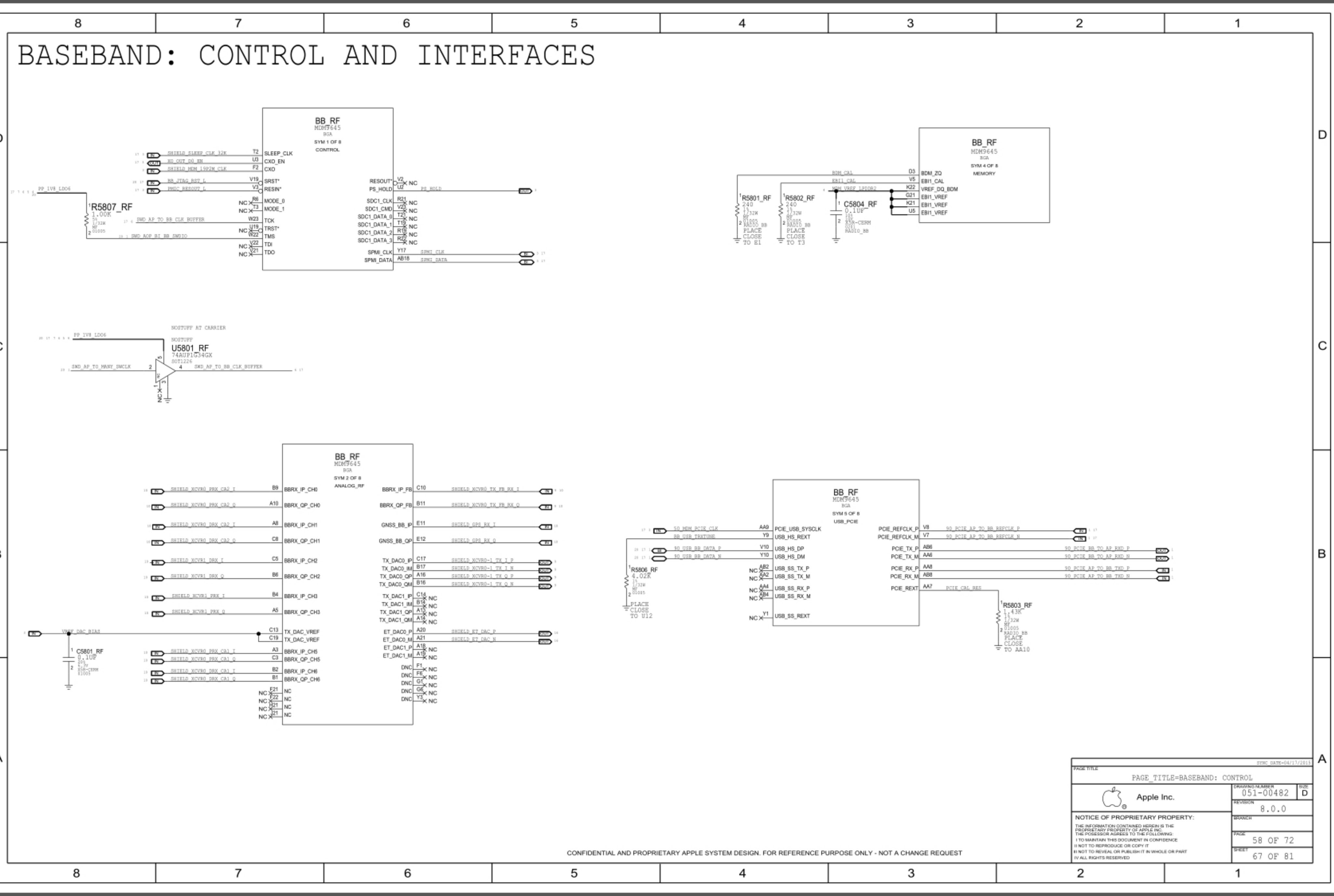
PMU: SWITCHERS AND LDOS



BASEBAND: POWER

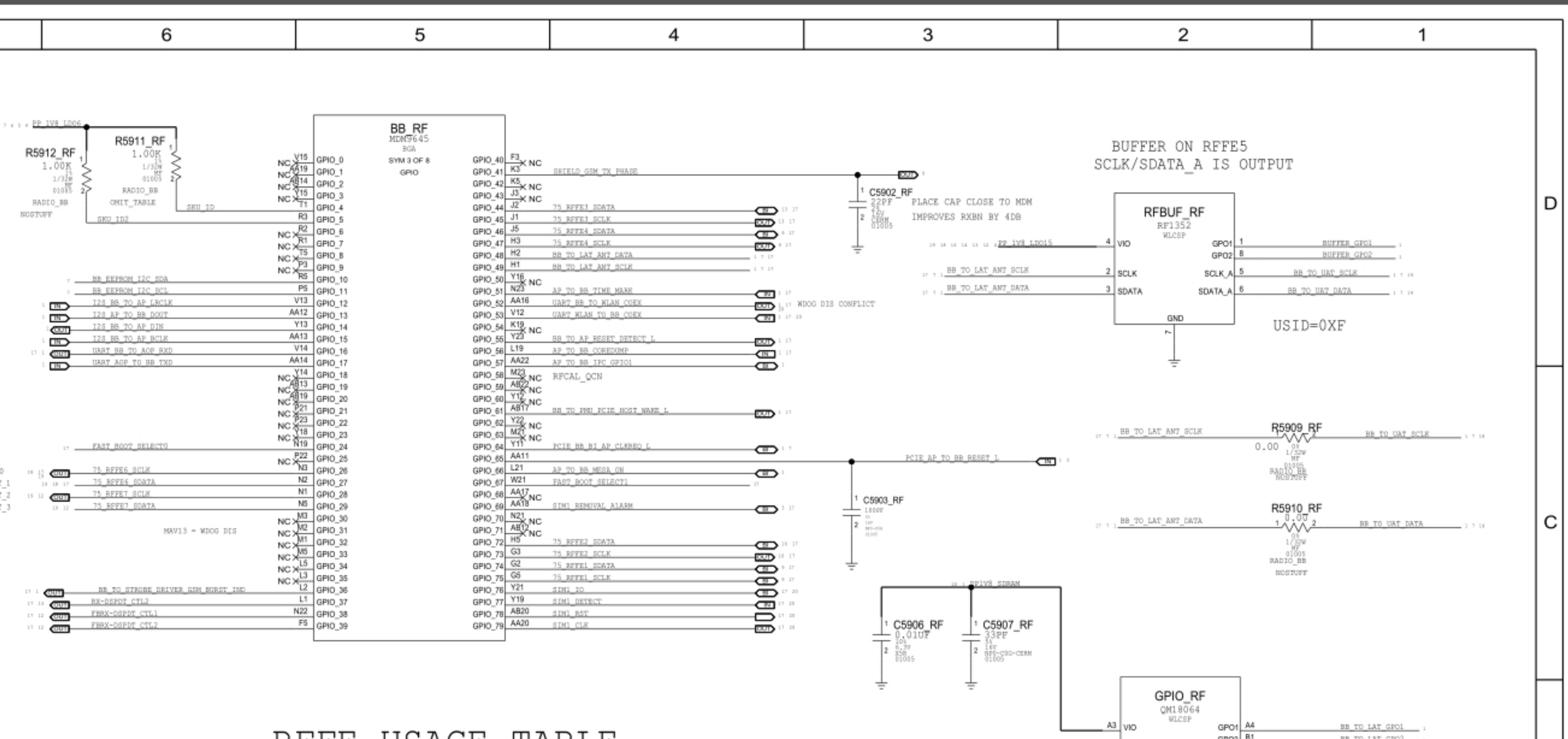


BASEBAND: CONTROL AND INTERFACES



BASEBAND: GPIOS

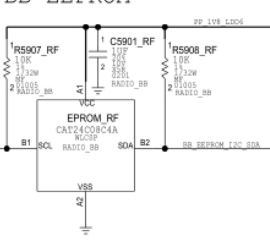
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ROW	NOSTUFF	NOSTUFF
JP	1.0 KOHM	NOSTUFF



RFFE USAGE TABLE

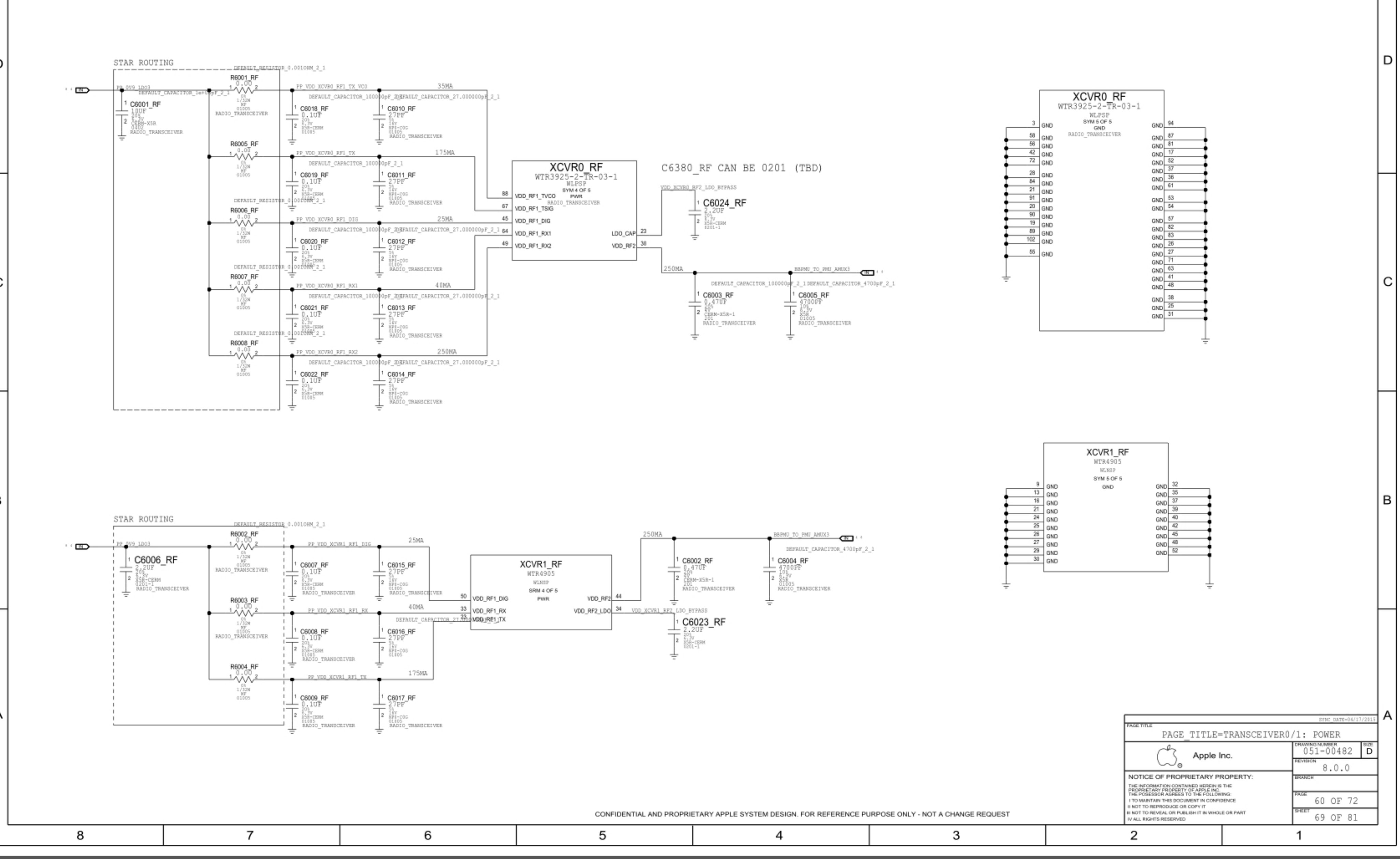
RFFE1 WTR3925
RFFE2 QFE3100
RFFE3 DIV MODULES
RFFE4 WTR4905
RFFE5 TUNERS + ELNAS
RFFE6 2G PA,MLB PA,MB/HB TDD PA,MB/HB FDD PA
RFFE7 LB PA, COUPLERS

BB EEPROM

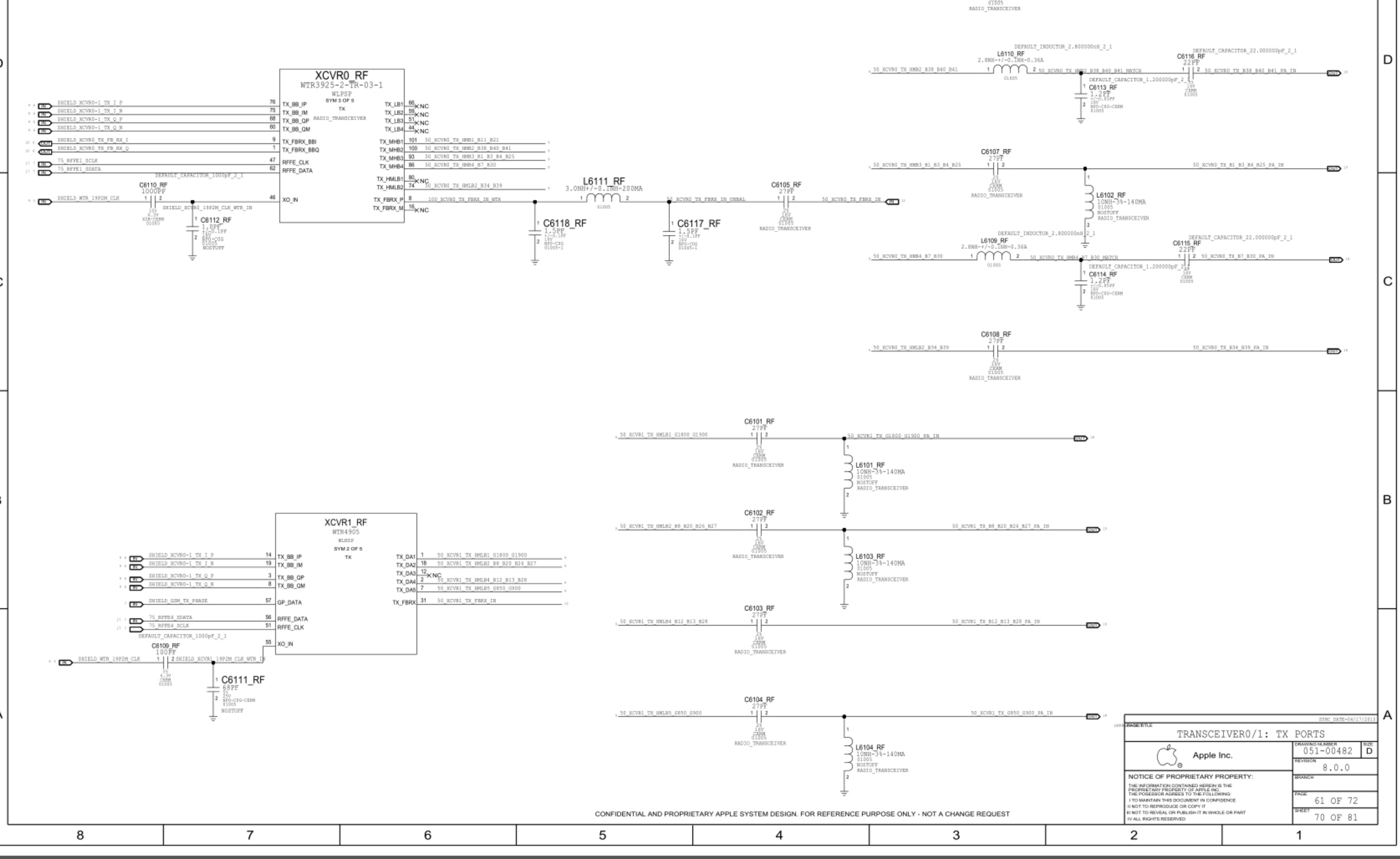


PCIE PULL-UPS TO BB RAIL

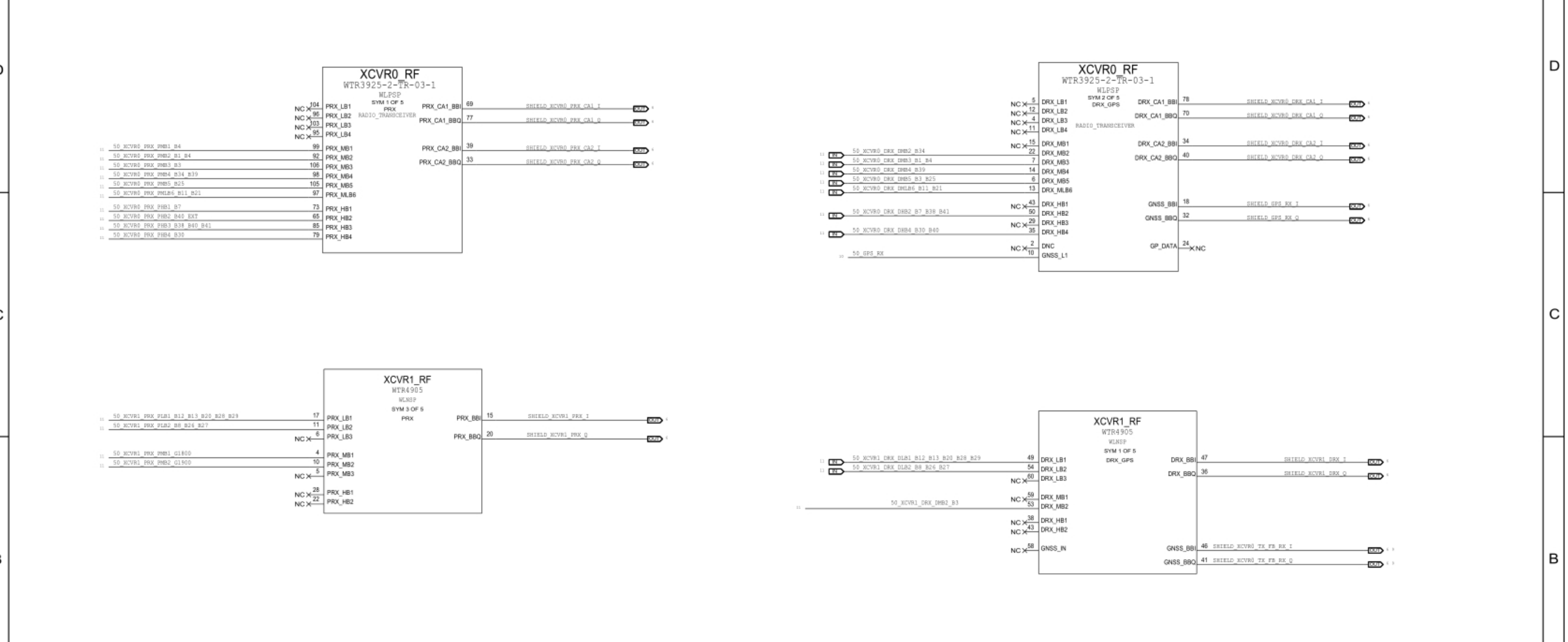
TRANSCEIVER: POWER



TRANSCEIVER: TX PORTS

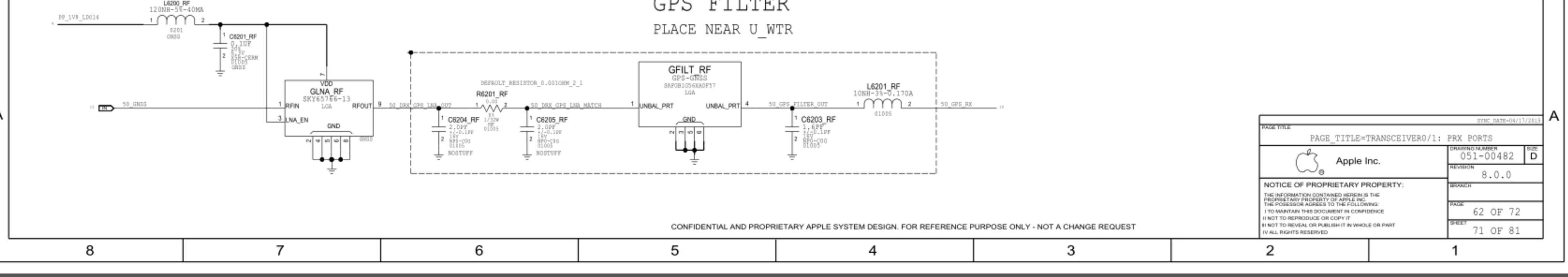


TRANSCEIVER: PRX, DRX, & GPS PORTS

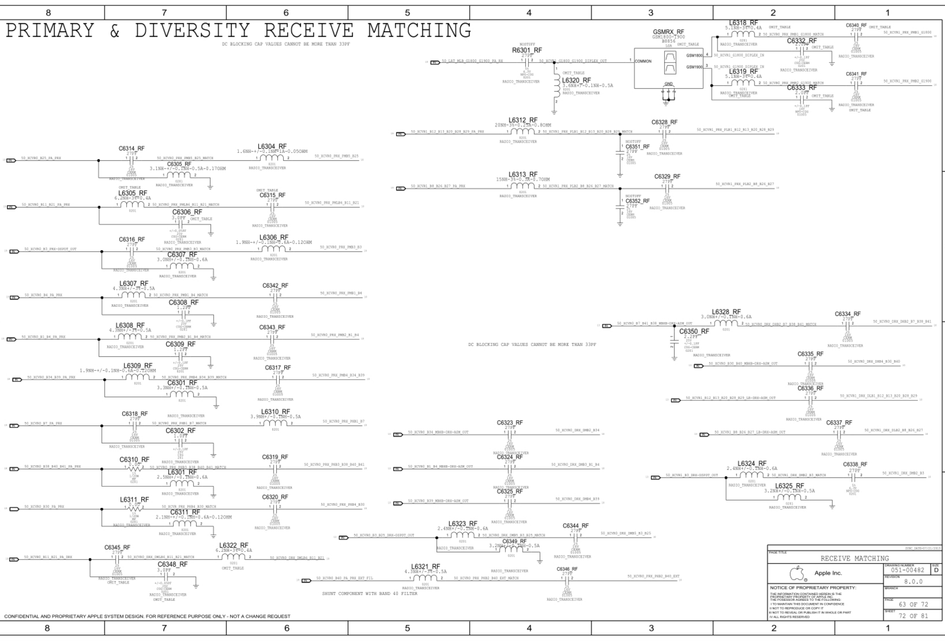


GPS FILTER

PLACE NEAR U_WTR

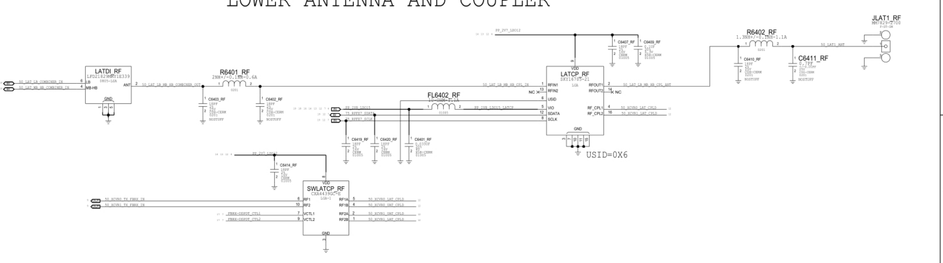


PRIMARY & DIVERSITY RECEIVE MATCHING

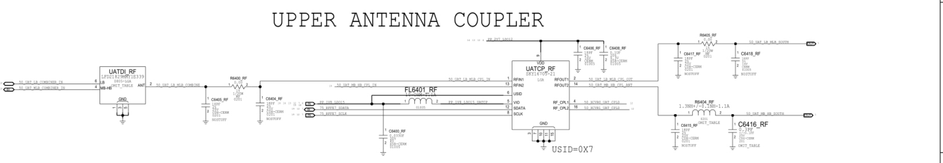


Title	RECEIVE MATCHING
Apple Inc.	051-00482 D
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	Sheet 63 OF 72
	Total 72 OF 81

LOWER ANTENNA AND COUPLER



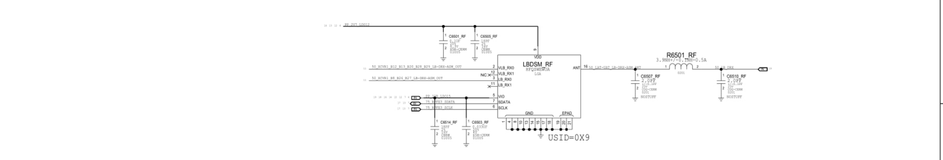
UPPER ANTENNA COUPLER



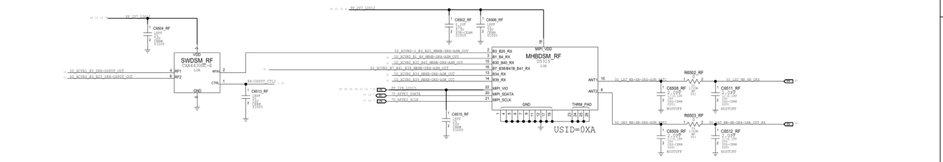
Title	PAGE TITLE=LOWER ANTENNA & COUPLERS
Apple Inc.	051-00482 D
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DIVERSITY RECEIVE

LB DRX ASM



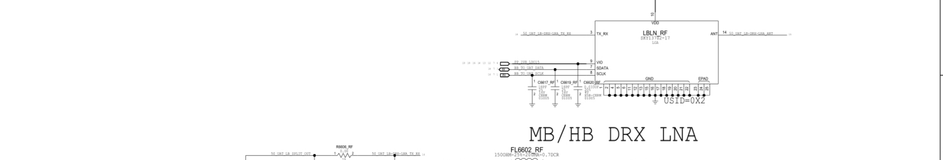
MB HB DRX ASM



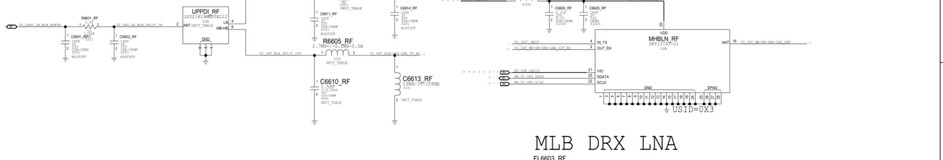
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DIVERSITY RECEIVE LNAS

LB DRX LNA



MB/HB DRX LNA



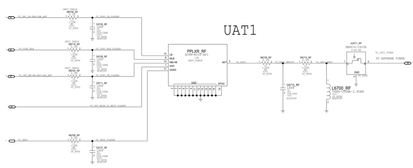
MLB DRX LNA



Title	DIVERSITY RECEIVE LNA'S
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UPPER ANTENNA FEEDS

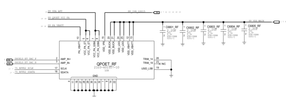
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PMU: ET MODULATOR



DESENSE CAPS



PAGE TITLE	PMU: ET MODULATOR
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REVISION	C
SCALE	1.5:1
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MLB TEST POINTS

BASEBAND



PCIE



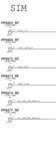
PCIE GND



BBPMU



SIM



BOOT CONFIG

DEFAULT PART BOOT[2:0]
USB = 0X2



ICEFALL

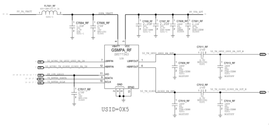


PAGE TITLE	TEST POINTS & BOOT CONFIG
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REVISION	C
SCALE	1.5:1
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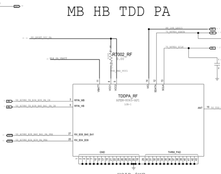
TDD TRANSMIT

2G PA



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MB HB TDD PA



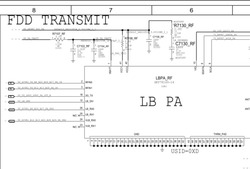
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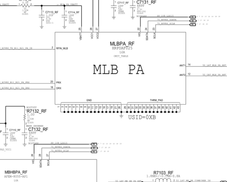
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LB PA



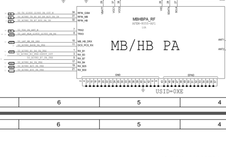
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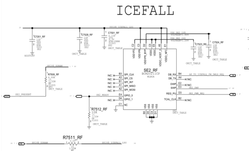
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MB/HB PA



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ICEFALL



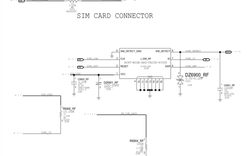
SWP MUX



ICEFALL LDO



SIM CARD CONNECTOR



DEBUG CONNECTOR

