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CLOCKS			20	20	SYSTEM POWER:PMU (3/3)			65	65	PMU: SWITCHERS AND LDOS			21	21	SYSTEM POWER:CHARGER			66	66	BASEBAND: POWER2			22	22	SYSTEM POWER:BATTERY CONN			67	67	BASEBAND: CONTROL			23	23	SYSTEM POWER:BOOST			68	68	BASEBAND GPIOS			24	24	SENSORS			69	69	TRANSCIEVER0/1: POWER			25	25	B2B FILTERS: UTAH			70	70	TRANSCIEVER0/1: TX PORTS			26	26	CAMERA:STROBE DRIVER			71	71	TRANSCIEVER0/1: PRX PORTS	<SYNC_MASTER71>	<SYNC_DATE71>	27	27	Accessory: Buck Circuit		<SYNC_DATE27>	72	72	RECEIVE MATCHING	<SYNC_MASTER72>	<SYNC_DATE72>	28	28	TRINITY:FF SPECIFIC			73	73	LOWER ANTENNA & COUPLERS	<SYNC_MASTER73>	<SYNC_DATE73>	29	29	B2B:FOREHEAD			74	74	DIVERSITY RECEIVE ASM'S	<SYNC_MASTER74>	<SYNC_DATE74>	30	30	B2B:NEVADA		<SYNC_DATE30>	75	75	DIVERSITY RECEIVE LNA'S	d		31	31	AUDIO:CALTRA CODEC (1/2)			76	76	UPPER ANTENNA FEEDS			32	32	AUDIO:CALTRA CODEC (2/2)			77	77	PMU: ET MODULATOR			33	33	AUDIO:SPEAKER AMP 2			78	<CSA_PAGE78>	TEST POINTS & BOOT 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25	25	B2B FILTERS: UTAH			70	70	TRANSCIEVER0/1: TX PORTS																																																																																																																																																																																																																																																																																																																																																																																																																																																																																				
26	26	CAMERA:STROBE DRIVER			71	71	TRANSCIEVER0/1: PRX PORTS	<SYNC_MASTER71>	<SYNC_DATE71>																																																																																																																																																																																																																																																																																																																																																																																																																																																																																		
27	27	Accessory: Buck Circuit		<SYNC_DATE27>	72	72	RECEIVE MATCHING	<SYNC_MASTER72>	<SYNC_DATE72>																																																																																																																																																																																																																																																																																																																																																																																																																																																																																		
28	28	TRINITY:FF SPECIFIC			73	73	LOWER ANTENNA & COUPLERS	<SYNC_MASTER73>	<SYNC_DATE73>																																																																																																																																																																																																																																																																																																																																																																																																																																																																																		
29	29	B2B:FOREHEAD			74	74	DIVERSITY RECEIVE ASM'S	<SYNC_MASTER74>	<SYNC_DATE74>																																																																																																																																																																																																																																																																																																																																																																																																																																																																																		
30	30	B2B:NEVADA		<SYNC_DATE30>	75	75	DIVERSITY RECEIVE LNA'S	d																																																																																																																																																																																																																																																																																																																																																																																																																																																																																			
31	31	AUDIO:CALTRA CODEC (1/2)			76	76	UPPER ANTENNA FEEDS																																																																																																																																																																																																																																																																																																																																																																																																																																																																																				
32	32	AUDIO:CALTRA CODEC (2/2)			77	77	PMU: ET MODULATOR																																																																																																																																																																																																																																																																																																																																																																																																																																																																																				
33	33	AUDIO:SPEAKER AMP 2			78	<CSA_PAGE78>	TEST POINTS & BOOT CONFIG	<SYNC_MASTER78>	<SYNC_DATE78>																																																																																																																																																																																																																																																																																																																																																																																																																																																																																		
34	34	AUDIO:SPEAKER AMP 1			79	<CSA_PAGE79>	TDD TRANSMIT	<SYNC_MASTER79>	<SYNC_DATE79>																																																																																																																																																																																																																																																																																																																																																																																																																																																																																		
35	35	ARC:DRIVER			80	<CSA_PAGE80>	FDD TRANSMIT	<SYNC_MASTER80>	<SYNC_DATE80>																																																																																																																																																																																																																																																																																																																																																																																																																																																																																		
36	36	ARC:MAGGIE			81	<CSA_PAGE81>	ICEFALL, SIM, DEBUG_CONN	<SYNC_MASTER81>	<SYNC_DATE81>																																																																																																																																																																																																																																																																																																																																																																																																																																																																																		
37	37	DISPLAY & MESA:POWER				<CSA_PAGE82>		<SYNC_MASTER82>	<SYNC_DATE82>																																																																																																																																																																																																																																																																																																																																																																																																																																																																																		
38	38	B2B:ORB & MESA				<CSA_PAGE83>		<SYNC_MASTER83>	<SYNC_DATE83>																																																																																																																																																																																																																																																																																																																																																																																																																																																																																		
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41	41	B2B:DOCK FLEX				<CSA_PAGE86>		<SYNC_MASTER86>	<SYNC_DATE86>																																																																																																																																																																																																																																																																																																																																																																																																																																																																																		
42	42	spare				<CSA_PAGE87>		<SYNC_MASTER87>	<SYNC_DATE87>																																																																																																																																																																																																																																																																																																																																																																																																																																																																																		
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D11 EEEE CALLOUTS

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
825-6838	1	EEEE CODE FOR 639-01812	EEEE_GY2T	CRITICAL	EEEE_D11_BEST_JP
825-6838	1	EEEE CODE FOR 639-01813	EEEE_GY2V	CRITICAL	EEEE_D11_SUPREME_JP
825-6838	1	EEEE CODE FOR 639-01814	EEEE_GY2W	CRITICAL	EEEE_D11_EXTREME_JP
825-6838	1	EEEE CODE FOR 639-02138	EEEE_H3RN	CRITICAL	EEEE_D11_BEST_ROW
825-6838	1	EEEE CODE FOR 639-02139	EEEE_H3RP	CRITICAL	EEEE_D11_SUPREME_ROW
825-6838	1	EEEE CODE FOR 639-02140	EEEE_H3RQ	CRITICAL	EEEE_D11_EXTREME_ROW
825-6838	1	EEEE CODE FOR 639-02460	EEEE_H8C1	CRITICAL	EEEE_D11_BEST_CH
825-6838	1	EEEE CODE FOR 639-02465	EEEE_H8CK	CRITICAL	EEEE_D11_SUPREME_CH
825-6838	1	EEEE CODE FOR 639-02462	EEEE_H8C3	CRITICAL	EEEE_D11_EXTREME_CH

CAYMAN DDR Alternates

339S00258	339S00257	ALTERNATE	ALL	DDR-S, 3G, B1
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CAYMAN OMIT TABLE


PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
339S00257	1	CAYMAN, DCR-H, 3G, B1	U0700	CRITICAL	

2.2uF CAP Alts

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
138S00049	138S00032	ALTERNATE	ALL	CAP, CSD, 33A, 2, 20P, 224K, 97, 21A, K50C8BA
138S0831	138S00032	ALTERNATE	ALL	CAP, CSD, 33A, 2, 20P, 21A, 4, 2V, 21A, MICRATA

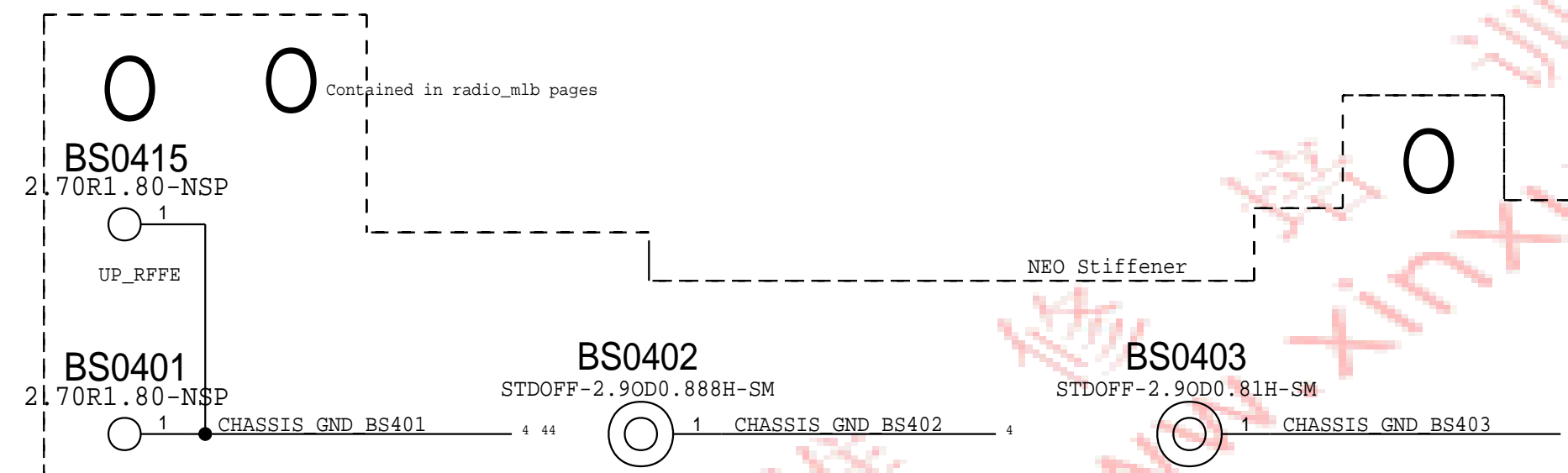
SIP Alternates

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
339M00009	339M00003	ALTERNATE	ALL	TRINITY BLUE,STATS
339M00008	339M00002	ALTERNATE	ALL	NEO,STATS

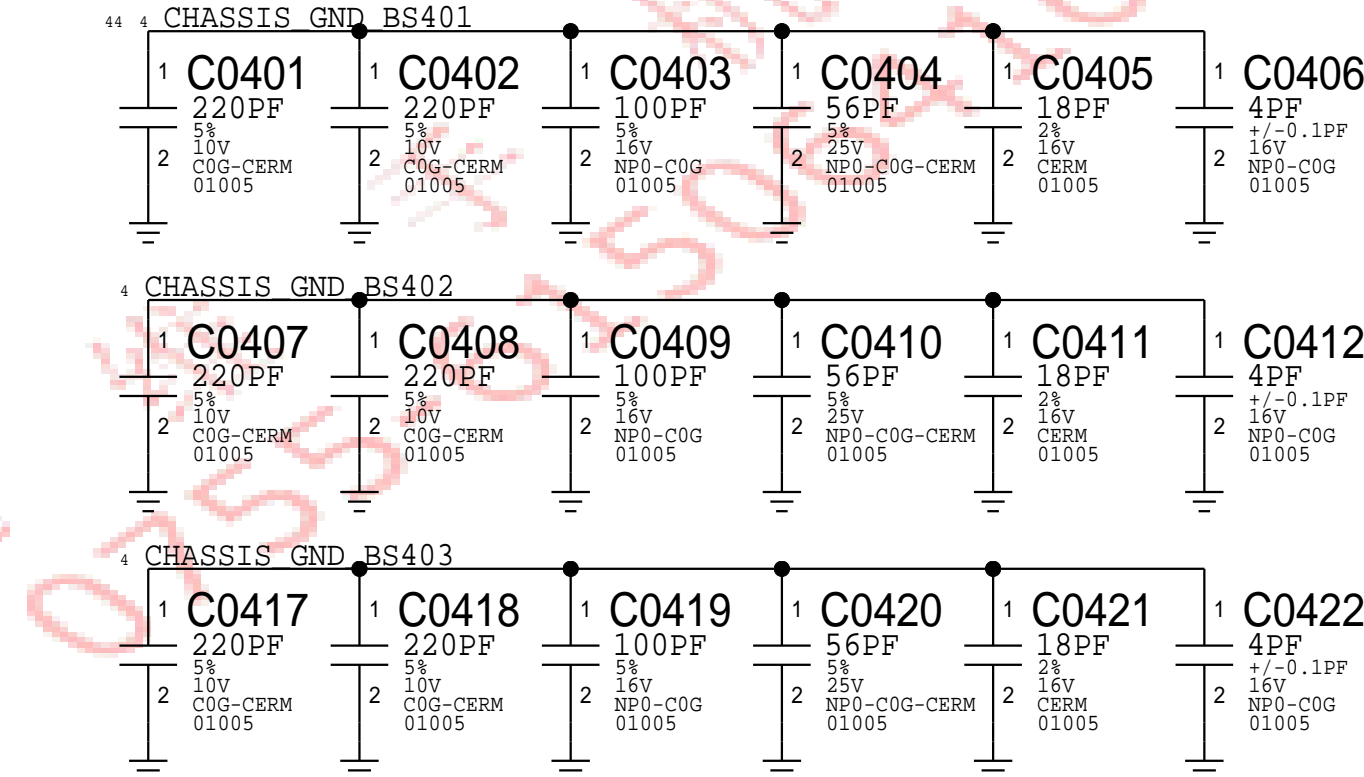
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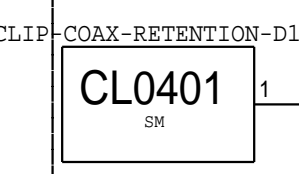
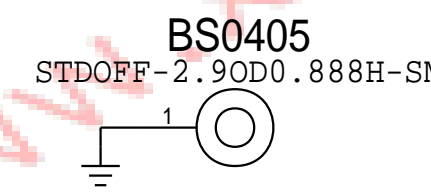
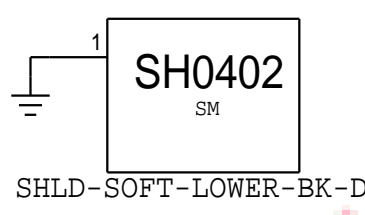
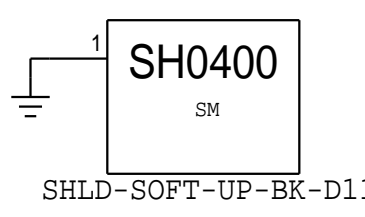
Current as of D11 MCO 056-01585 rev. 63



PTH per Rev63 for 1.8mm Drill

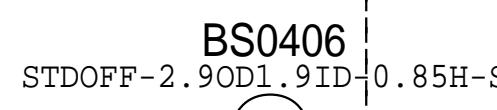
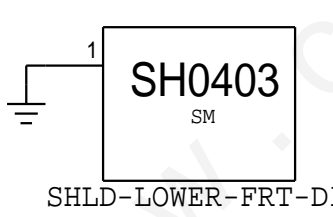
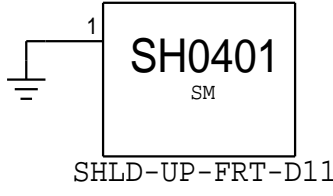


Back Shields



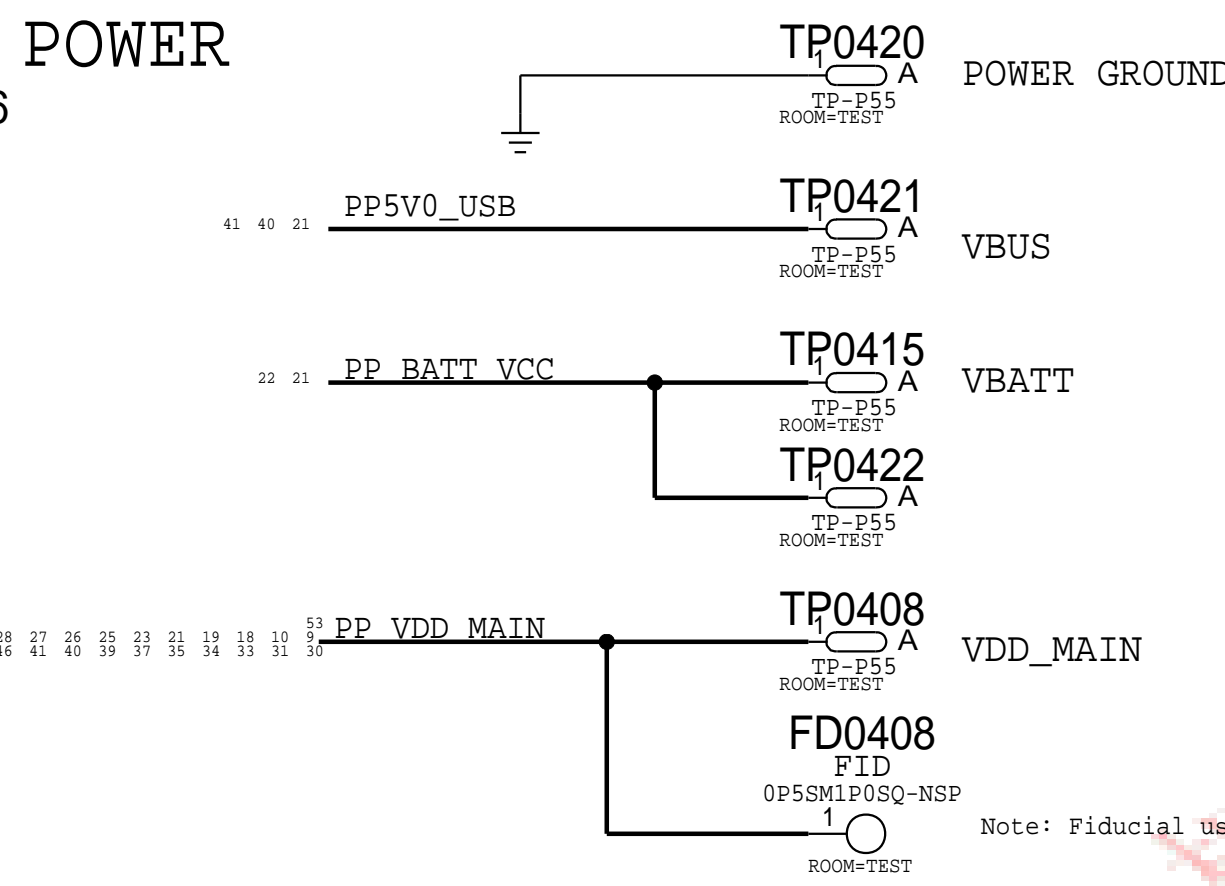
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Front Shields

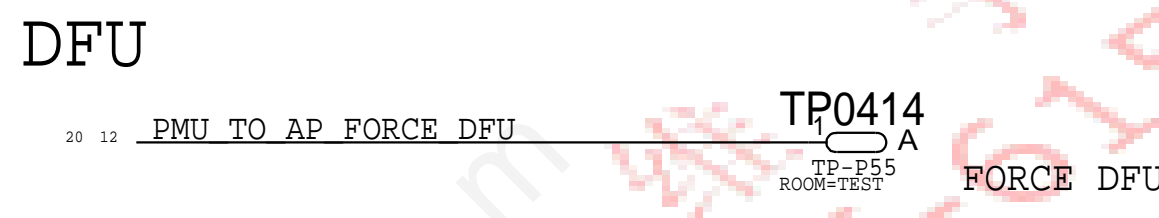


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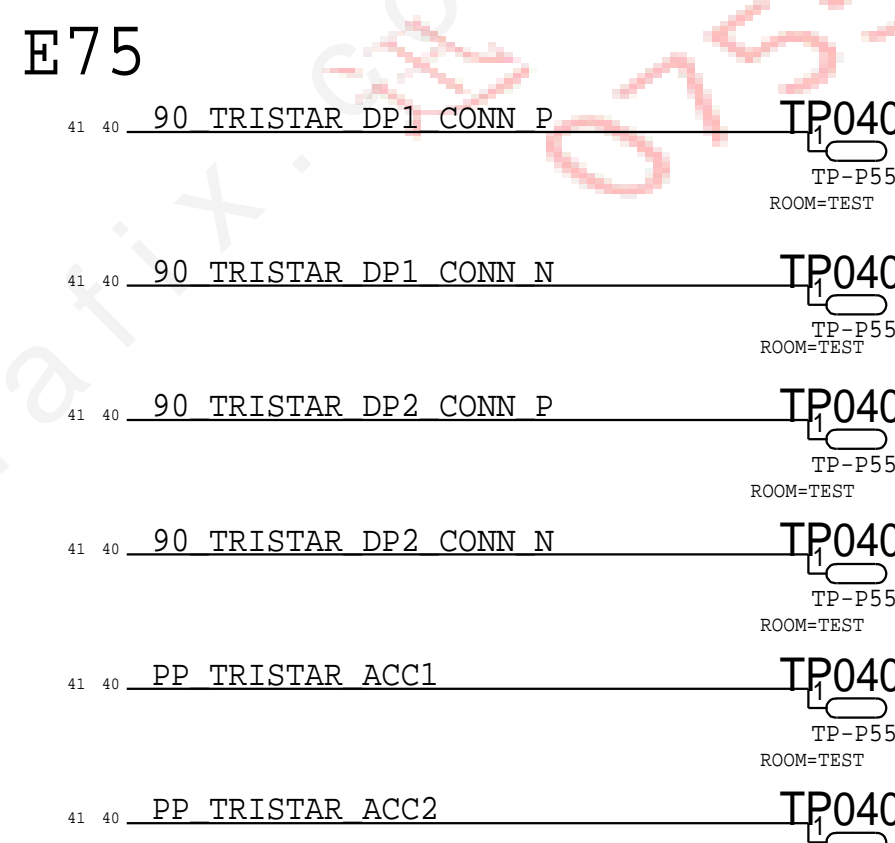
POWER



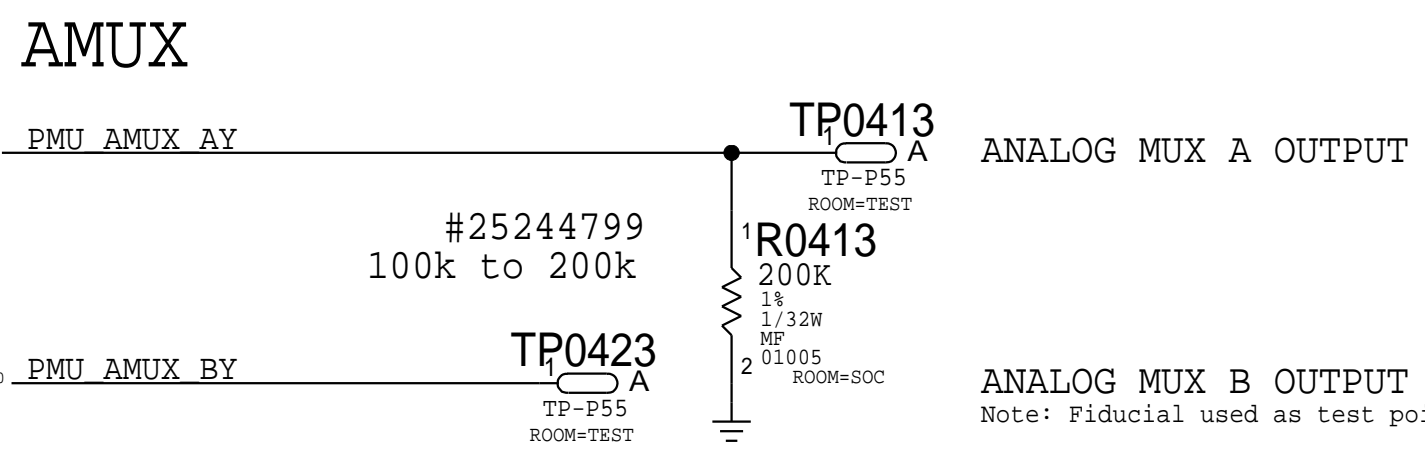
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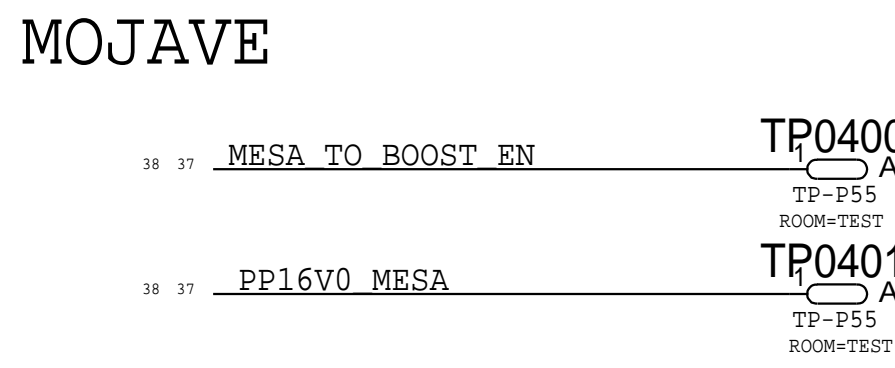
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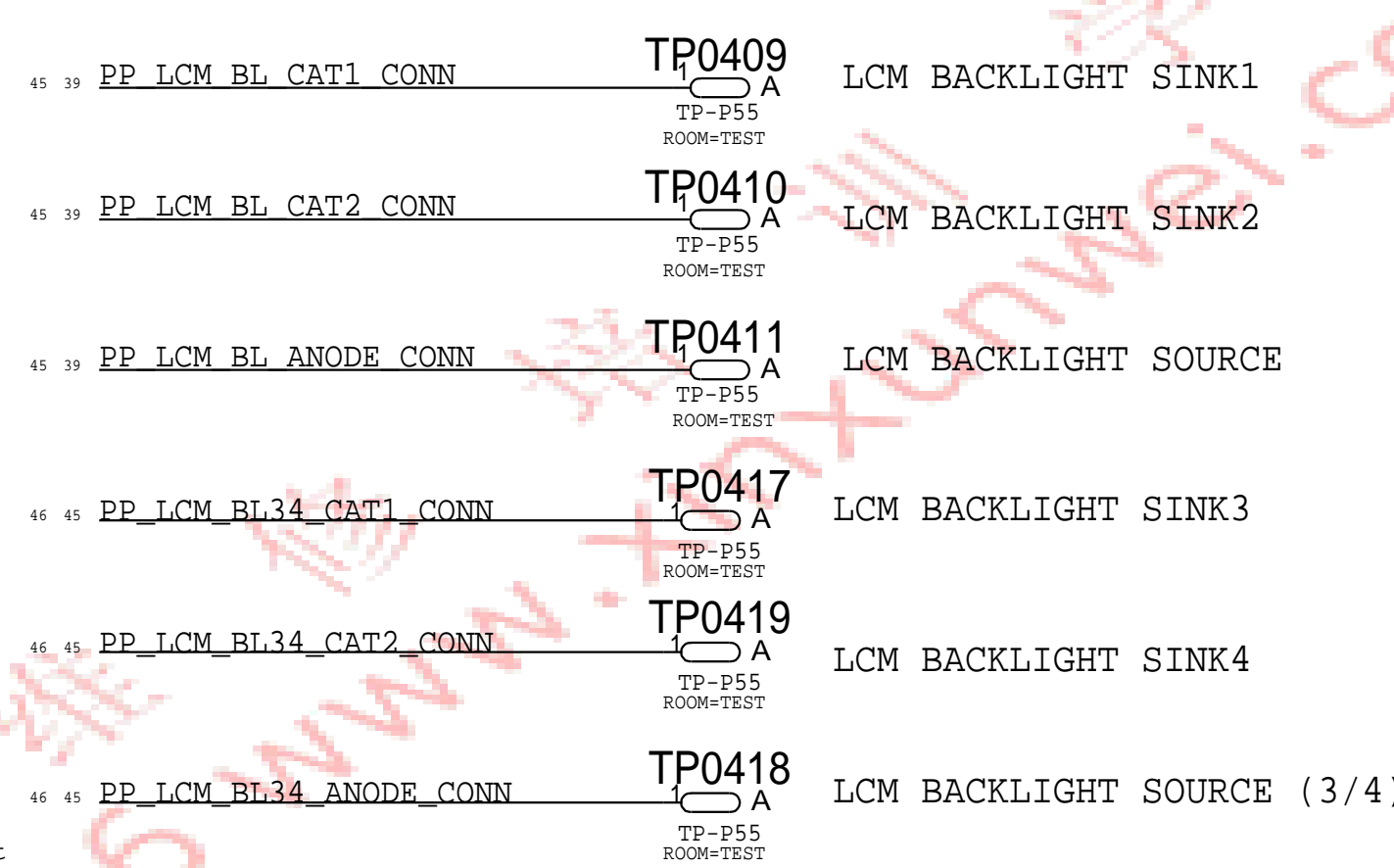
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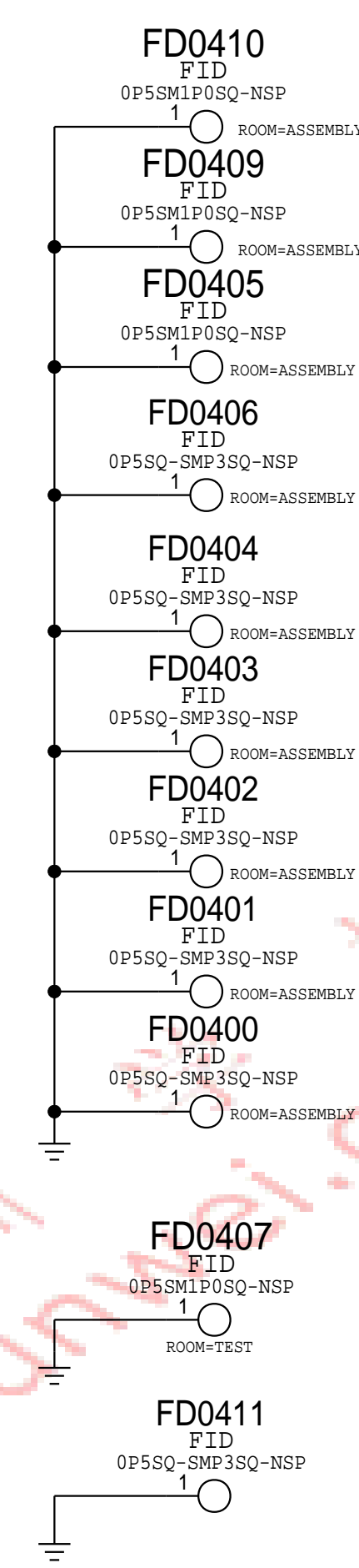
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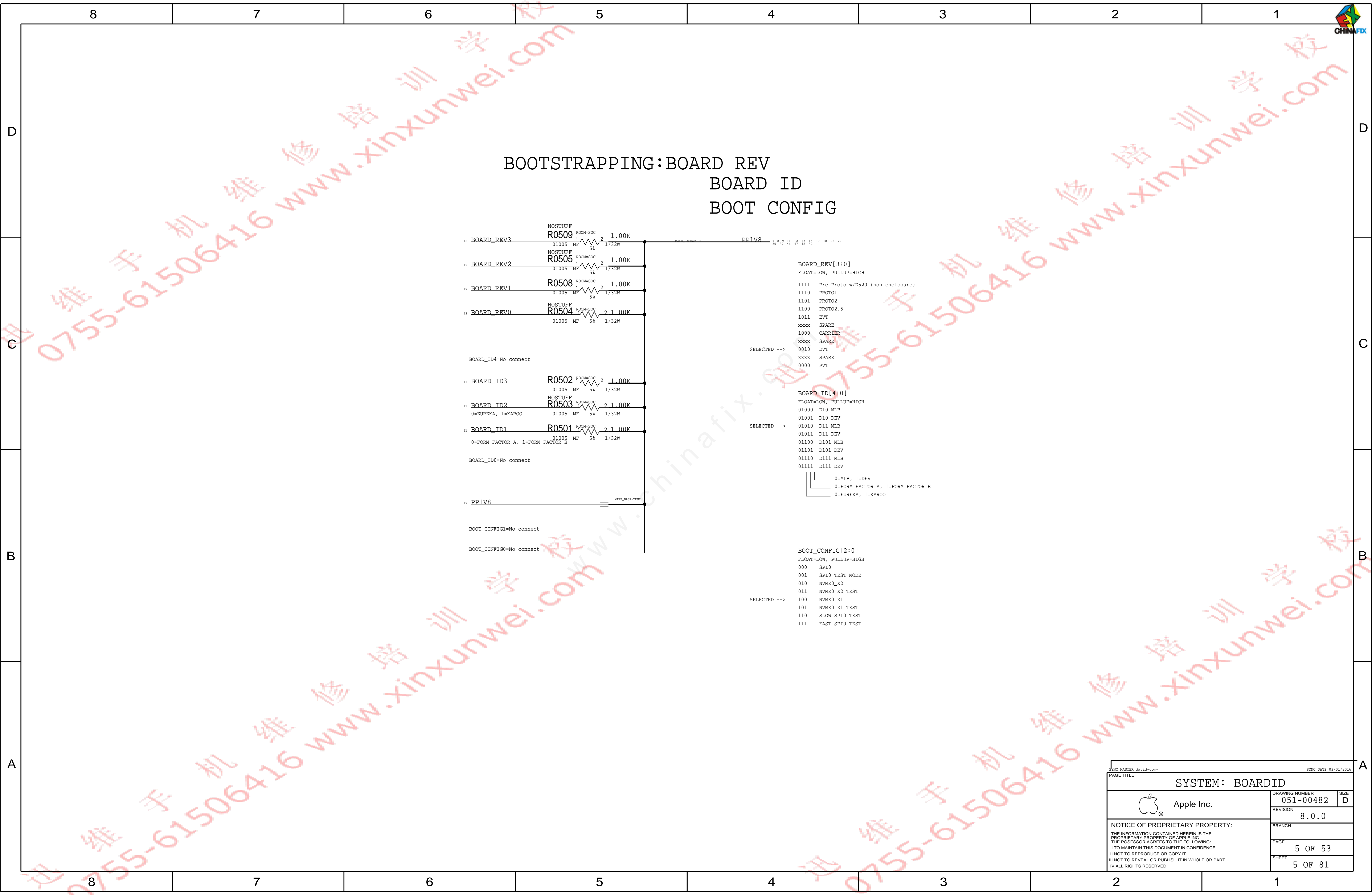


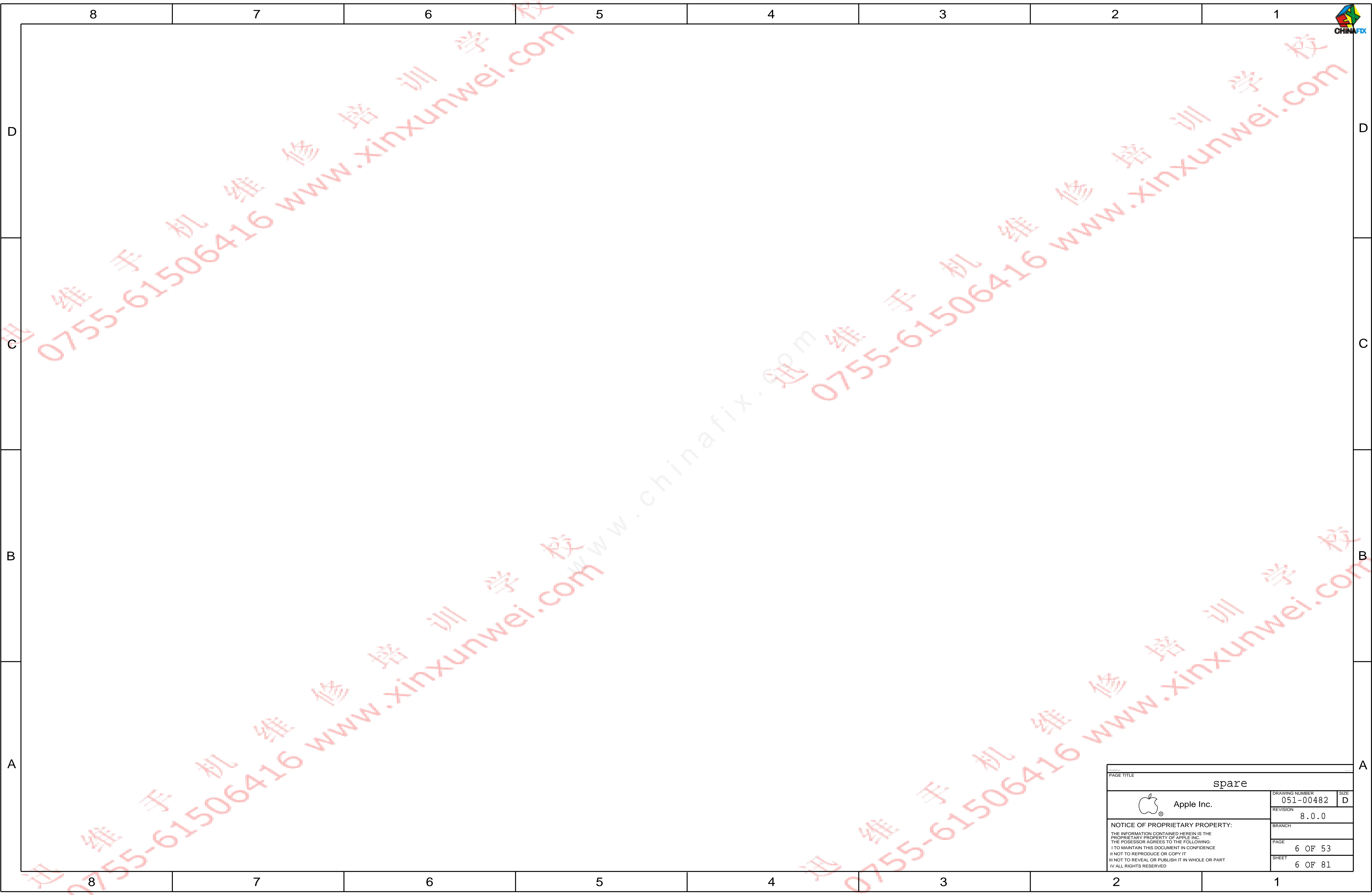
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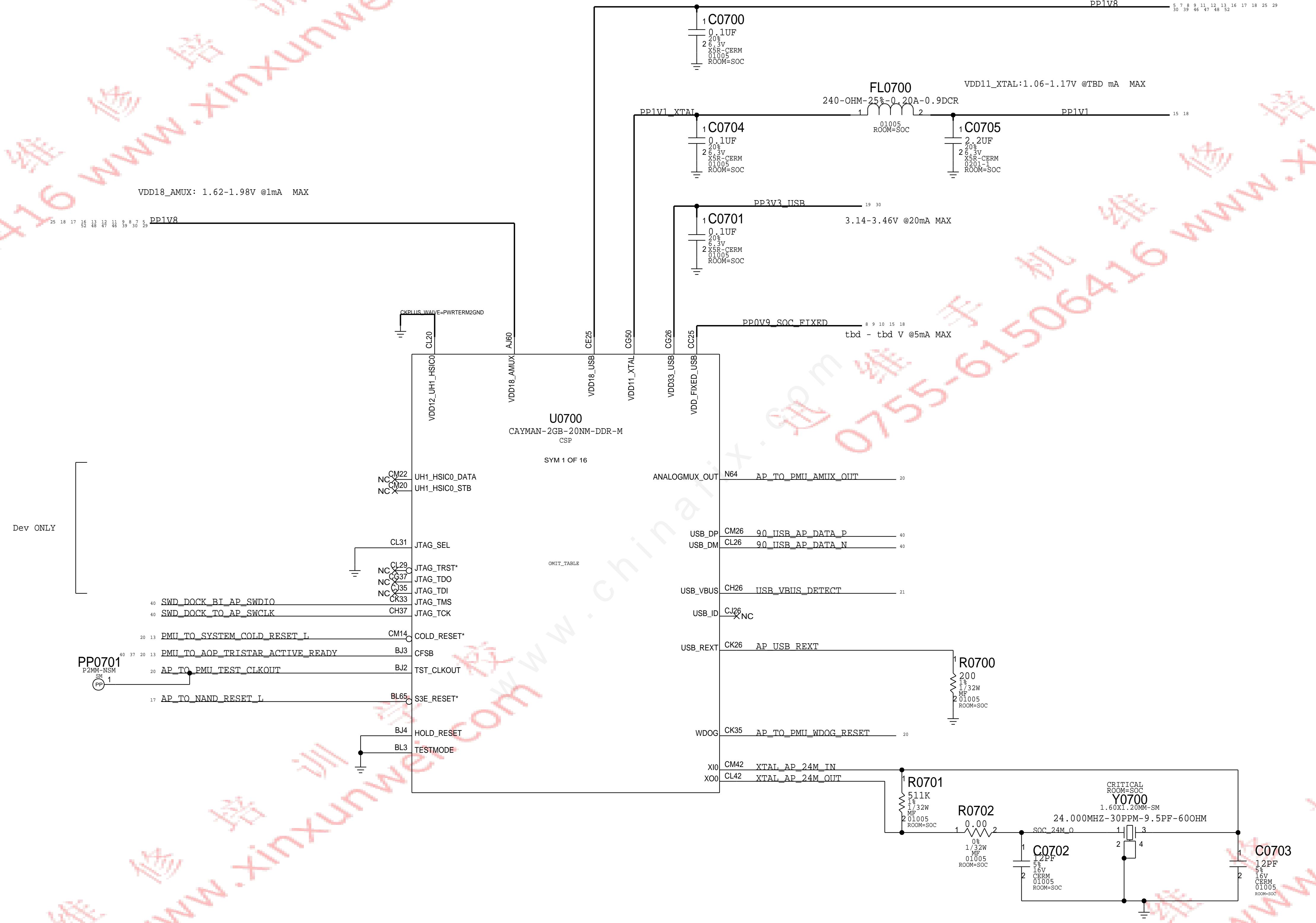
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SOC - USB, JTAG, XTAL



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SOC - PCIE INTERFACES



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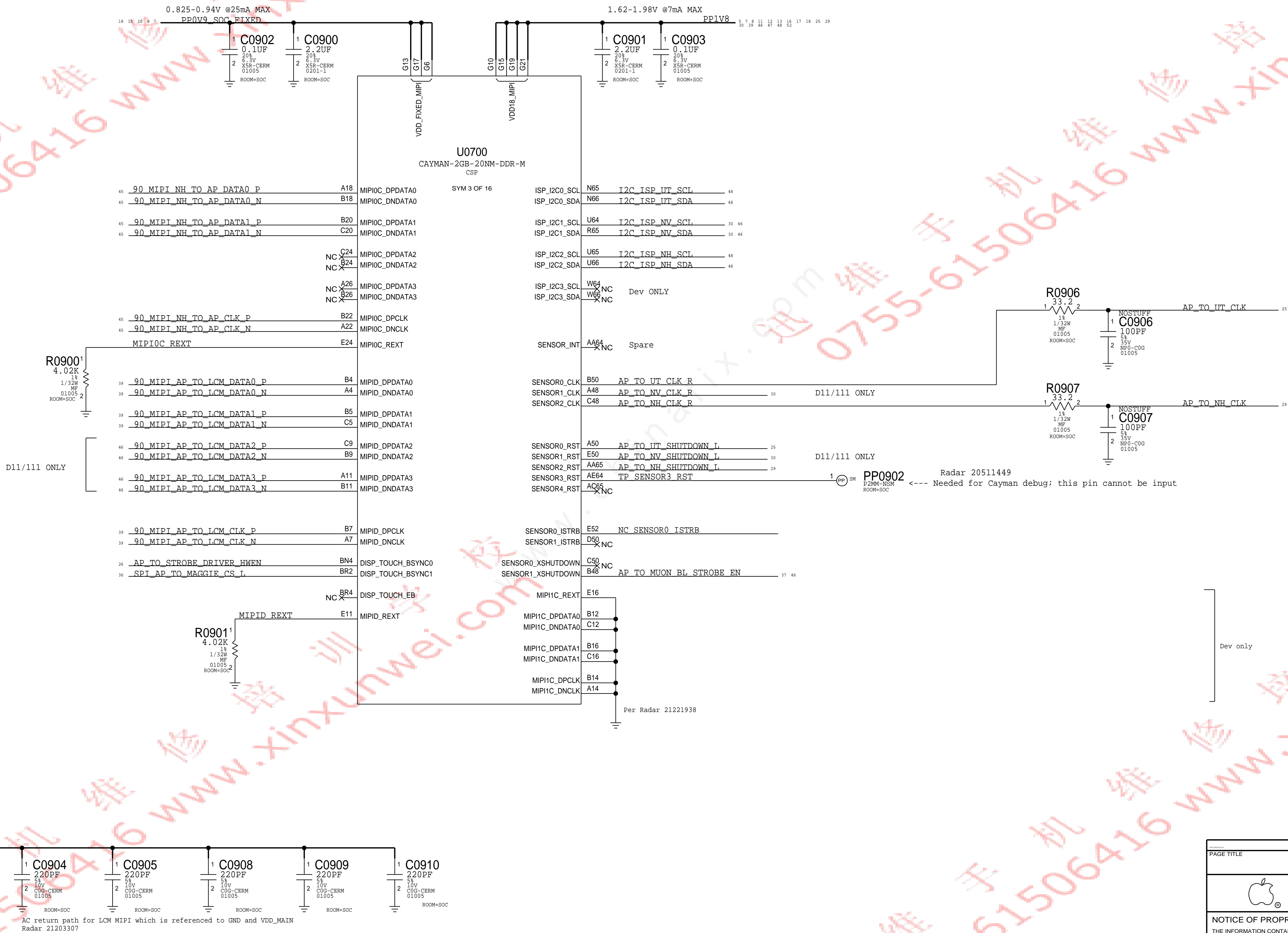
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SOC - MIPI & ISP INTERFACES



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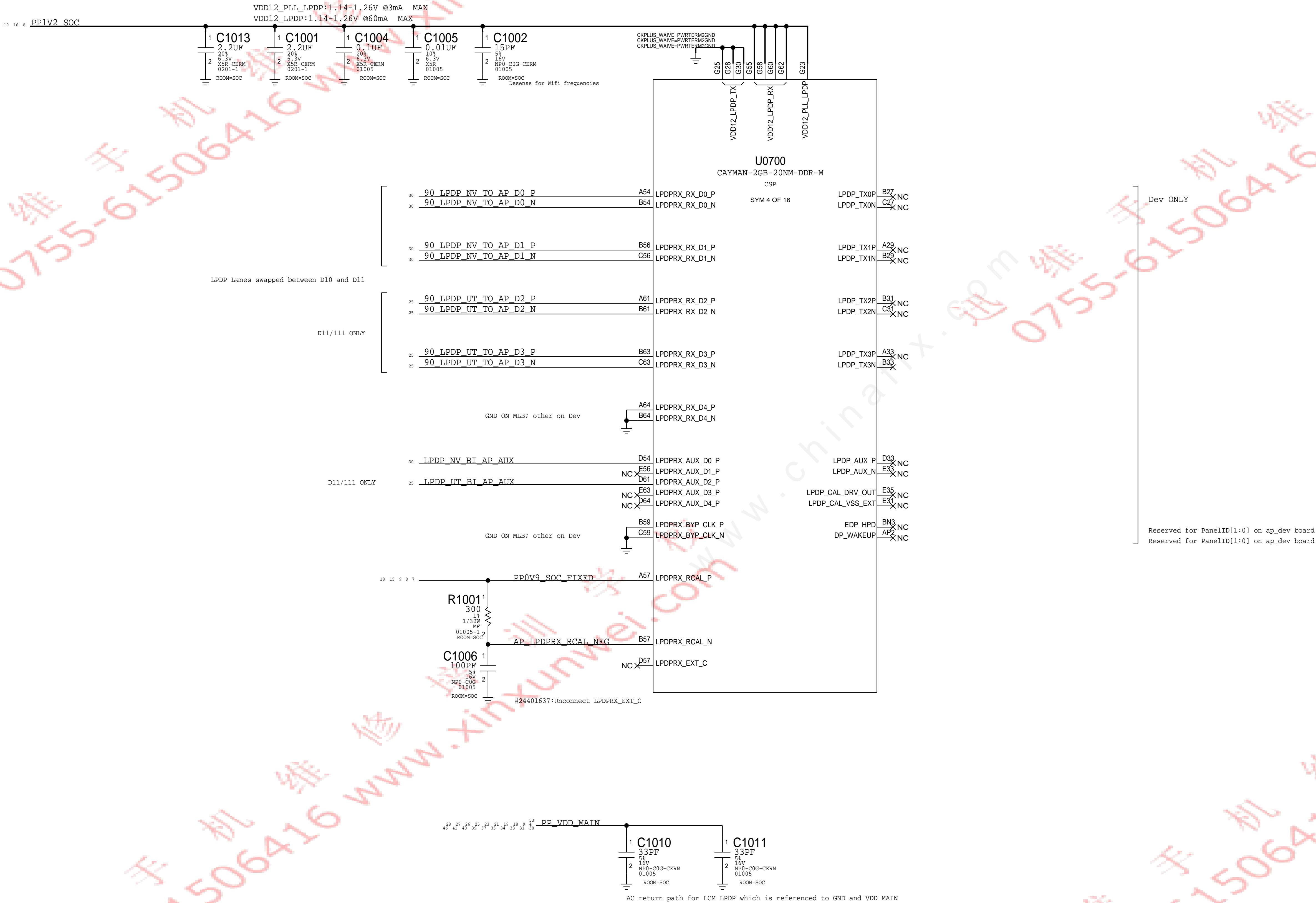
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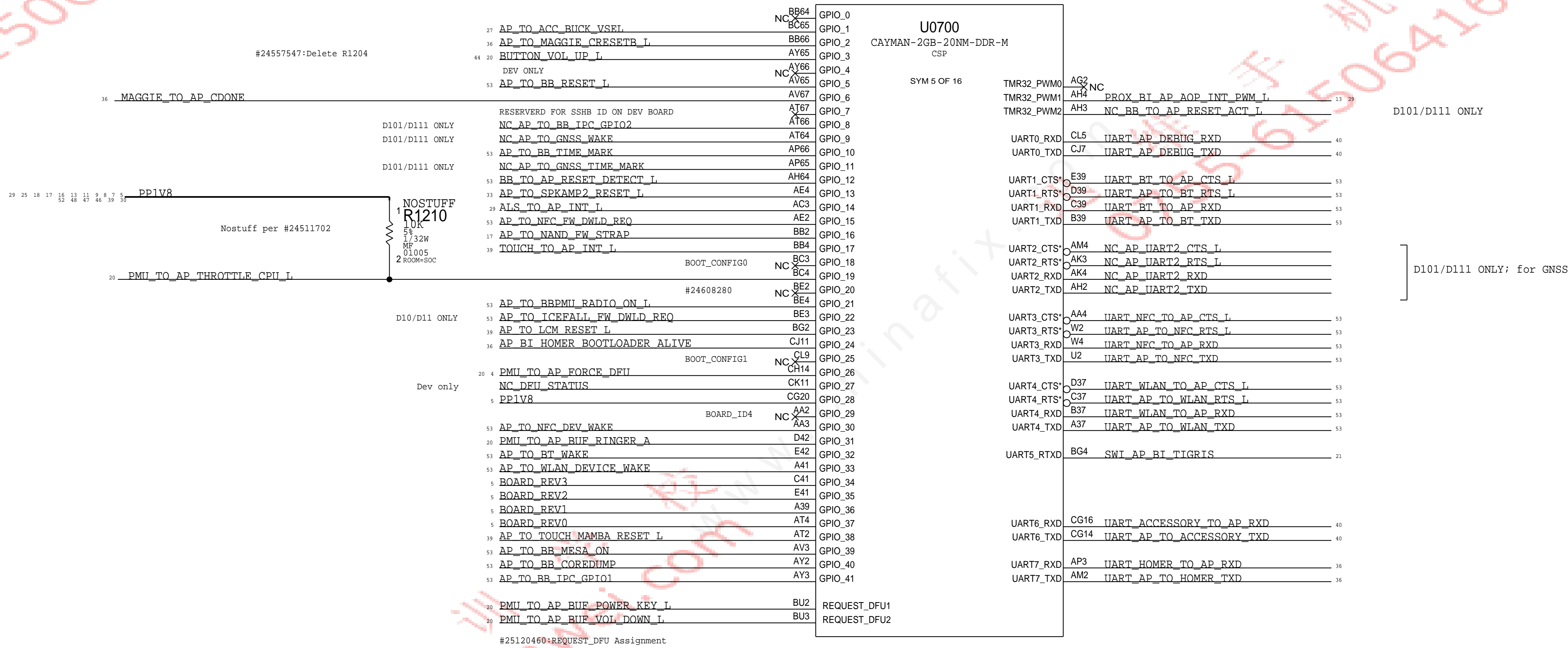
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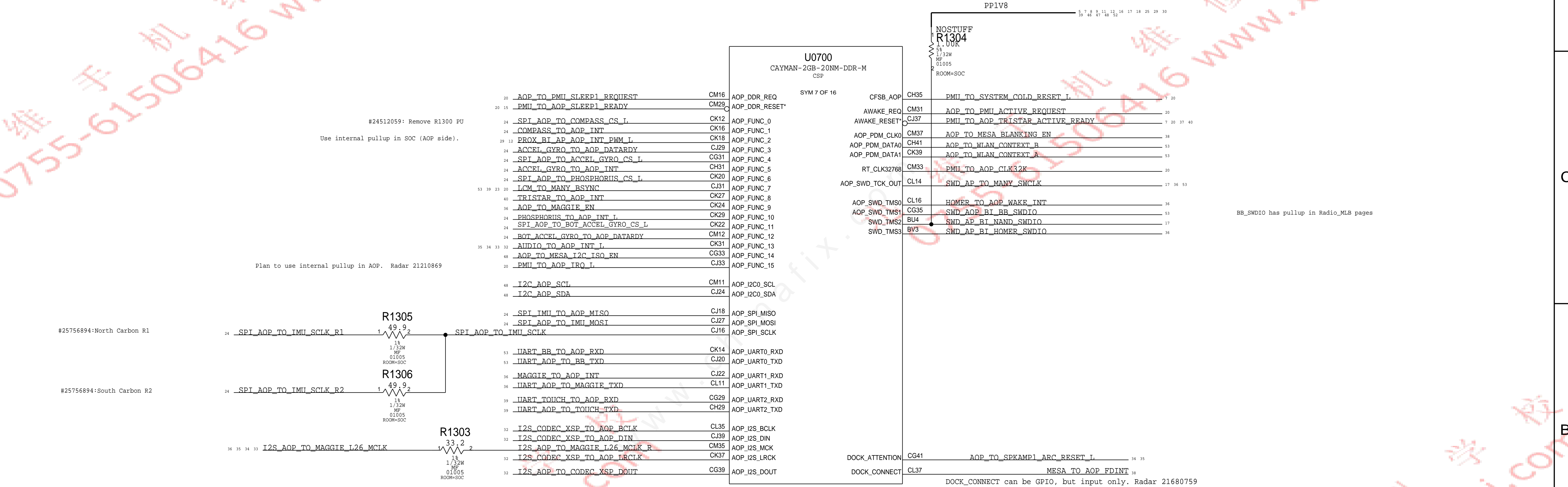
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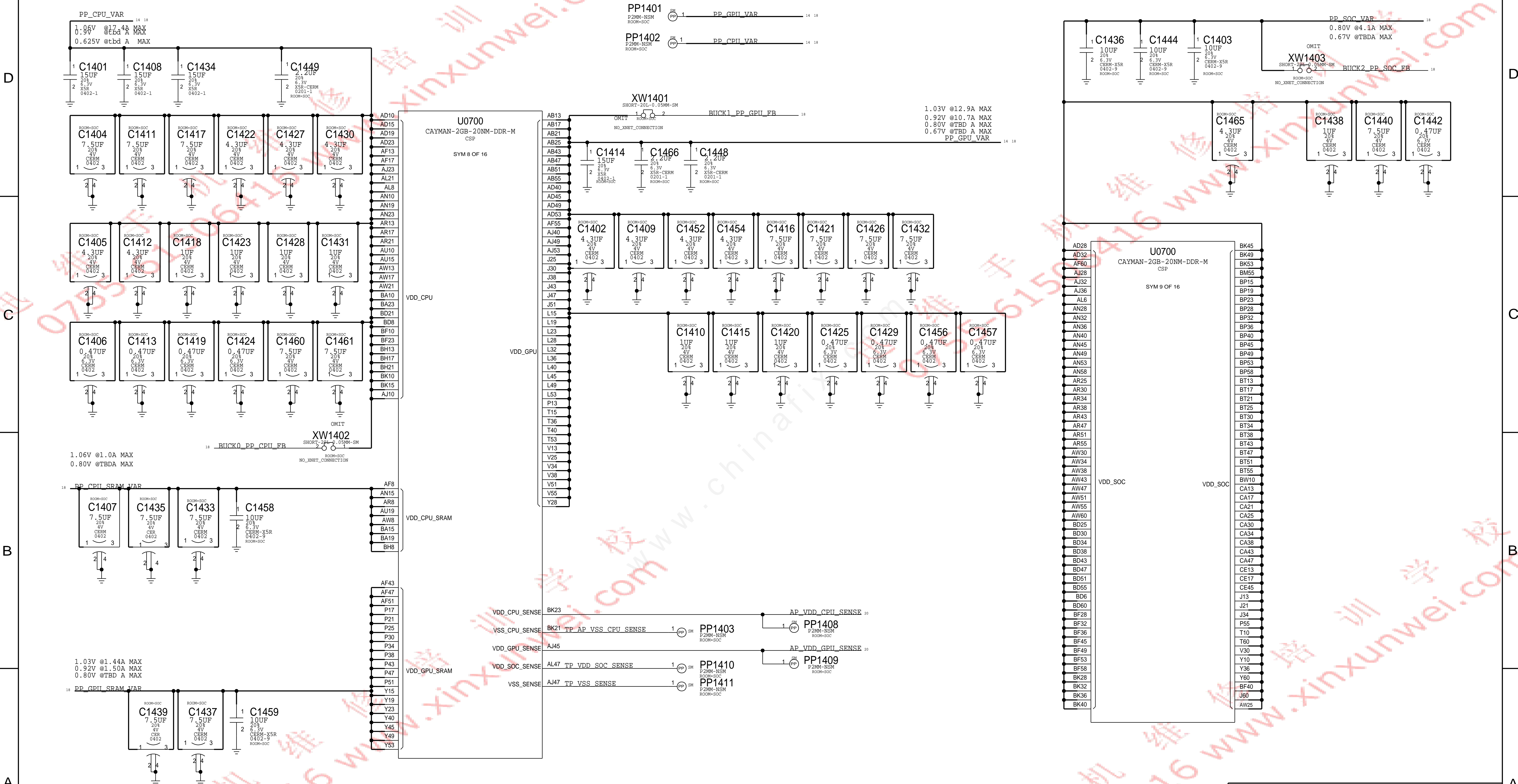
SOC - GPIO INTERFACES



SOC - AOP

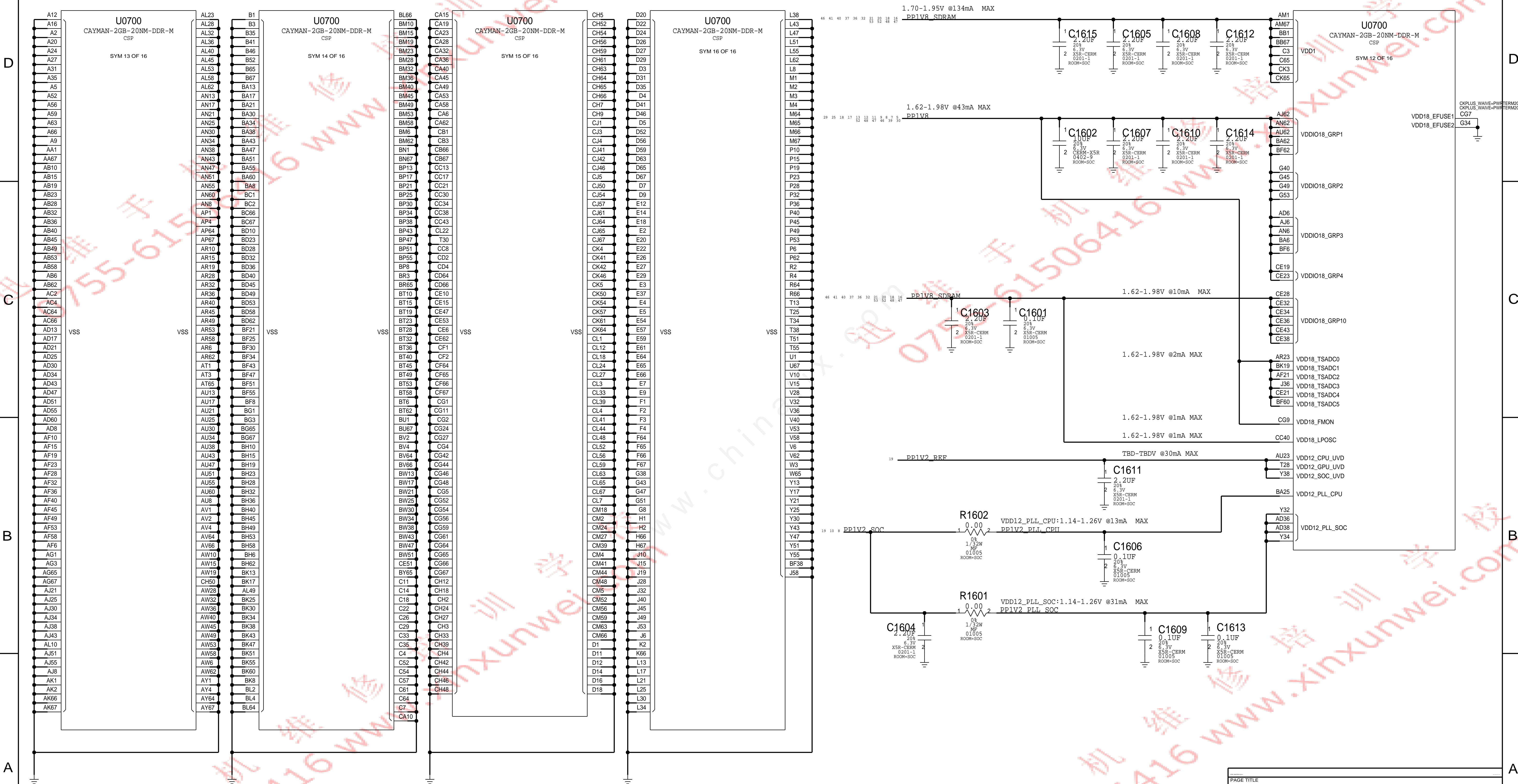


SOC - CPU, GPU & SOC RAILS

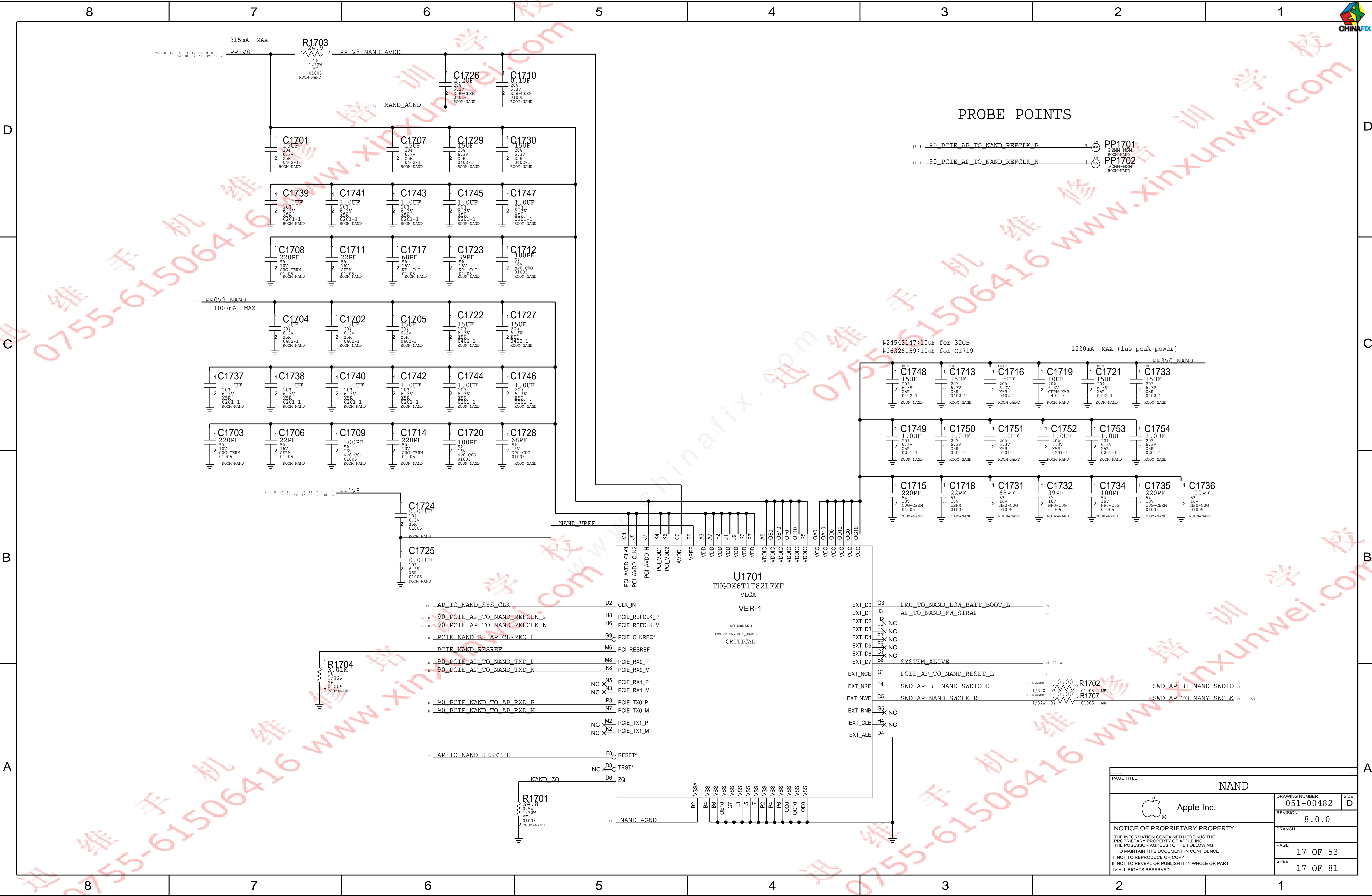




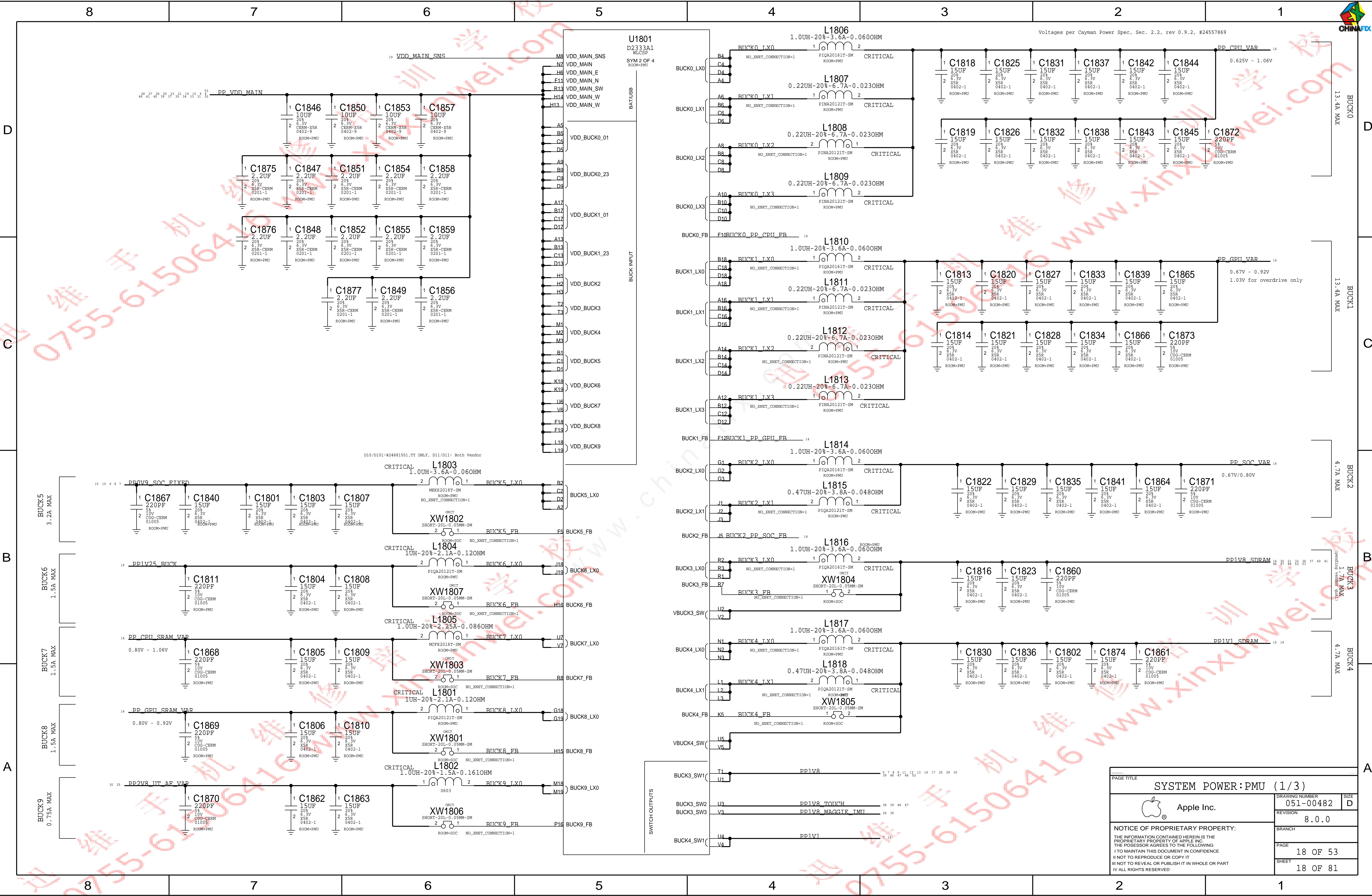
SOC - POWER SUPPLIES




PAGE TITLE			SOC:POWER (3/3)		
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			REVISION	8.0.0	D
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	8.0.0		
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	SHEET		
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ADELYN LDO SPECS

LDO#	ADJ. RANGE, LOW	ADJ. RANGE, HI	ACCURACY	MAX. CURRENT
LDO1 (Ca)	1.2-2.475V	2.4-3.675V	+/-1.4%	50mA
LDO2 (Ca)	1.2-2.475V	2.4-3.675V	+/-2.5%	50mA
LDO3 (Ca)	1.2-2.475V	2.4-3.675V	+/-2.5%	50mA
LDO4 (D)	0.7-1.2V		+/-2.5%	60mA
LDO5 (F)	2.5-3.6V(tbc)		+/-75mV	1000mA
LDO6 (Cb)	1.2-2.475V	2.4-3.675V	+/-2.5%	250mA (500/100mA in bypass)
LDO7 (Cb)	1.2-2.475V	2.4-3.675V	+/-30mV	250mA
LDO8 (Cb)	1.2-2.475V	2.4-3.675V	+/-30mV	250mA
LDO9 (Cb)	1.2-2.475V	2.4-3.675V	+/-25mV	250mA
LDO10 (Ga)	0.7-1.2V		+/-4.5%	1150mA

LDO#	ADJ. RANGE, LOW	ADJ. RANGE, HI	ACCURACY	MAX. CURRENT
LDO11 (Cb)	1.2-2.475V	2.4-3.675V	+/-30mV	250mA
LDO12 (E)	1.8V		+/-5%	10mA
LDO13 (Cb)	1.2-2.475V	2.4-3.675V	+/-30mV	250mA
LDO14 (Gb)	0.7-1.4V		+/-3.0%	400mA
LDO15 (Ca)	1.2-2.475V	2.4-3.675V	+/-2.5%	50mA
LDO16 (Cb)	1.2-2.475V	2.4-3.675V	+/-30mV	250mA
LDO17 (Ca)	1.2-2.475V	2.4-3.675V	+/-2.5%	50mA
LDO18 (Gb)	0.7-1.4V		+/-3.0%	400mA
LDO19 (Gb)	0.7-1.4V		+/-3.0%	400mA
LDO_RTC	2.5V		+/-2.0%	10mA
BUF_1V2	1.2V		+/-5.0%	10mA

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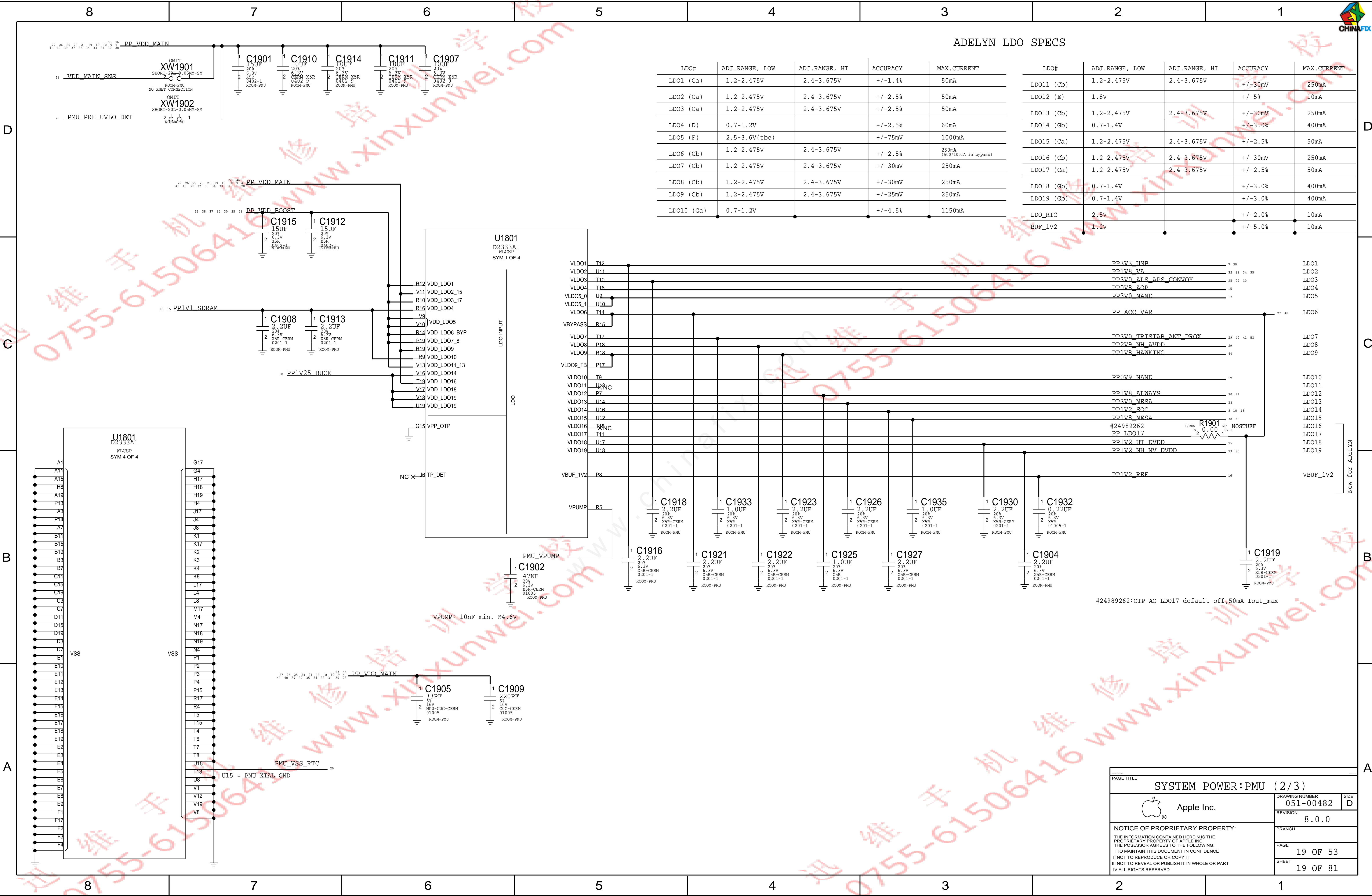
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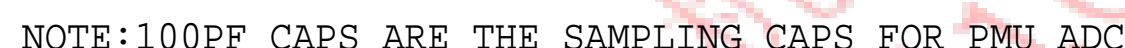
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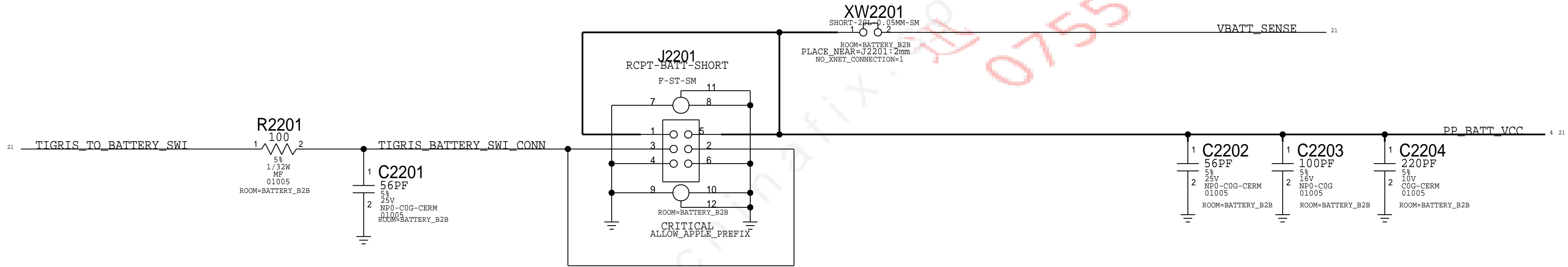
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			BRANCH		
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<p>QUESTION 1</p> <p>1. The following table shows the results of a survey of 100 people who were asked to rate their satisfaction with their current job on a scale of 1 to 5, where 1 is 'Very Dissatisfied' and 5 is 'Very Satisfied'.</p> <table border="1"> <thead> <tr> <th>Satisfaction Level</th> <th>Number of People</th> </tr> </thead> <tbody> <tr> <td>1 (Very Dissatisfied)</td> <td>10</td> </tr> <tr> <td>2 (Dissatisfied)</td> <td>20</td> </tr> <tr> <td>3 (Neutral)</td> <td>30</td> </tr> <tr> <td>4 (Satisfied)</td> <td>25</td> </tr> <tr> <td>5 (Very Satisfied)</td> <td>15</td> </tr> </tbody> </table> <p>Calculate the mean satisfaction level.</p>	Satisfaction Level	Number of People	1 (Very Dissatisfied)	10	2 (Dissatisfied)	20	3 (Neutral)	30	4 (Satisfied)	25	5 (Very Satisfied)	15	<p>ANSWER 1</p> <p>3.25</p>
Satisfaction Level	Number of People												
1 (Very Dissatisfied)	10												
2 (Dissatisfied)	20												
3 (Neutral)	30												
4 (Satisfied)	25												
5 (Very Satisfied)	15												



BATTERY CONNECTOR

THIS ONE ON MLB ----> 516S00172 (matches d10 m1b MCO rev 27)



PAGE TITLE		
SYSTEM POWER:BATTERY CONN		
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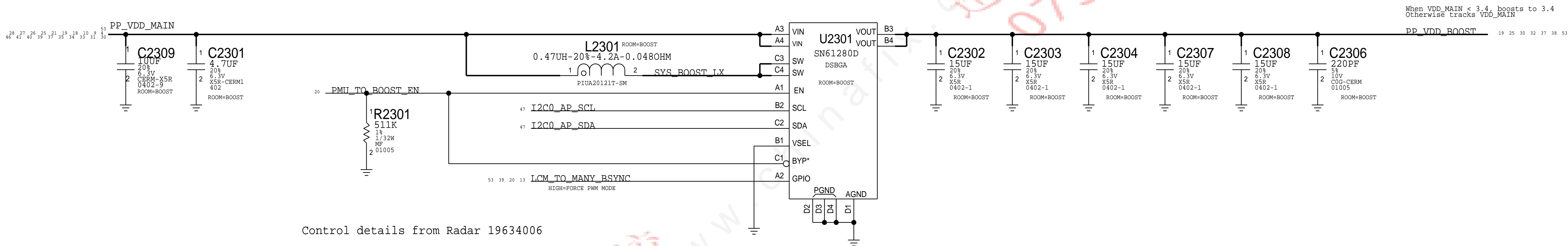
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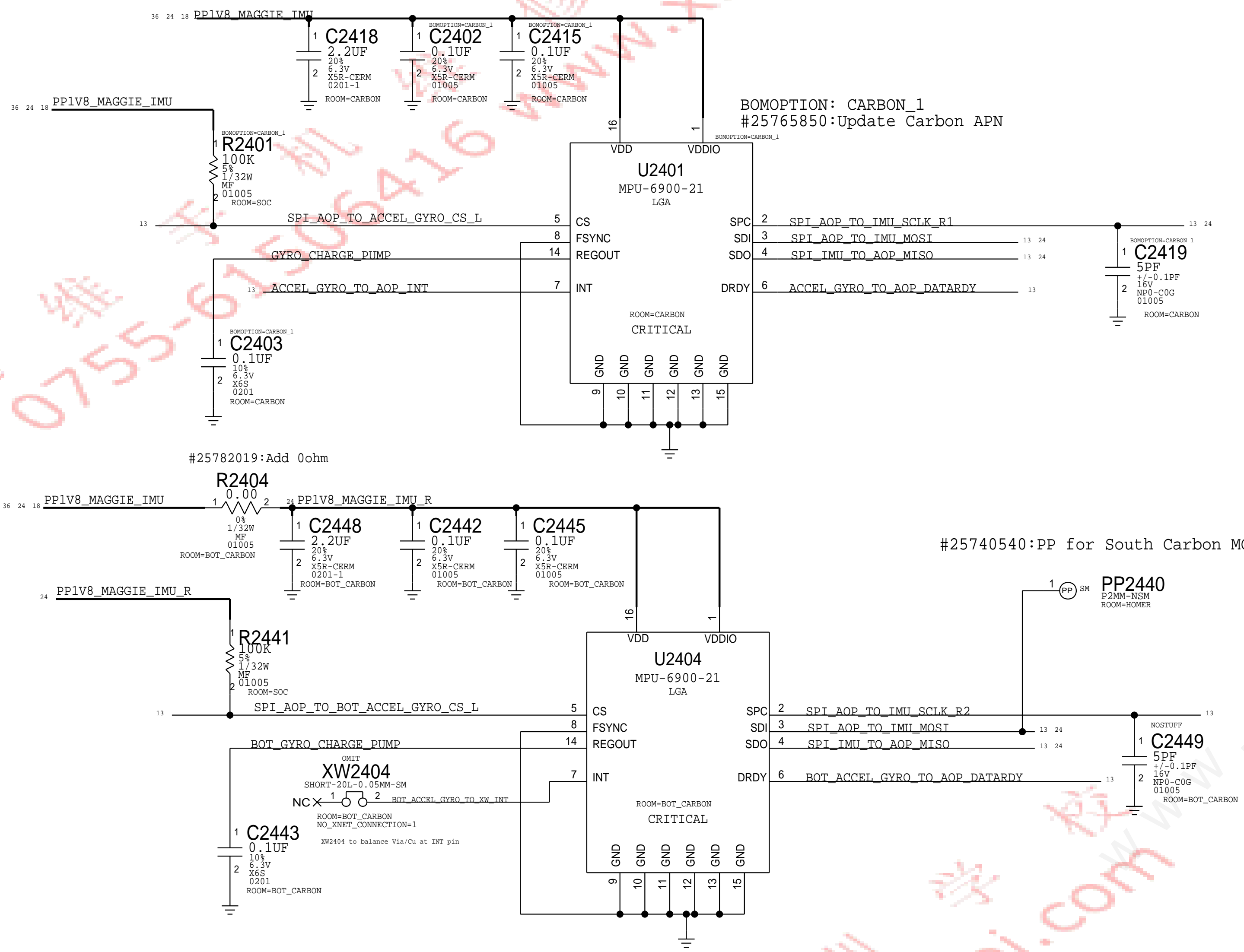
BOOST



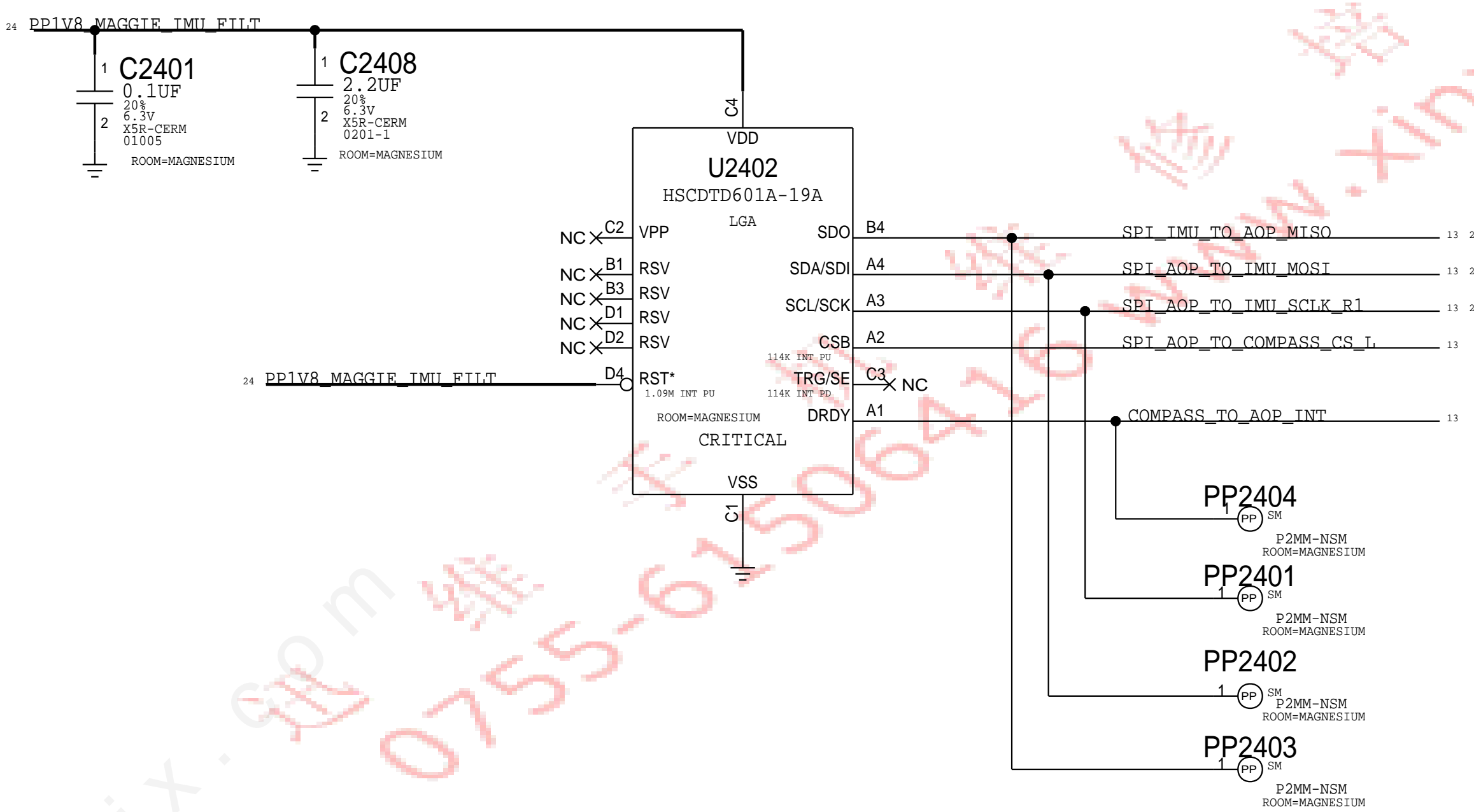
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SYSTEM POWER:BOOST		
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CARBON - ACCEL & GYRO

INVENSENSE, MPU-6800: C2403=0.1UF



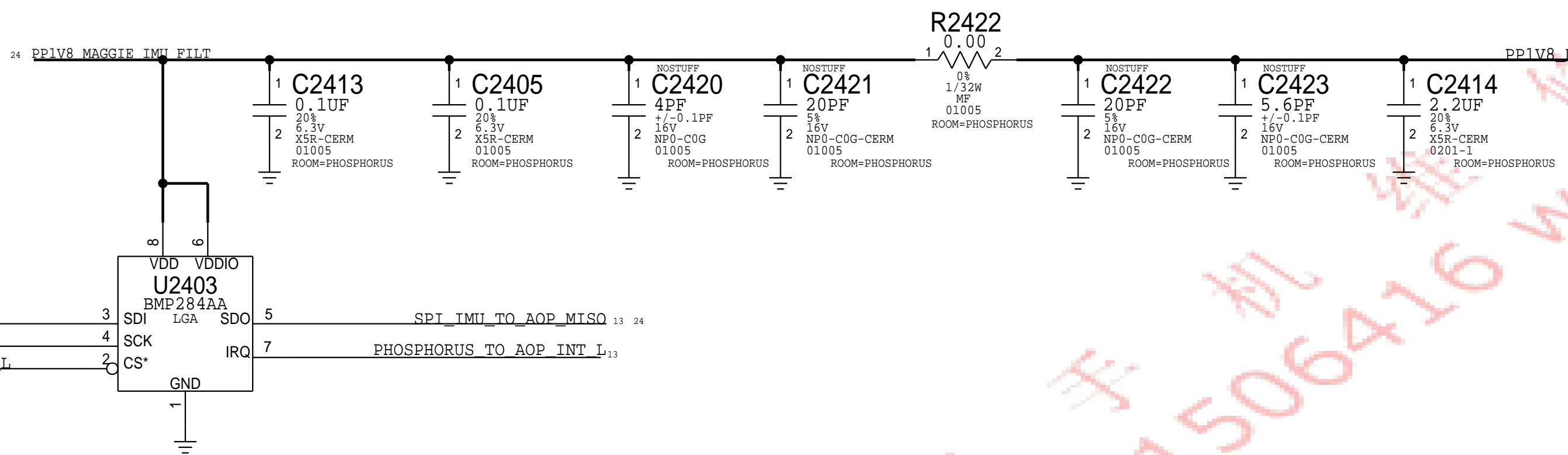
MAGNESIUM - COMPASS



PHOSPHORUS

#24593845

BOSCH (APN:338S00188): nostuff C2420/C2421/C2422/C2423 and R2403 PU
ST (APN:338S00230): stuff C2420-C2423, C2420R2422 with 155S00017, stuff R2403 PU
C2420=4pF(131S0253),C2405=3pF(131S0251) per #25691124



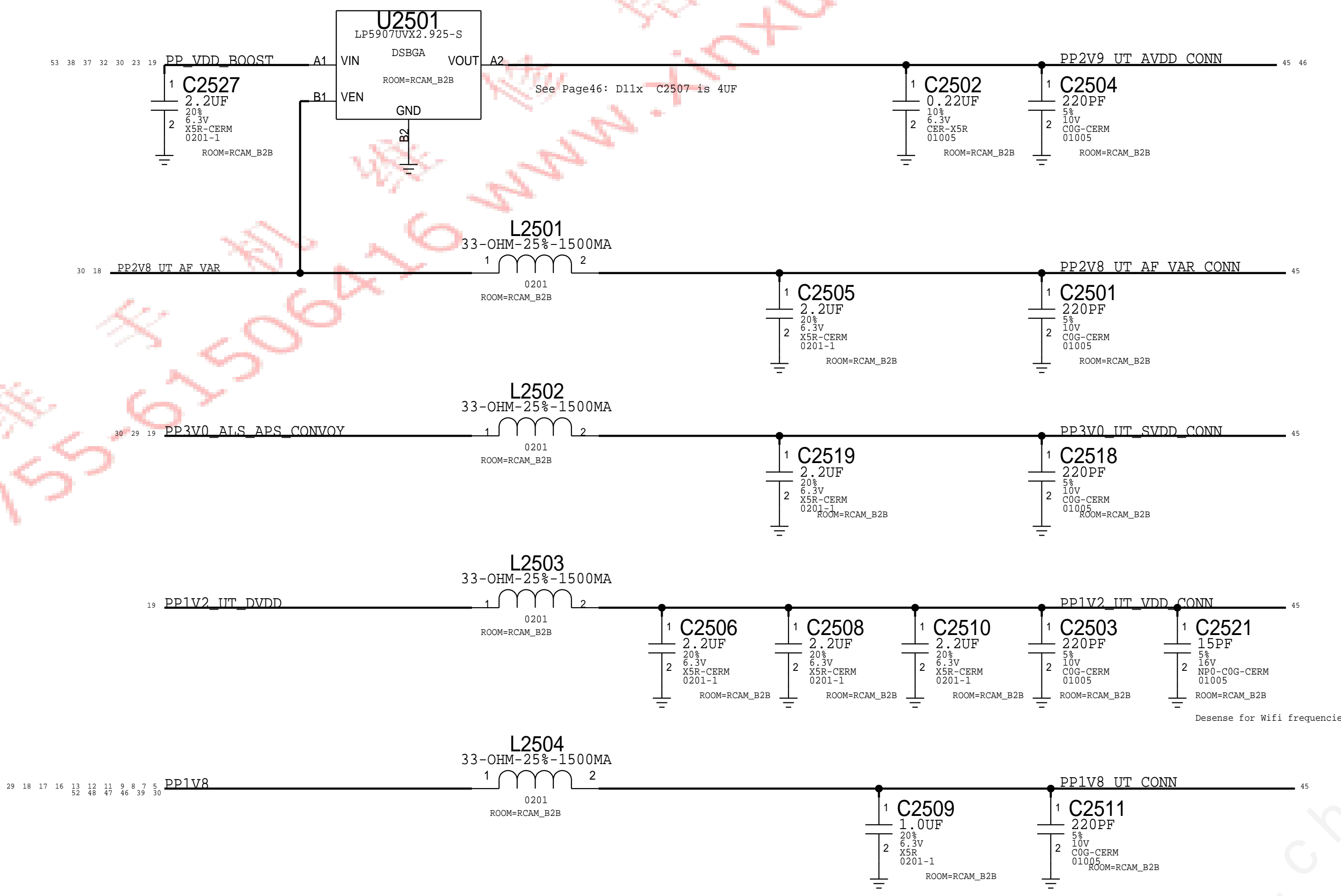
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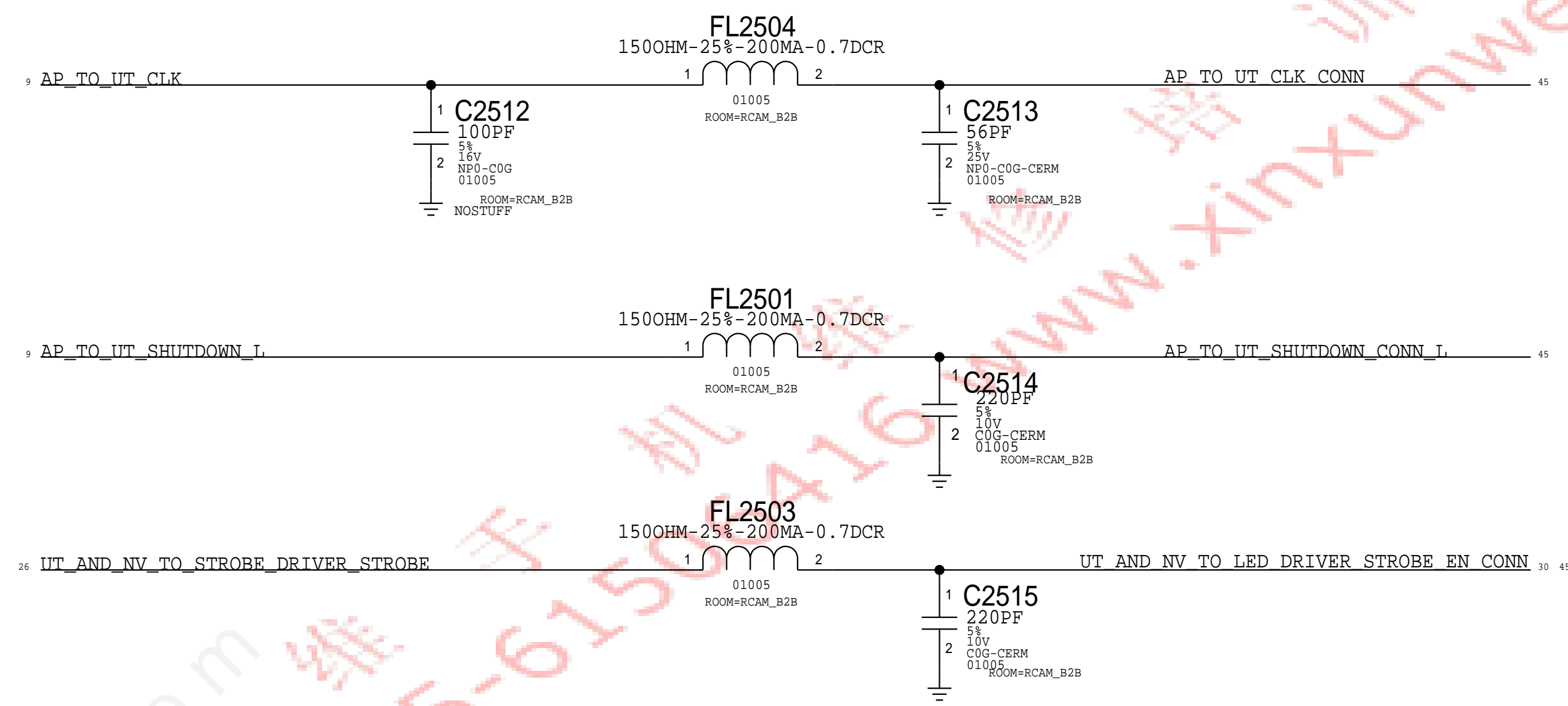
UTAH POWER

NOTE: OUTPUT IMPEDANCE MUST BE >0.005-OHM
IN ORDER TO MEET CAP ESR REQUIREMENT PER LDO SPEC.
VENDOR ALSO RECOMMENDS CIN = COUT FOR STABILITY

Scrub voltage selection

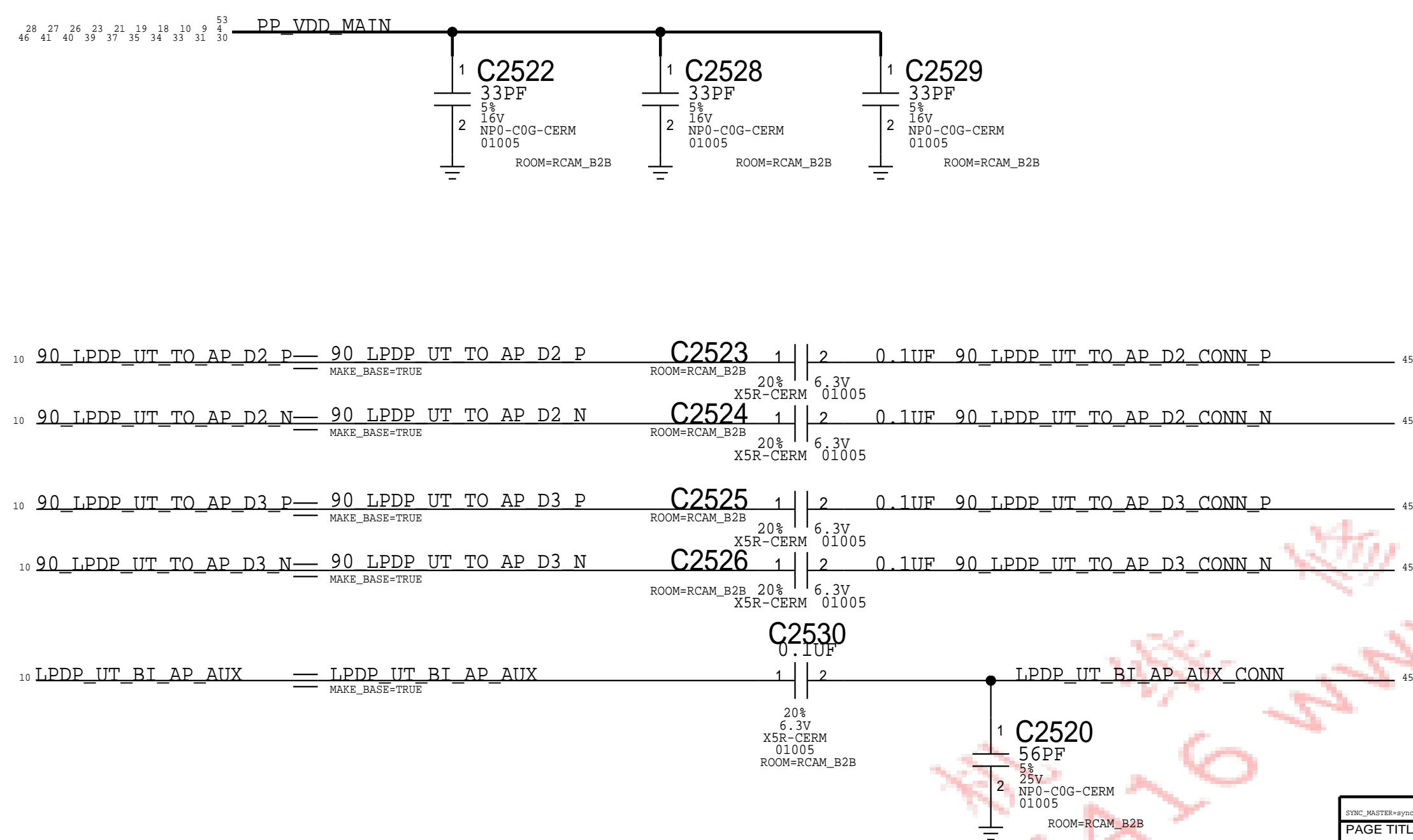


IO FILTERS



LPDP FILTERS

AC return path for LPDP which is referenced to GND and VDD_MAIN

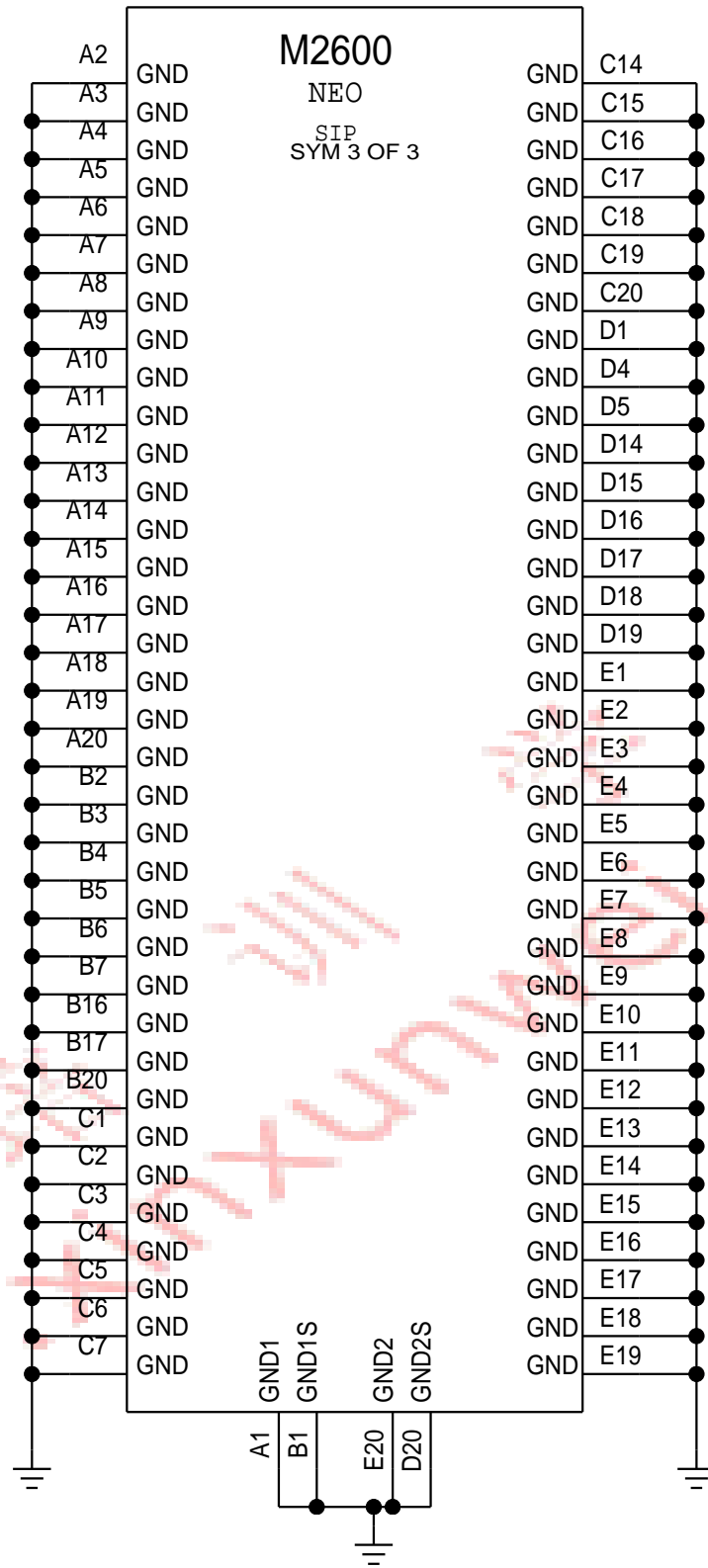
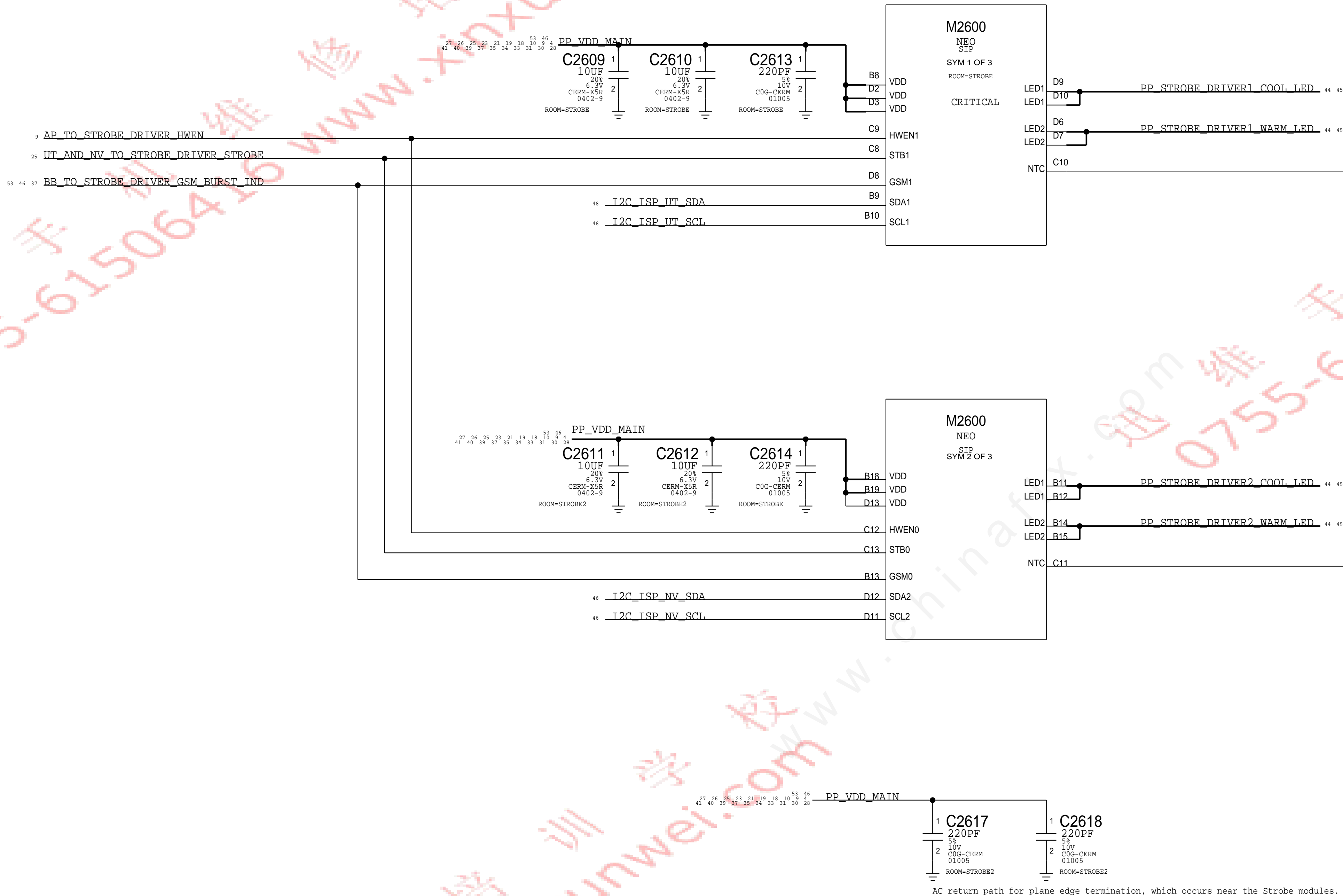


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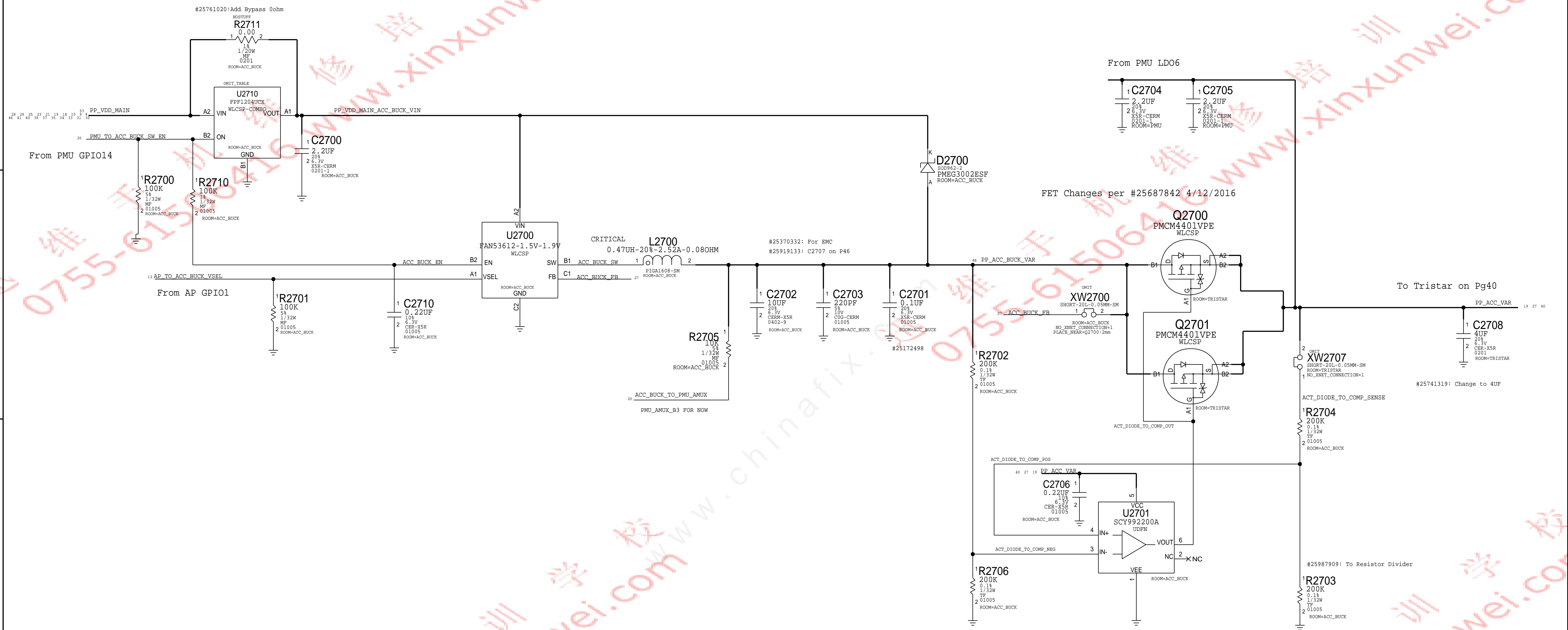
STROBE DRIVERS INSIDE NEO SIP MODULE

D10/sip_neo



PAGE TITLE			
CAMERA:STROBE DRIVER			
	DRAWING NUMBER	051-00482	SIZE
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ACCESSORY BUCK



PAGE TITLE			D	
Accessory: Buck Circuit			D	
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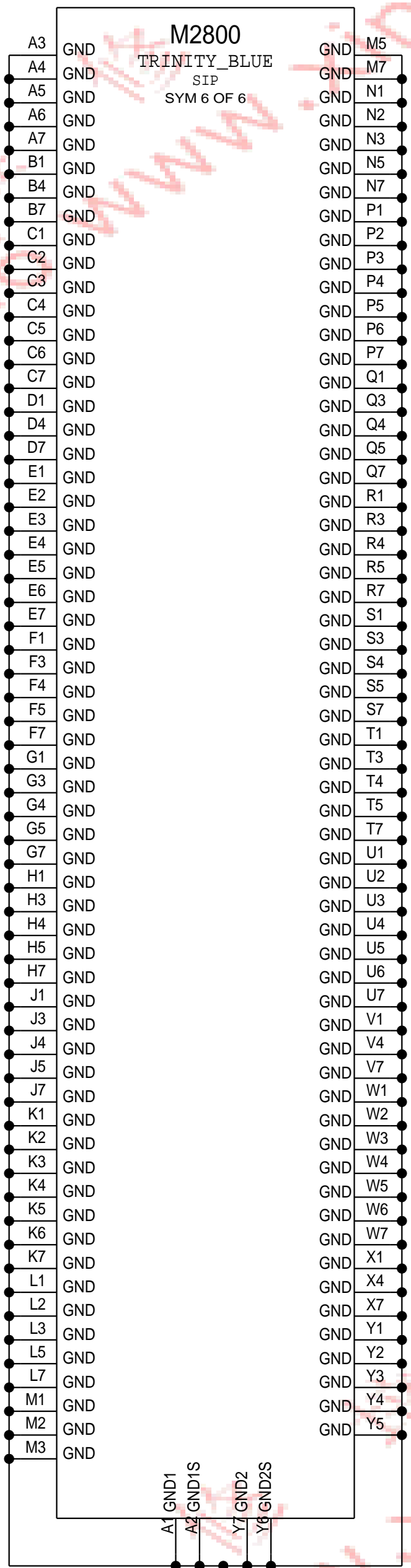
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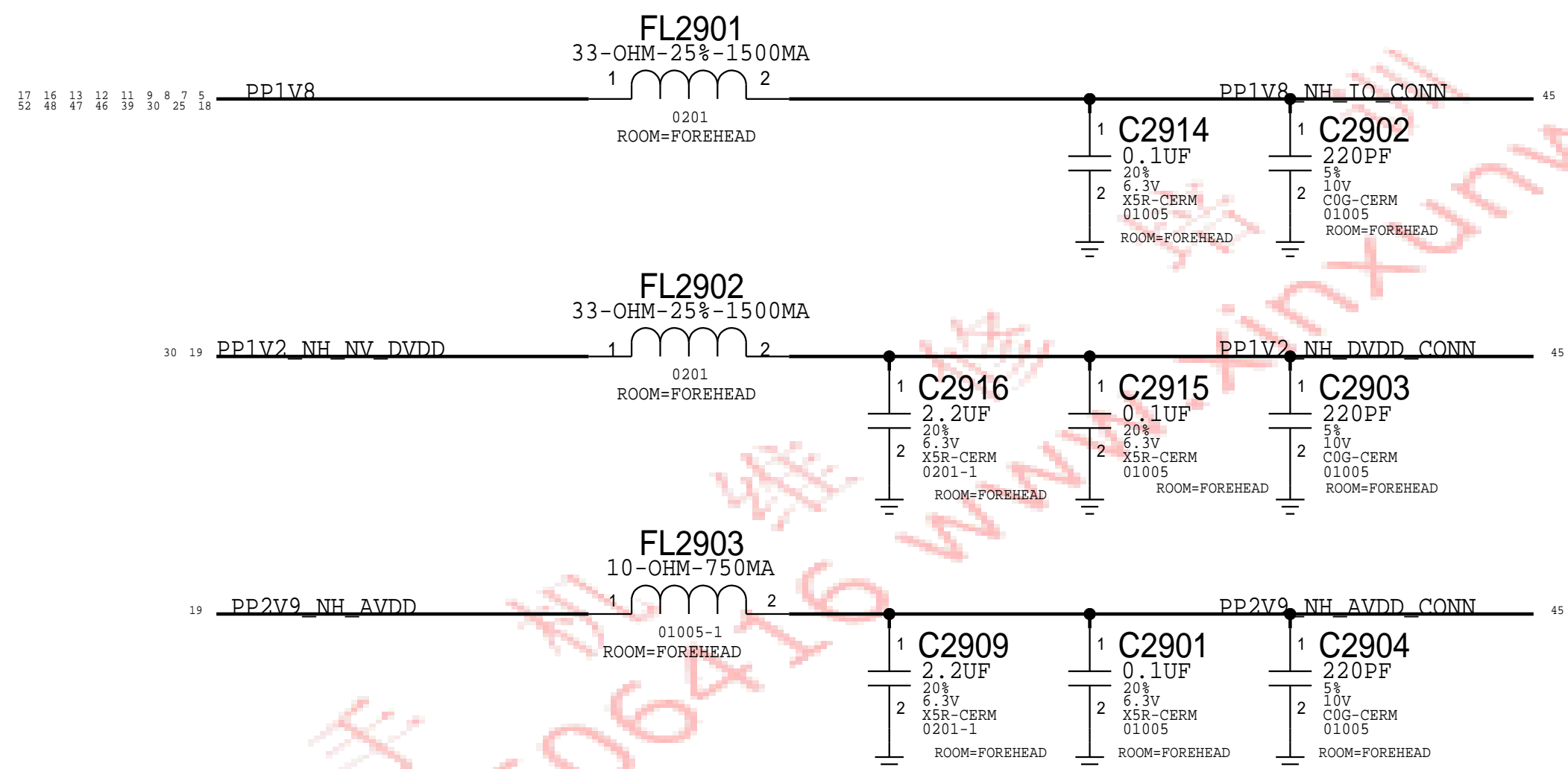
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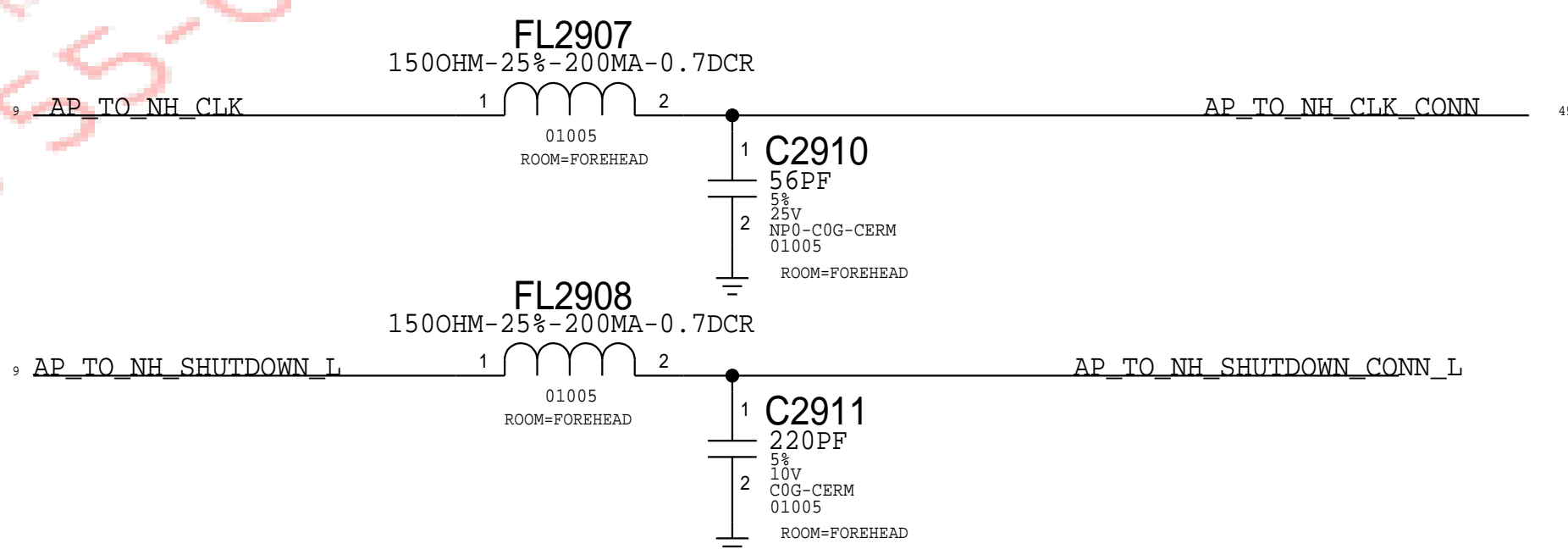
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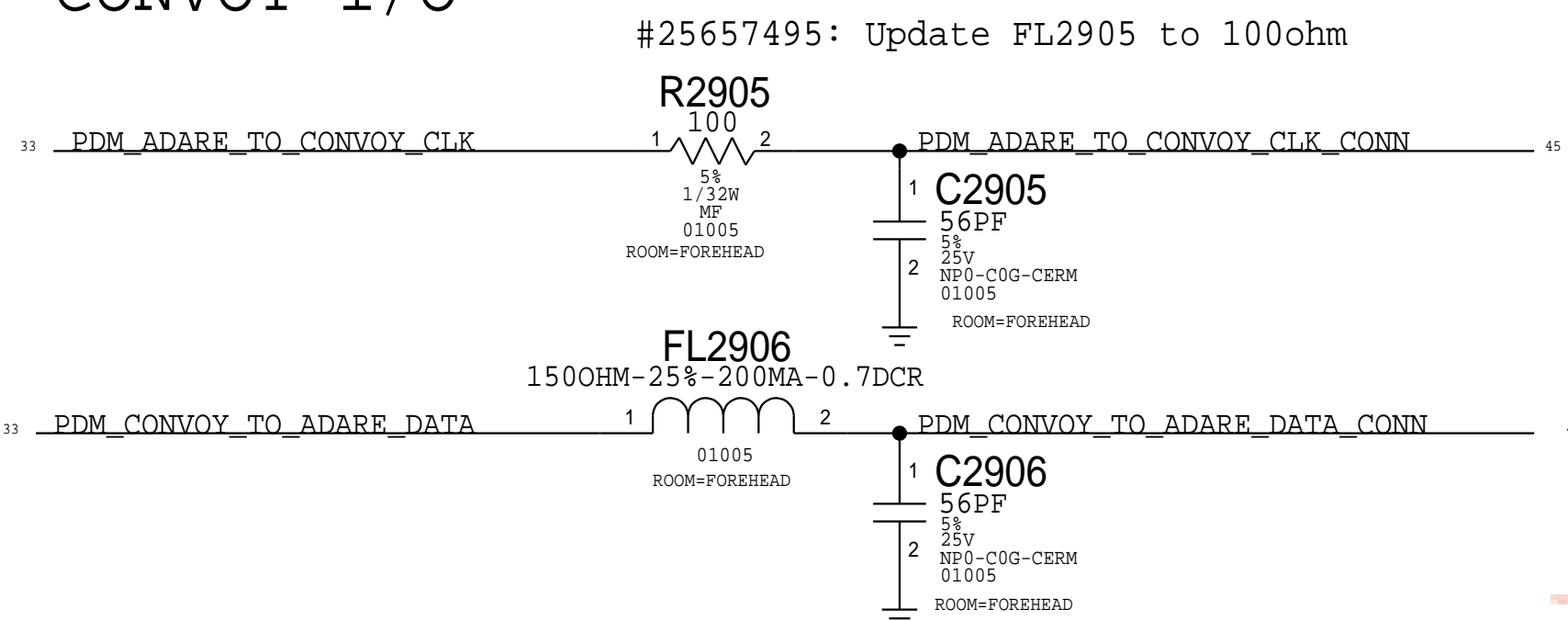
NEW HAMPSHIRE POWER



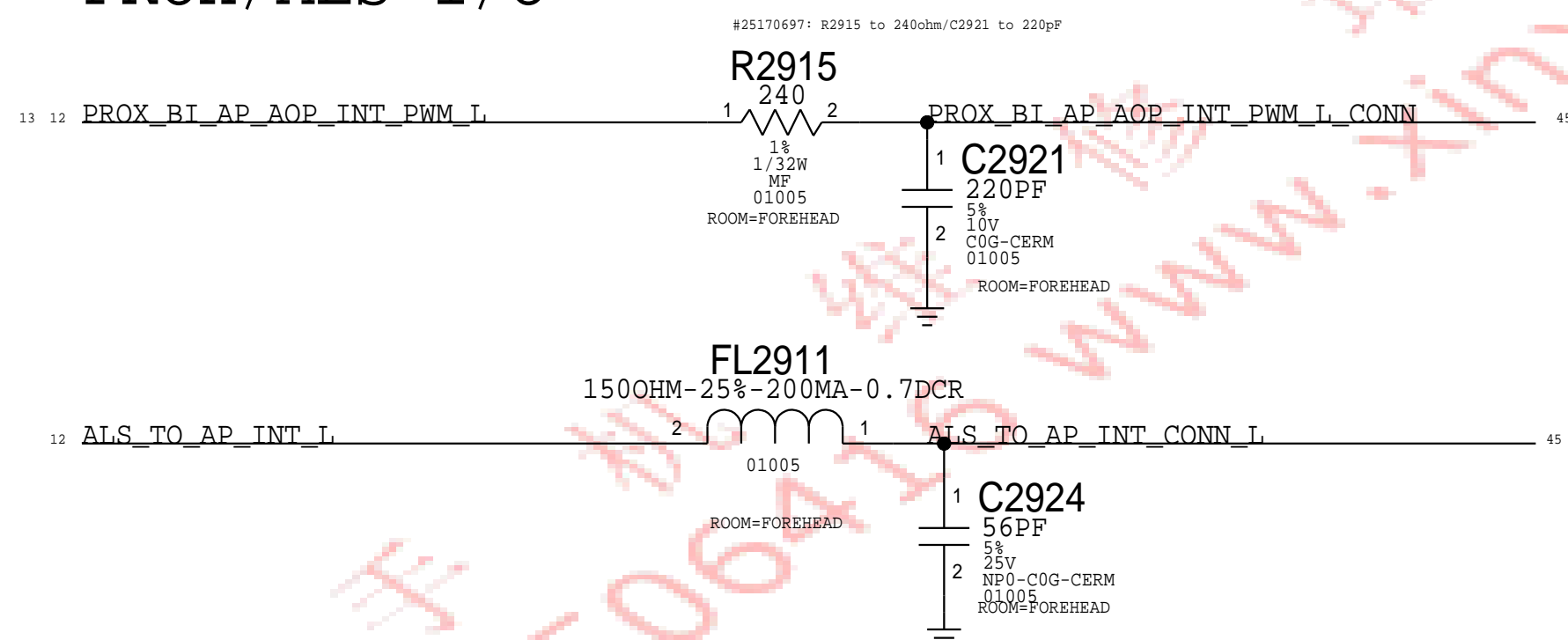
NEW HAMPSHIRE I/O



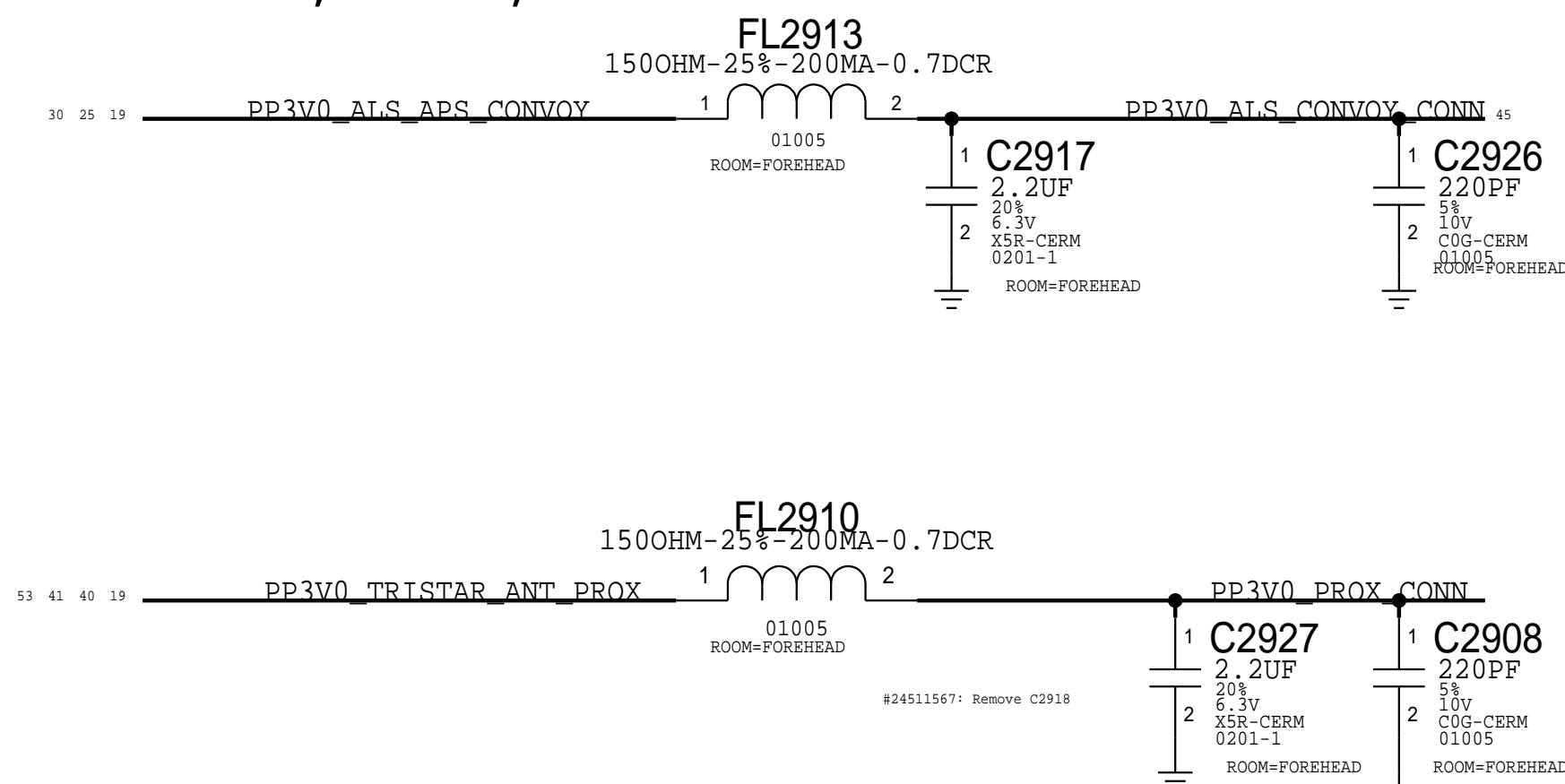
CONVOY I/O



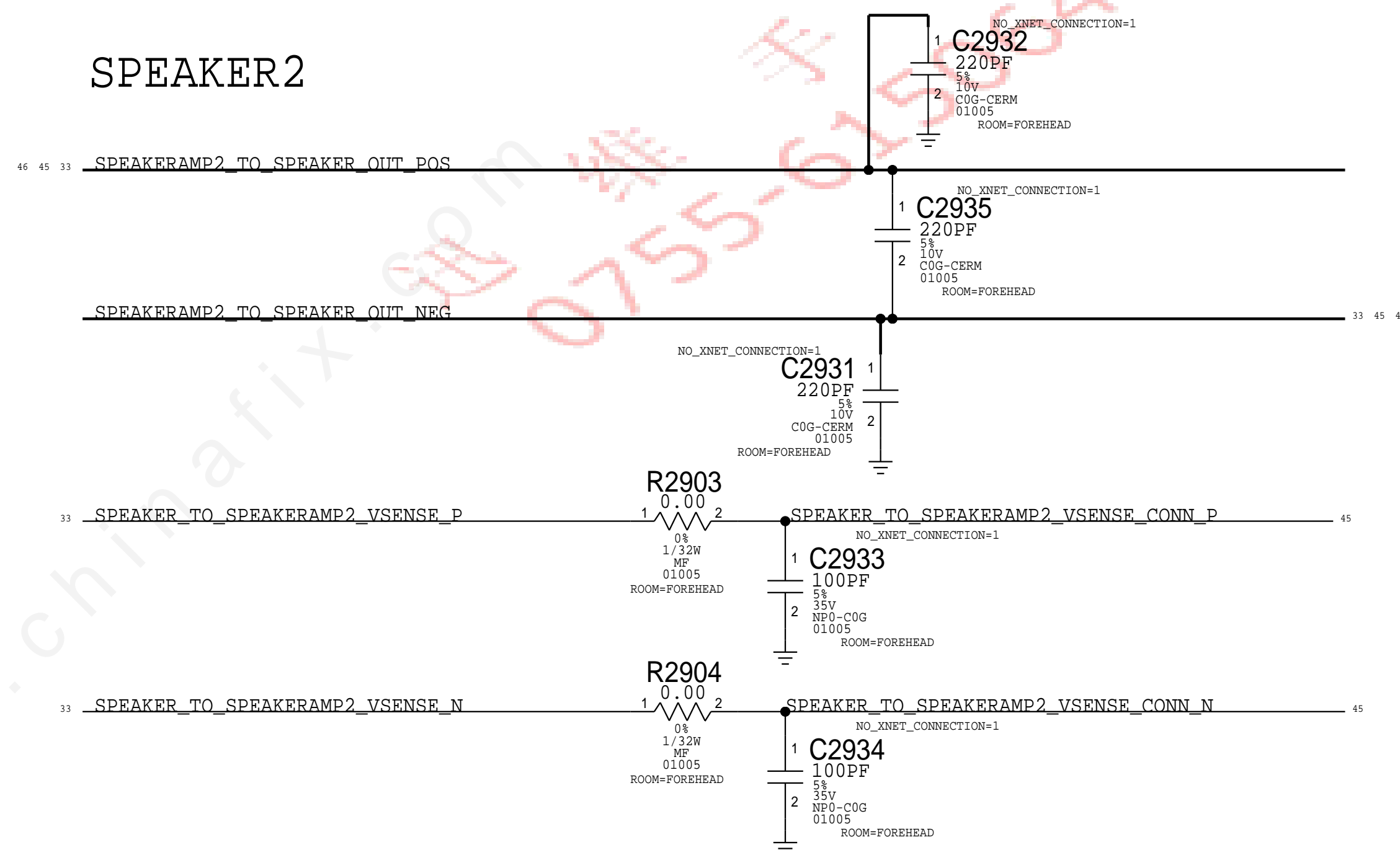
PROX/ALS I/O



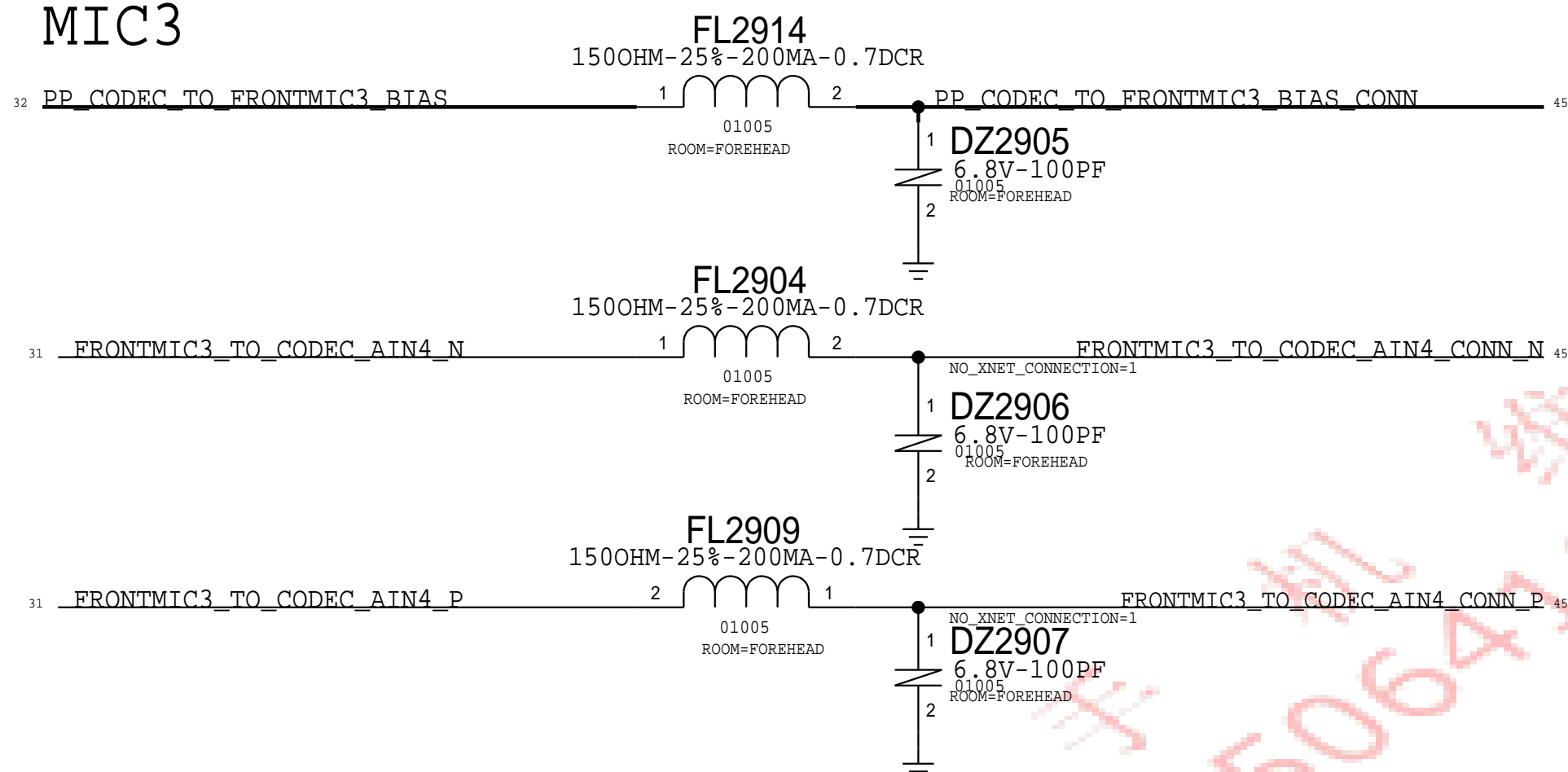
PROX, ALS, & CONVOY POWER



SPEAKER2



MIC3



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B2B:FOREHEAD		
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NEVADA POWER

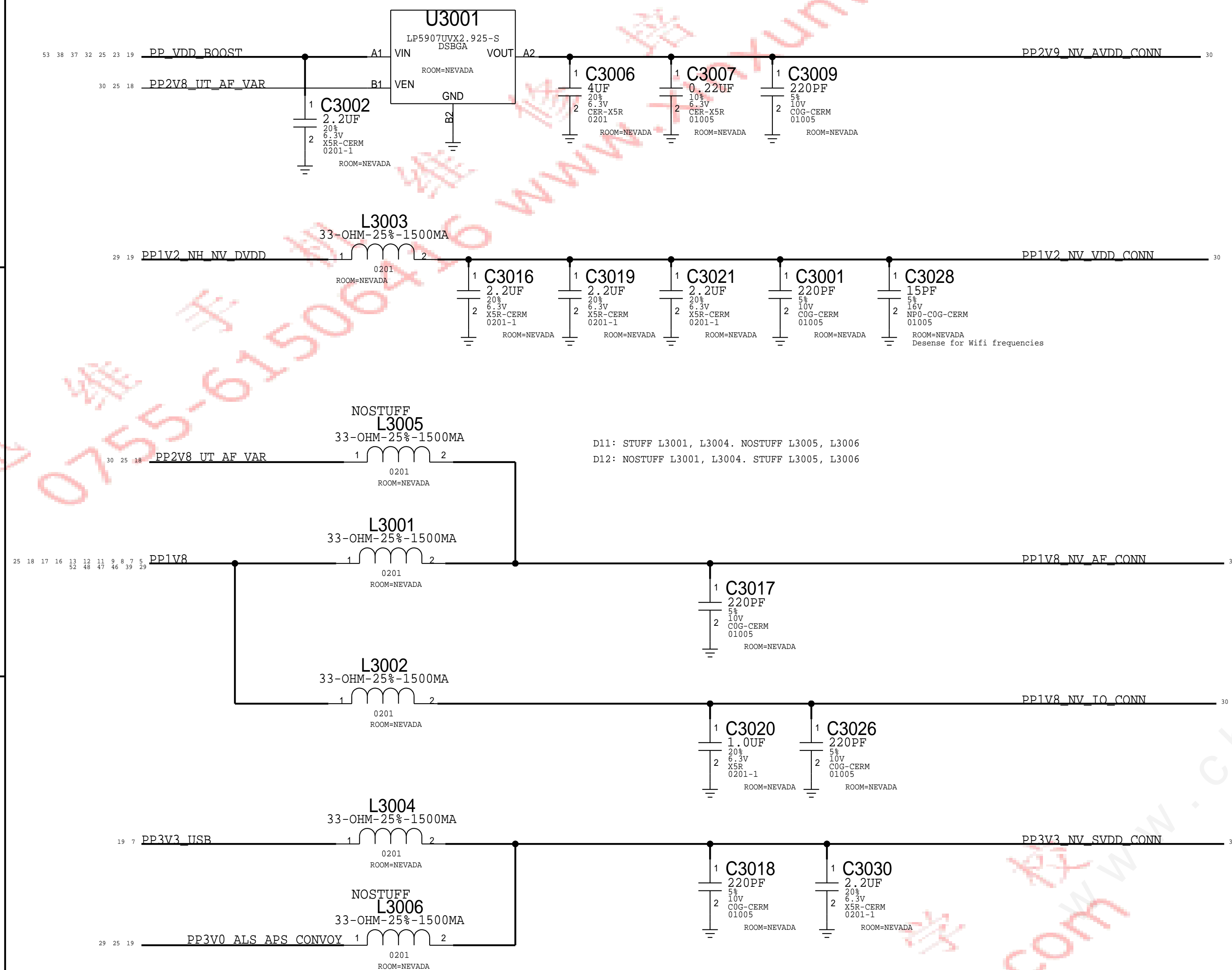
NOTE: OUTPUT IMPEDANCE MUST BE >0.005-OHM
IN ORDER TO MEET CAP ESR REQUIREMENT PER LDO SPEC.
VENDOR ALSO RECOMMENDS CIN = COUT FOR STABILITY

D

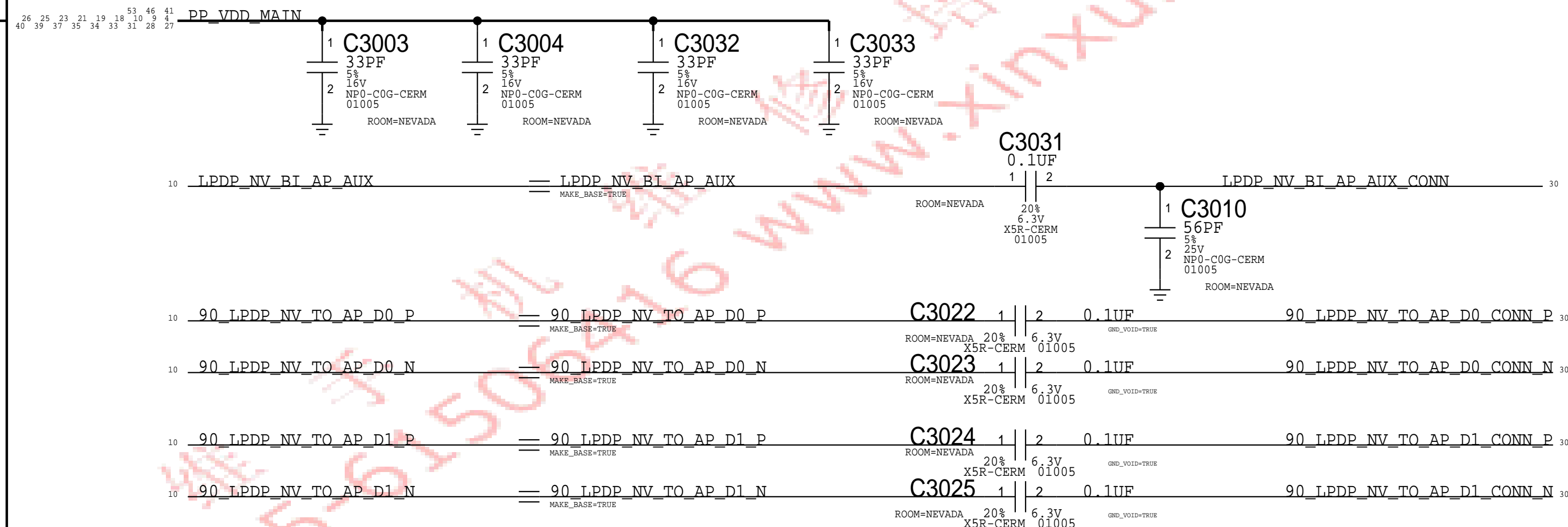
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LPDP FILTERS



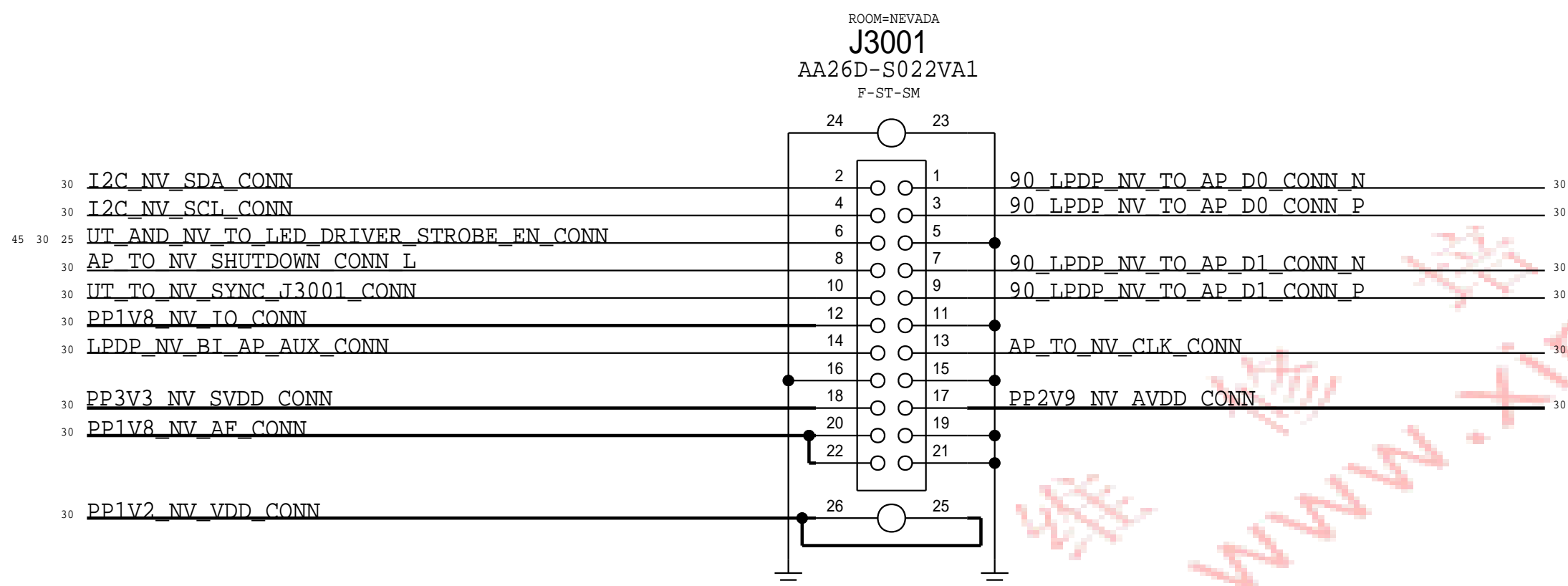
NEVADA CONNECTOR

THIS ONE ---> 516S00152 RCPT (USED ON MLB)
516S00151 PLUG

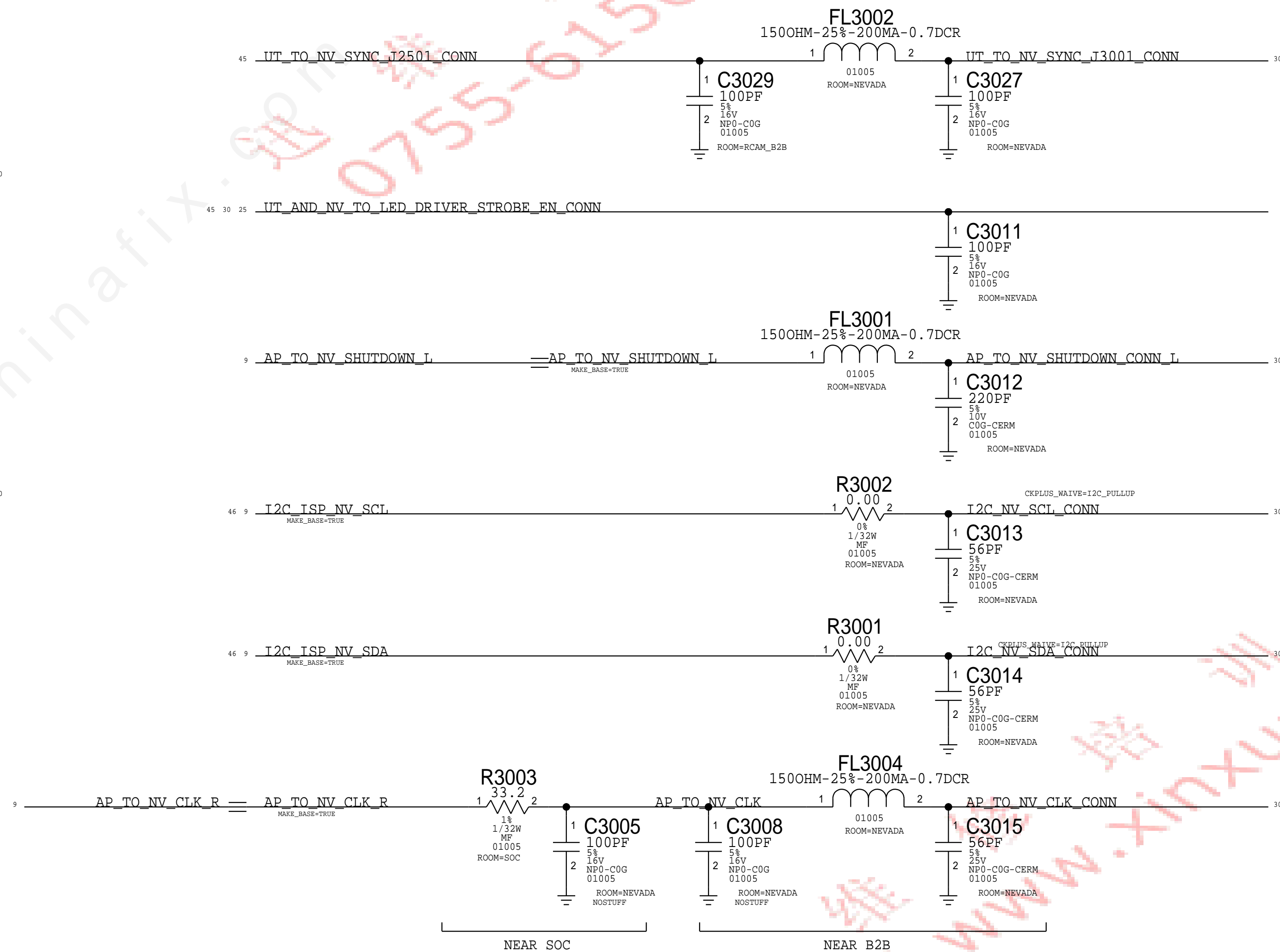
THIS PAGE UNIQUE TO LARGE FORM FACTOR

OTHER FORM FACTOR SPECIFIC PAGES:

4 - Mechanical
46 - FF Specific



GPIO FILTERS



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B2B:NEVADA		
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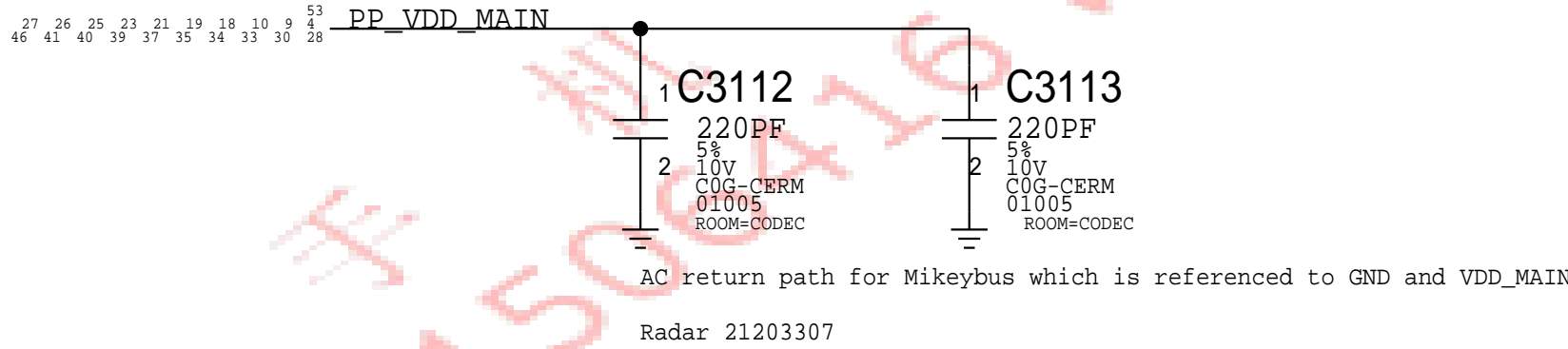
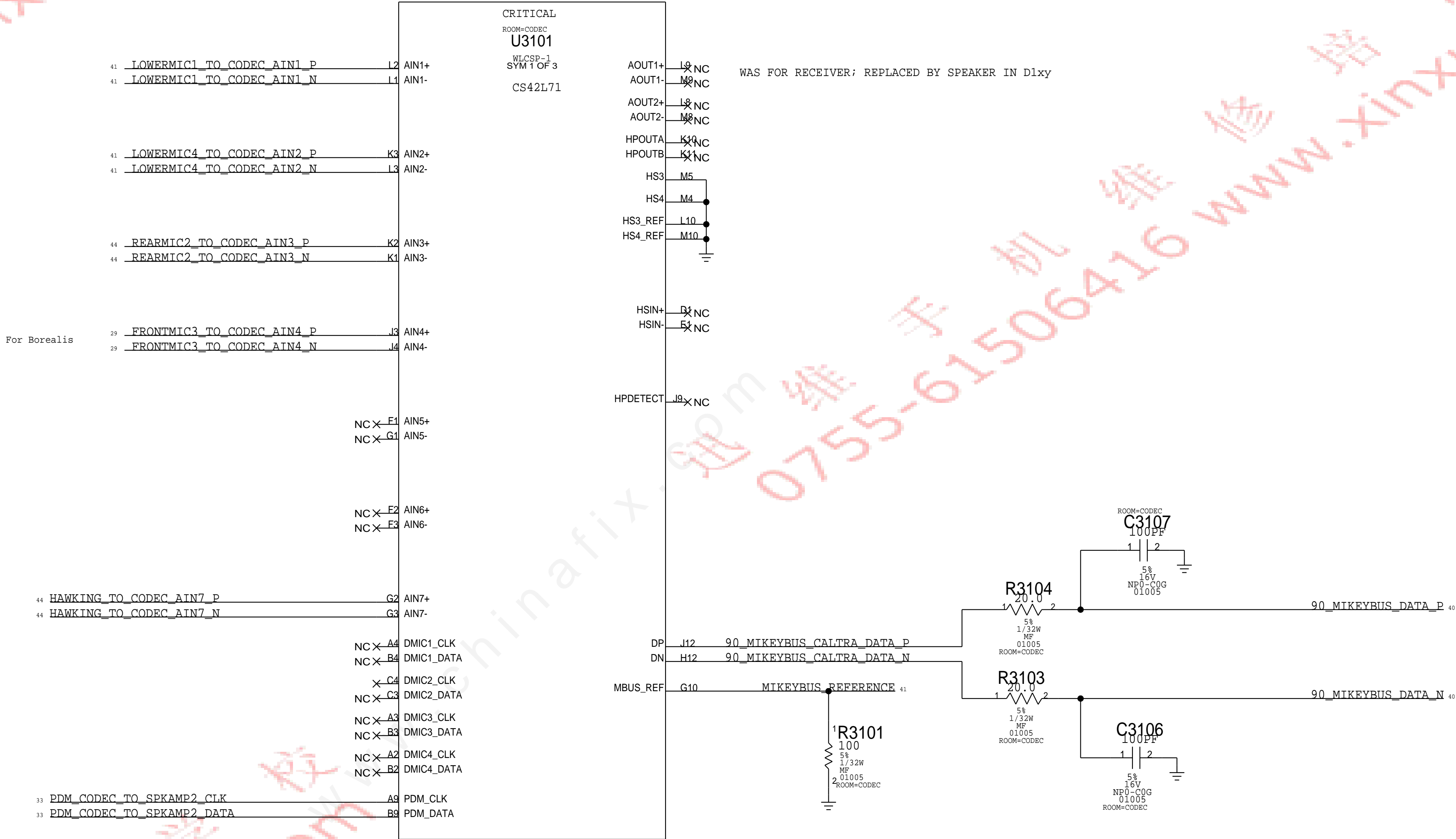
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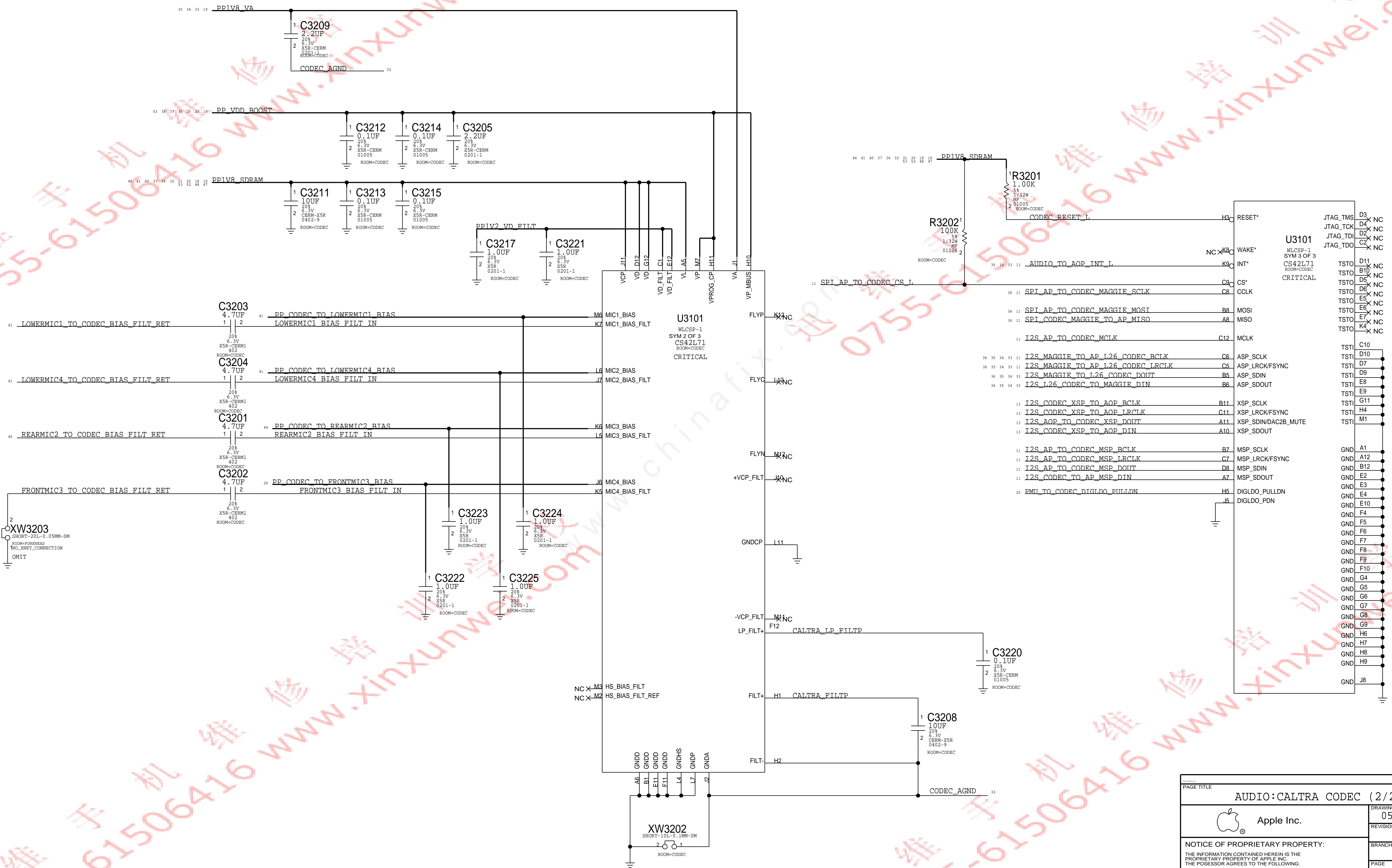
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CALTRA AUDIO CODEC (ANALOG INPUTS & OUTPUTS)



PAGE TITLE		
AUDIO:CALTRA CODEC (1/2)		
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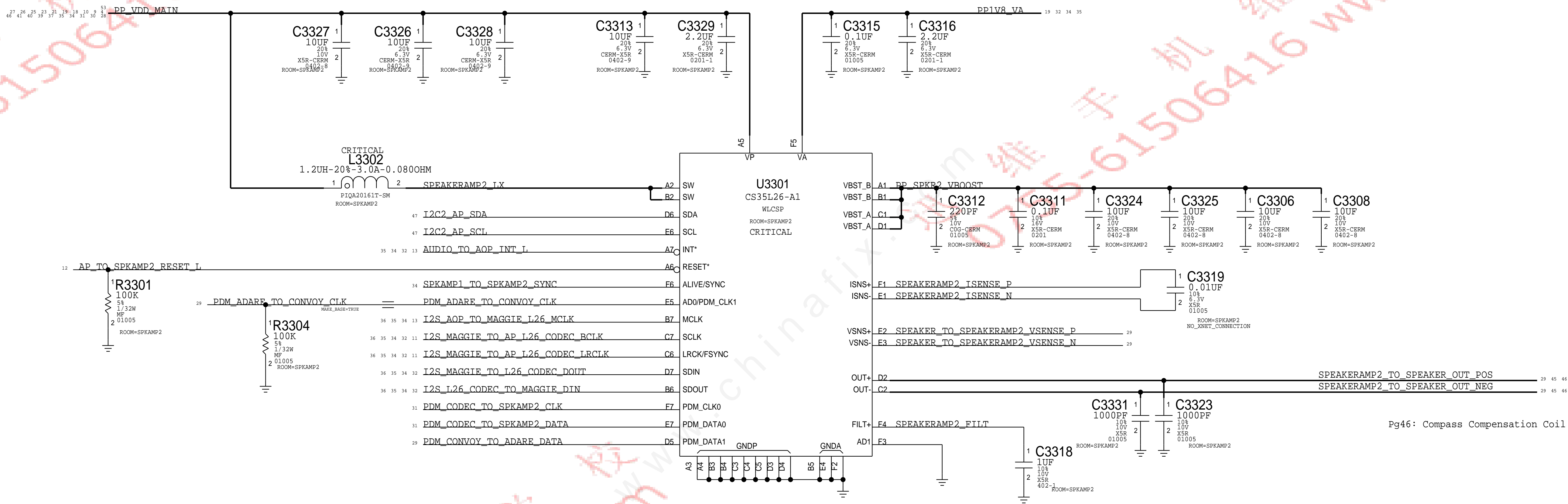
CALTRA AUDIO CODEC (POWER & I/O)




PAGE TITLE			
AUDIO:CALTRA CODEC (2/2)			
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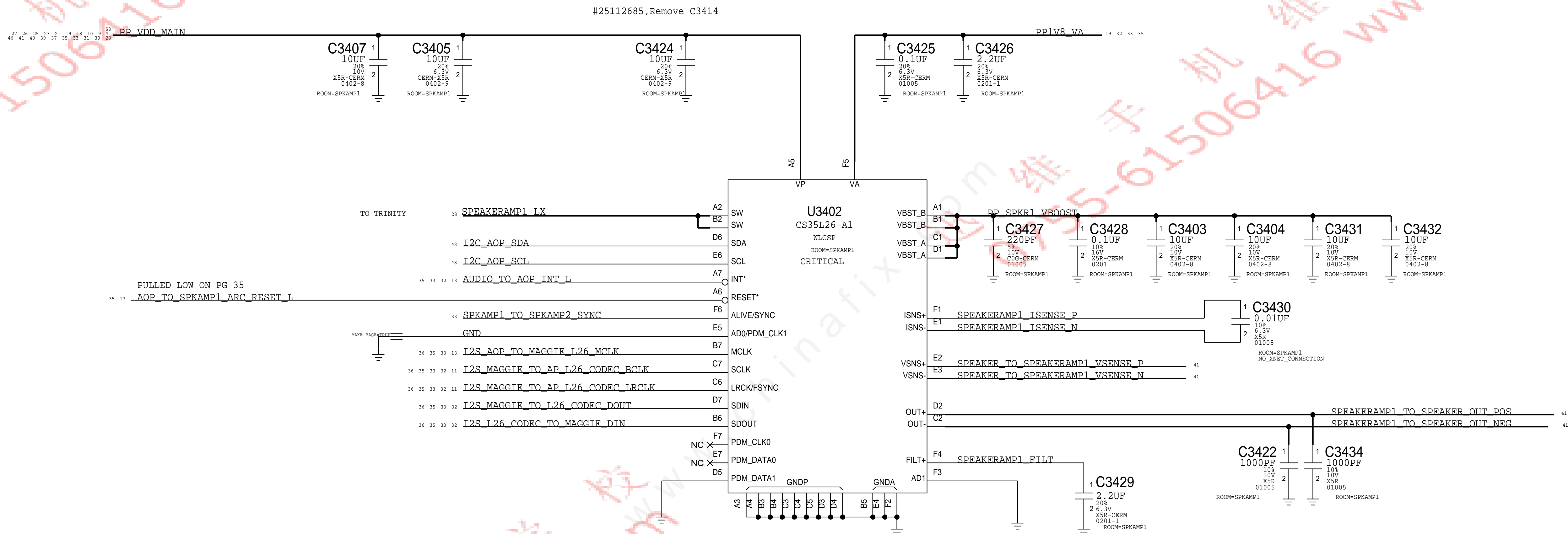
SPEAKER AMPLIFIER 2 (North)




PAGE TITLE		
AUDIO:SPEAKER AMP 2		
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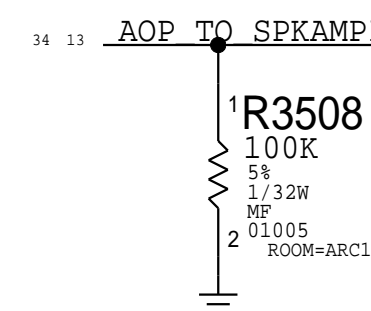



SPEAKER AMPLIFIER 1 (South)



PAGE TITLE		
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ARC D



<small>Page Number:</small>			
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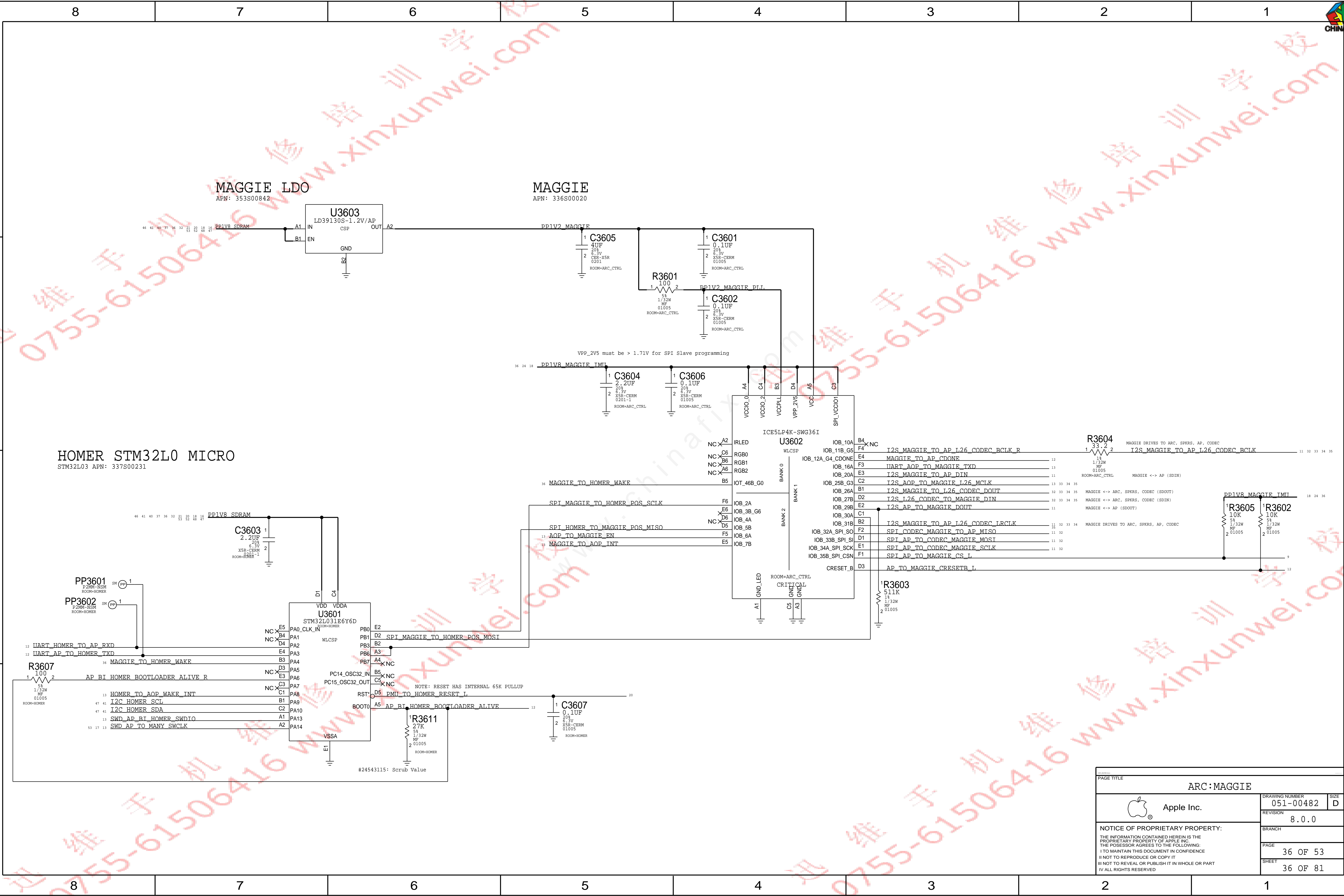
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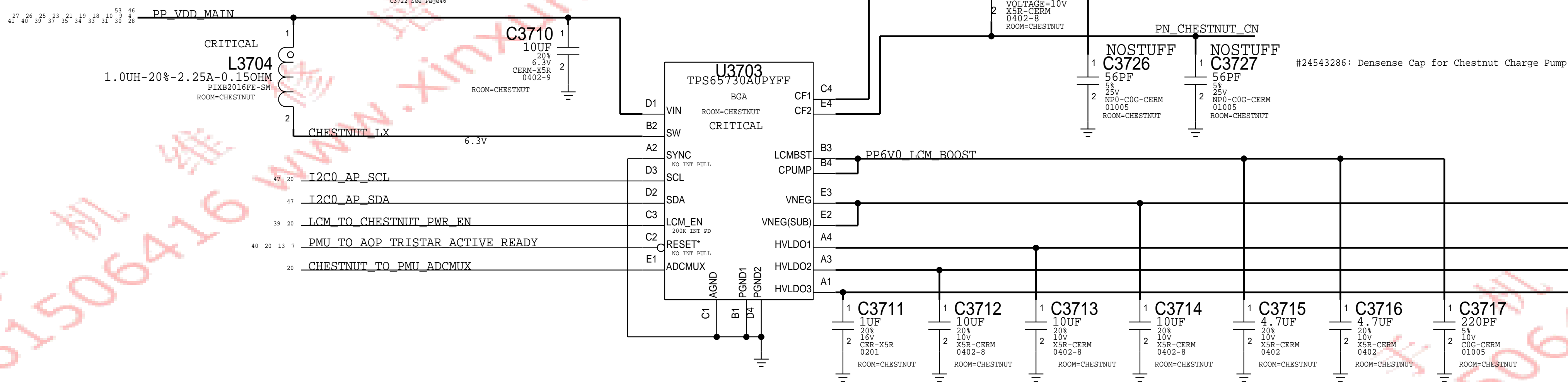
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	REVISION	8.0.0	D
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DISPLAY & TOUCH - POWER SUPPLIES

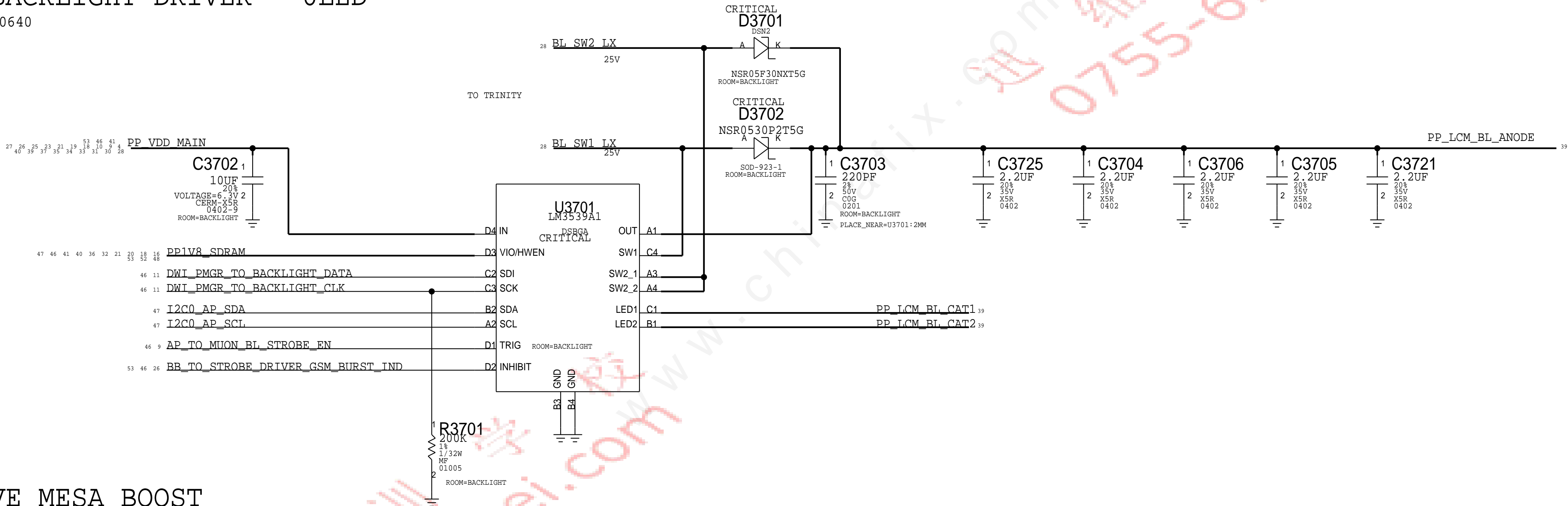
CHESTNUT DISPLAY PMU

APN:338S1172



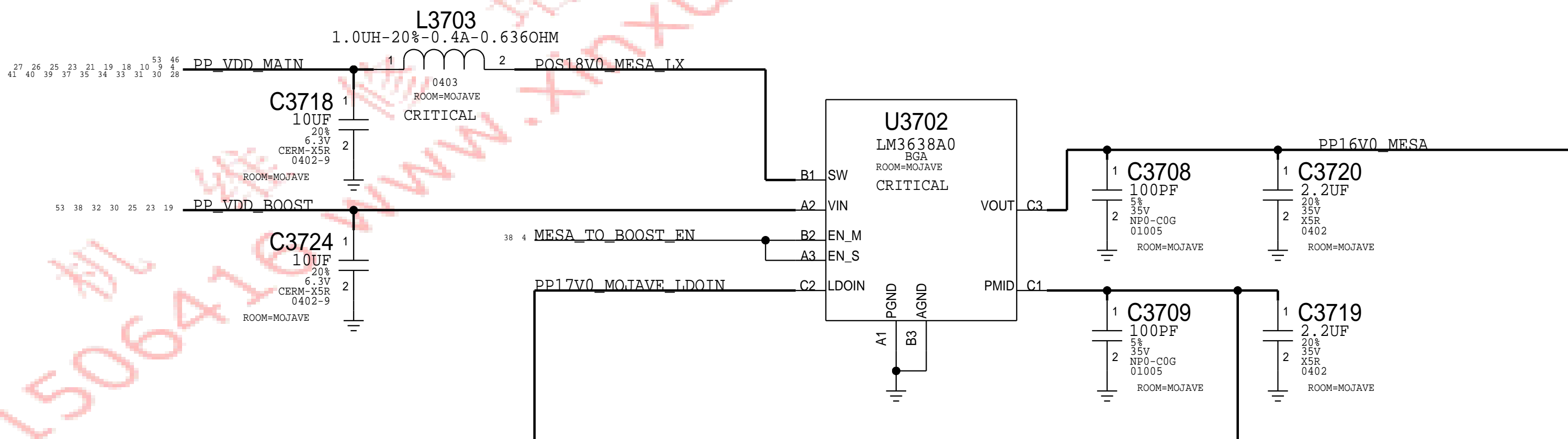
LED BACKLIGHT DRIVER - 6LED

APN:353s00640



MOJAVE MESA BOOST

APN:353S00671



PAGE TITLE		
DISPLAY & MESA:POWER		
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MESA POWER

MAMBA AND MESA CONNECTOR

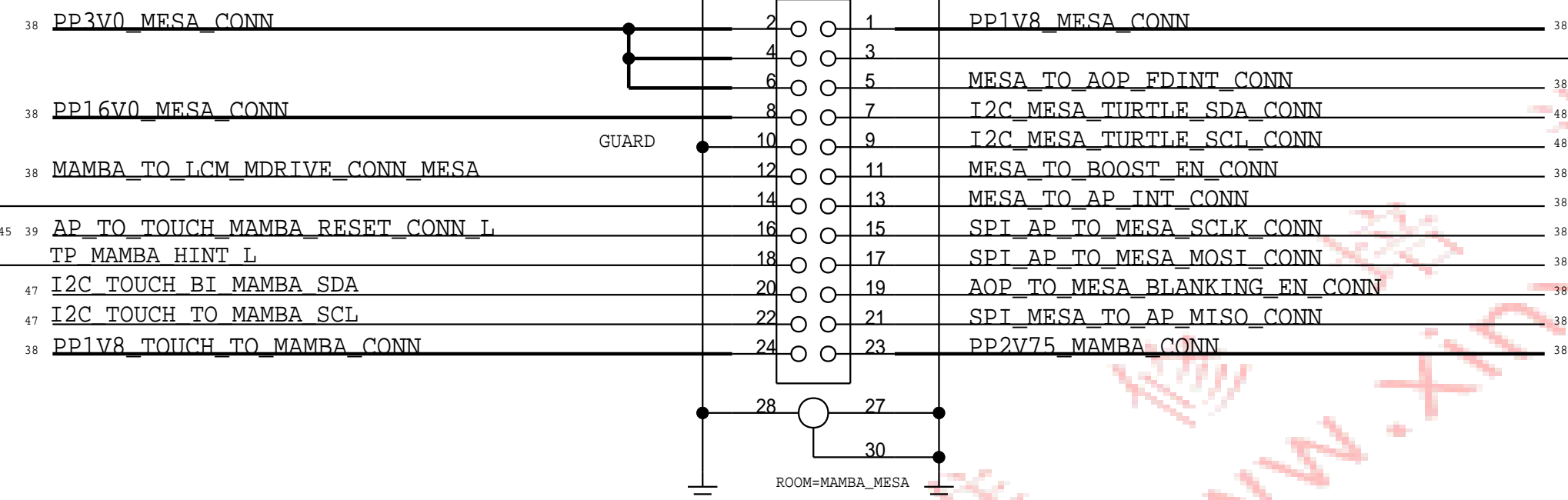
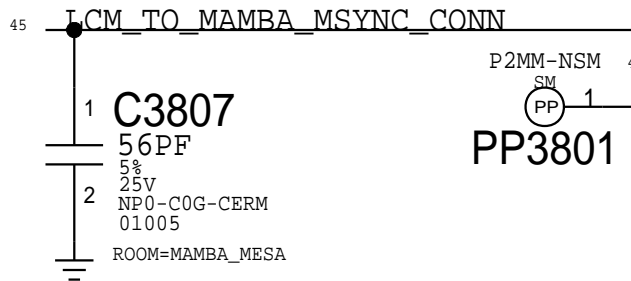
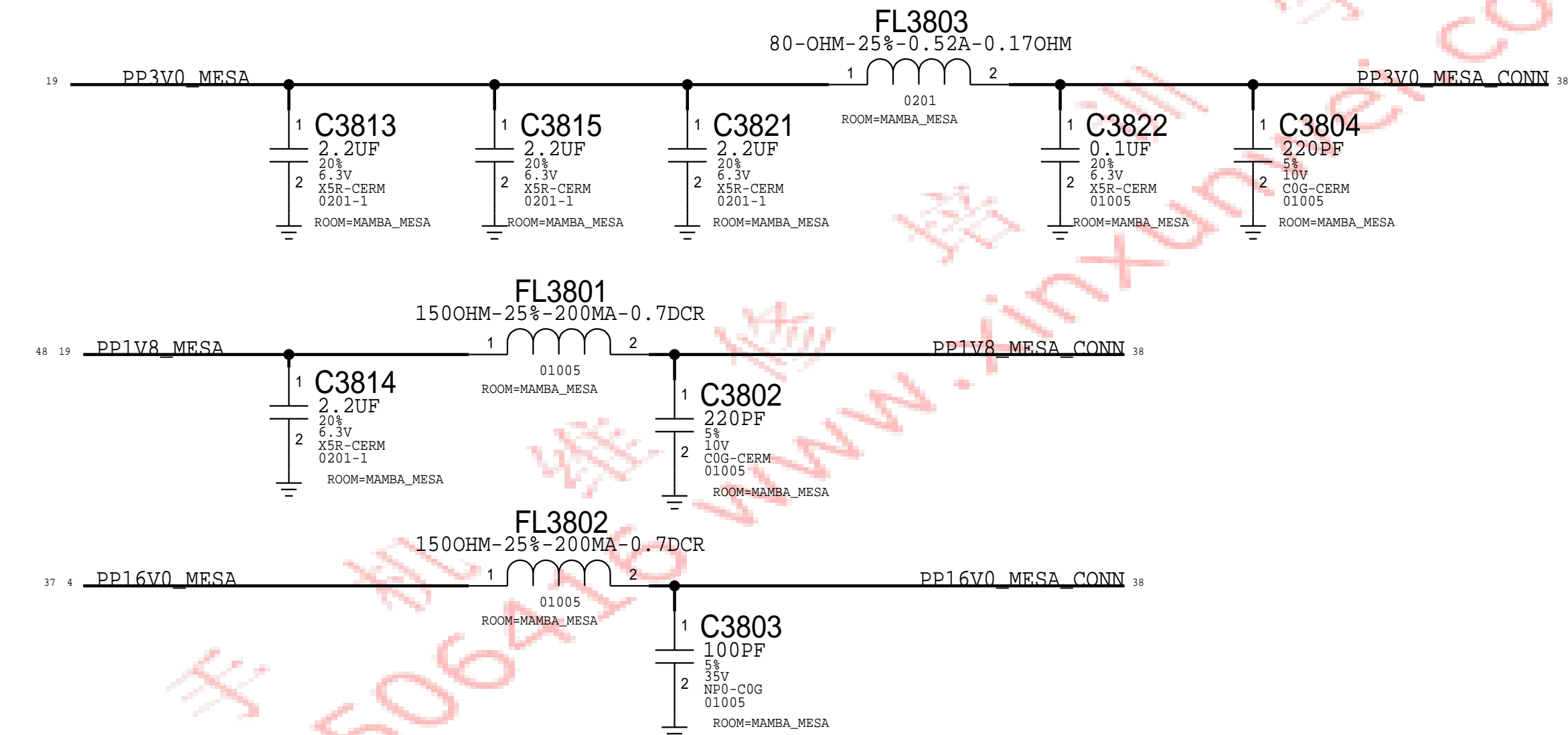
MLB: 516S00141 (RCPT)
FLEX: 516S00142 (PLUG)

CRITICAL
J3801

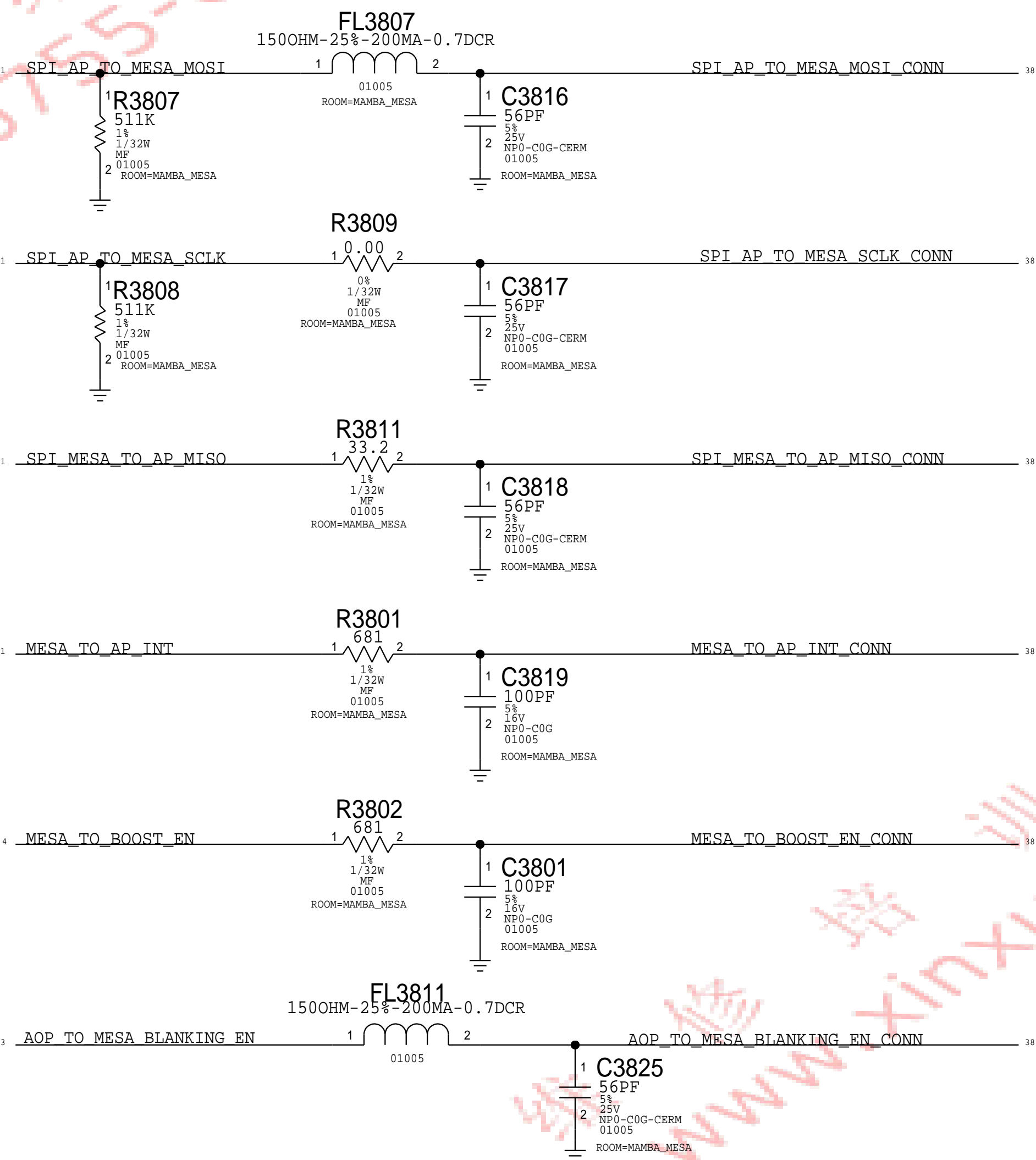
BB35C-RA24-3A

F-ST-SM

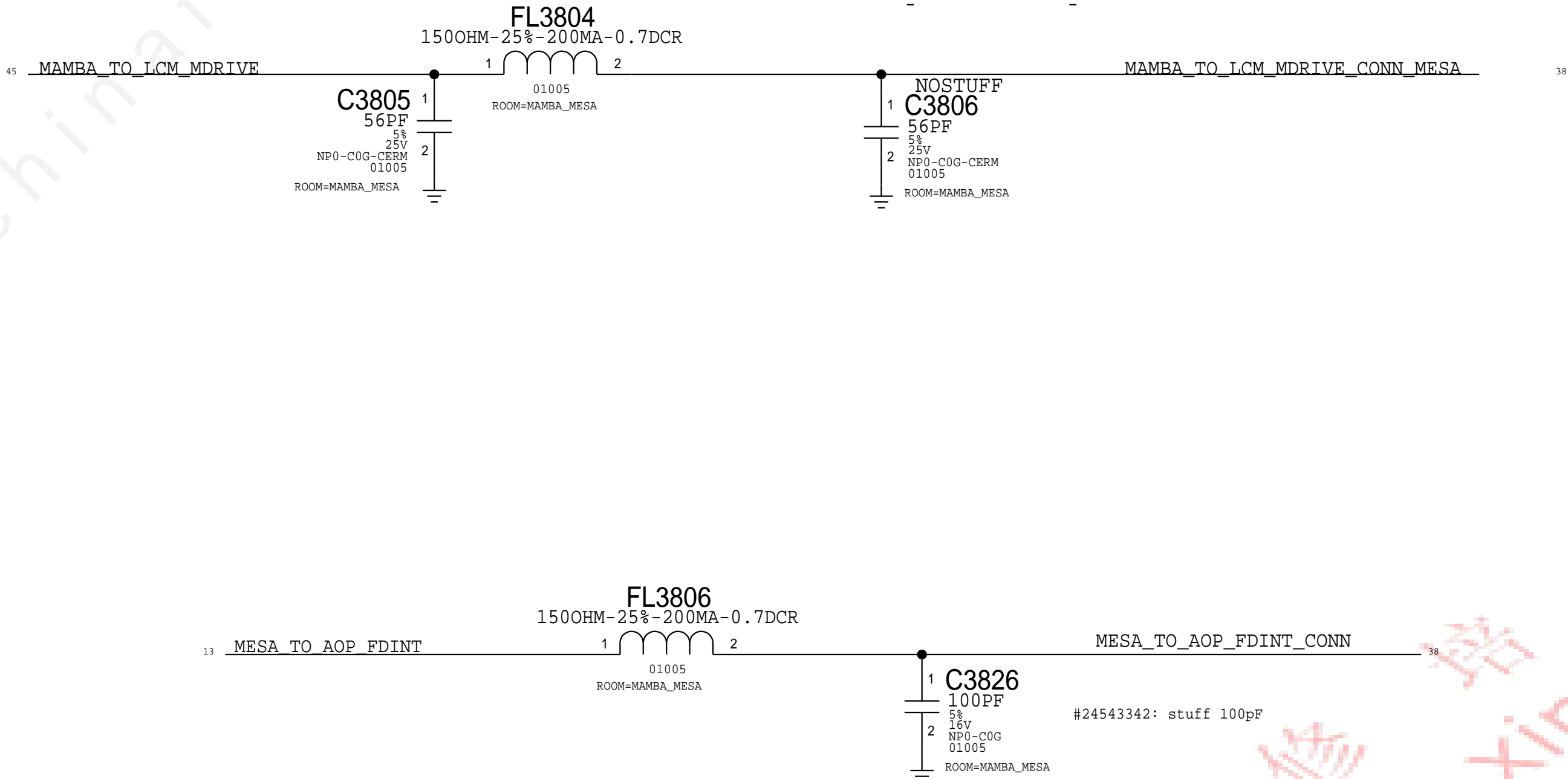
Matches flex_x452_acf, schematic revision 1.5.0 pinout



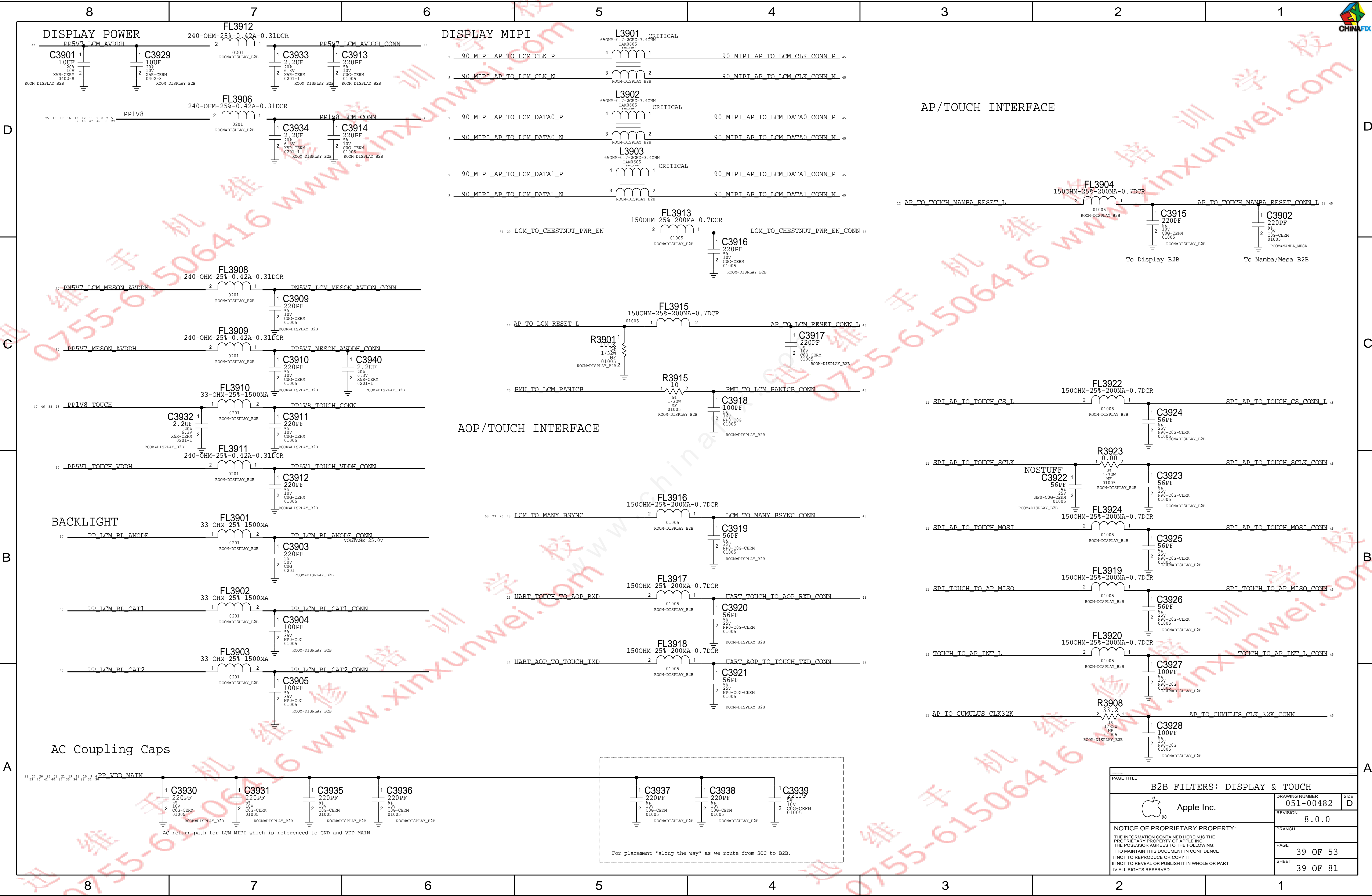
MESA DIGITAL I/O



MAMBA DIGITAL I/O



PAGE TITLE			
B2B:ORB & MESA			
	DRAWING NUMBER	051-00482	SIZE D
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D

D

C

C

B

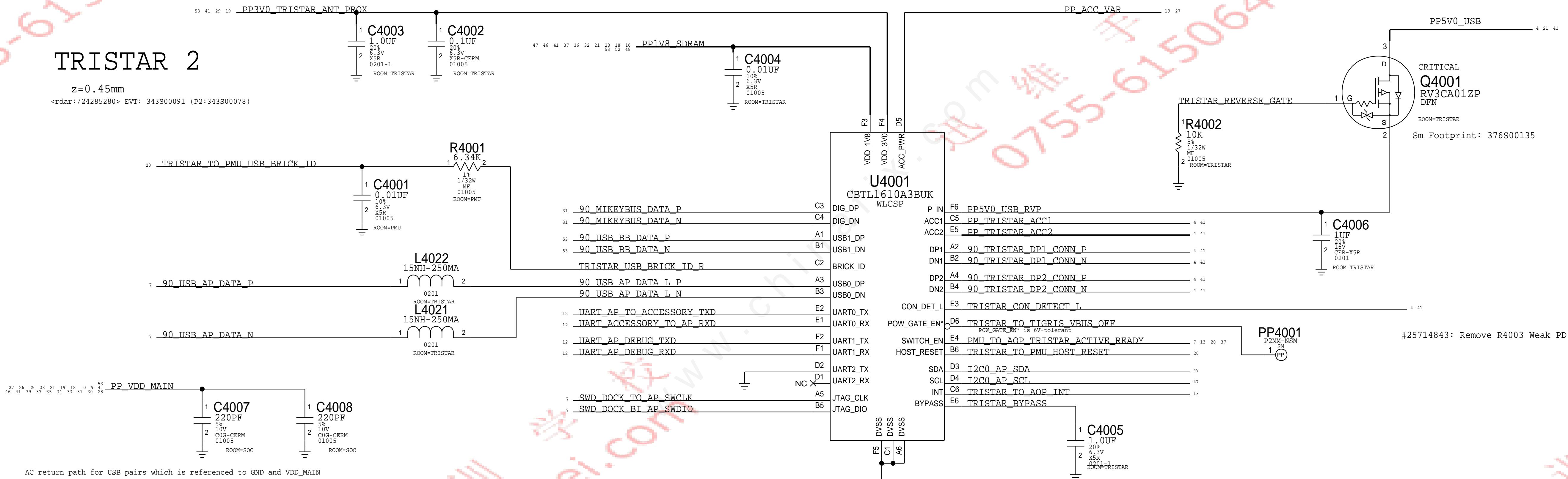
B


A

A

TRISTAR 2

z=0.45mm
<rdar:24285280> EVT: 343S00091 (P2:343S00078)



PAGE TITLE			
TRISTAR 2			
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ANTENNA

Please loop in Matt Mow (Antenna Team)
when changing these components!

ARC CONTROL

#26118161: Update FL4104 to 49.9ohm

ARC1

Antenna GPIO

TRISTAR


LOWER MIC1/4

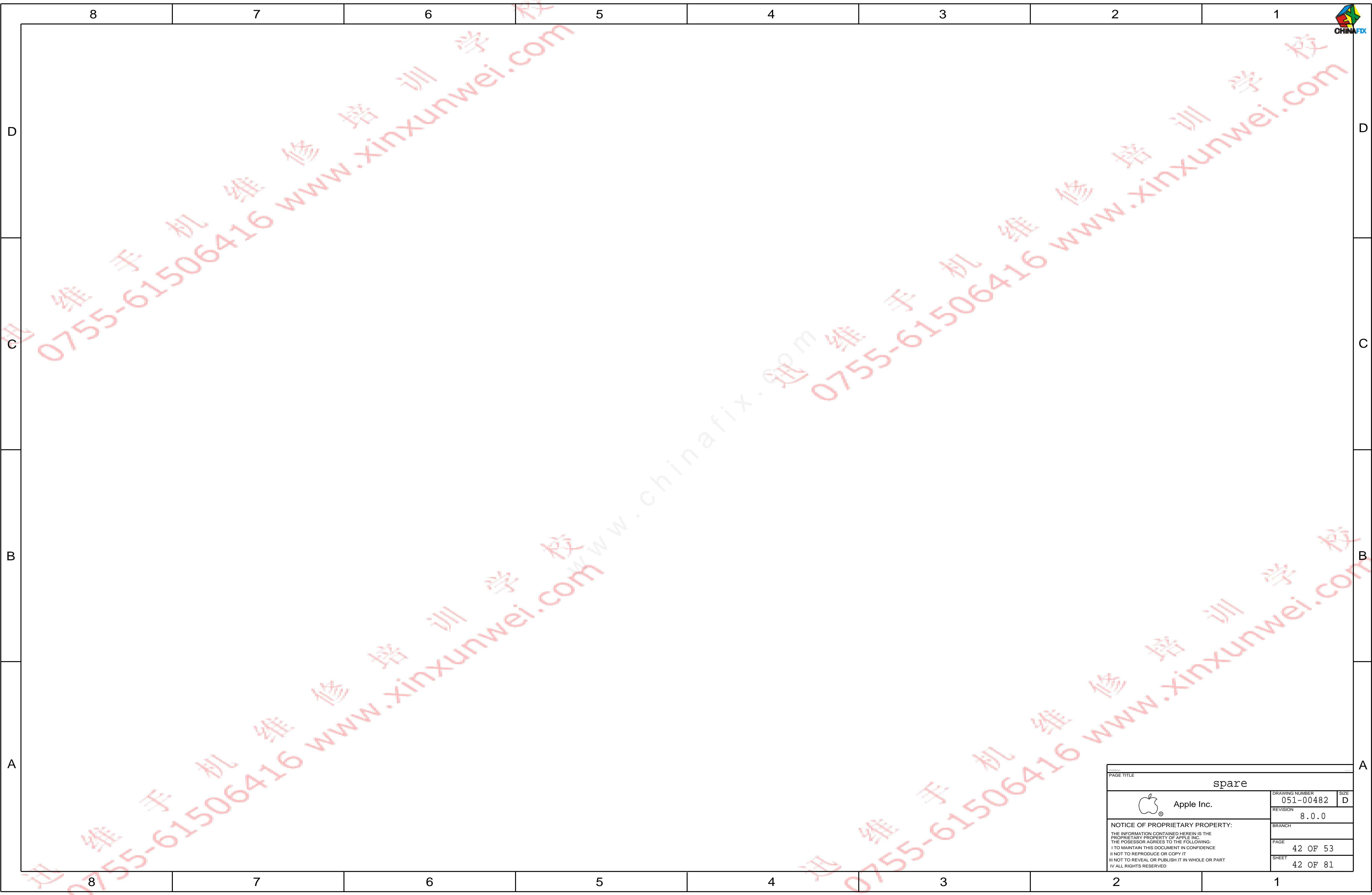
SPEAKER1

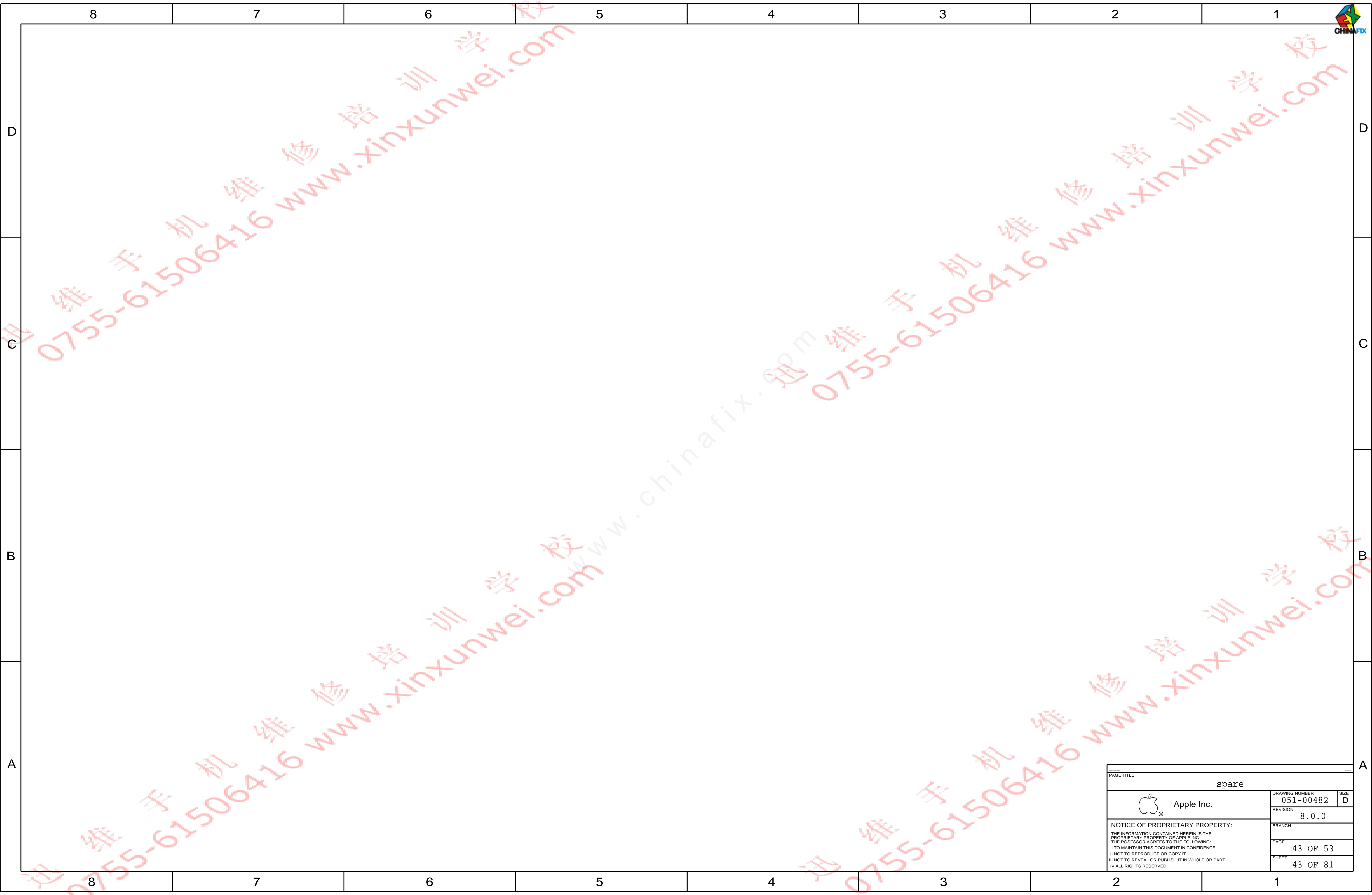
USB AC Coupling

AC return path for USB pairs which is referenced to GND and VDD_MAIN

DOCK FLEX CONNECTOR

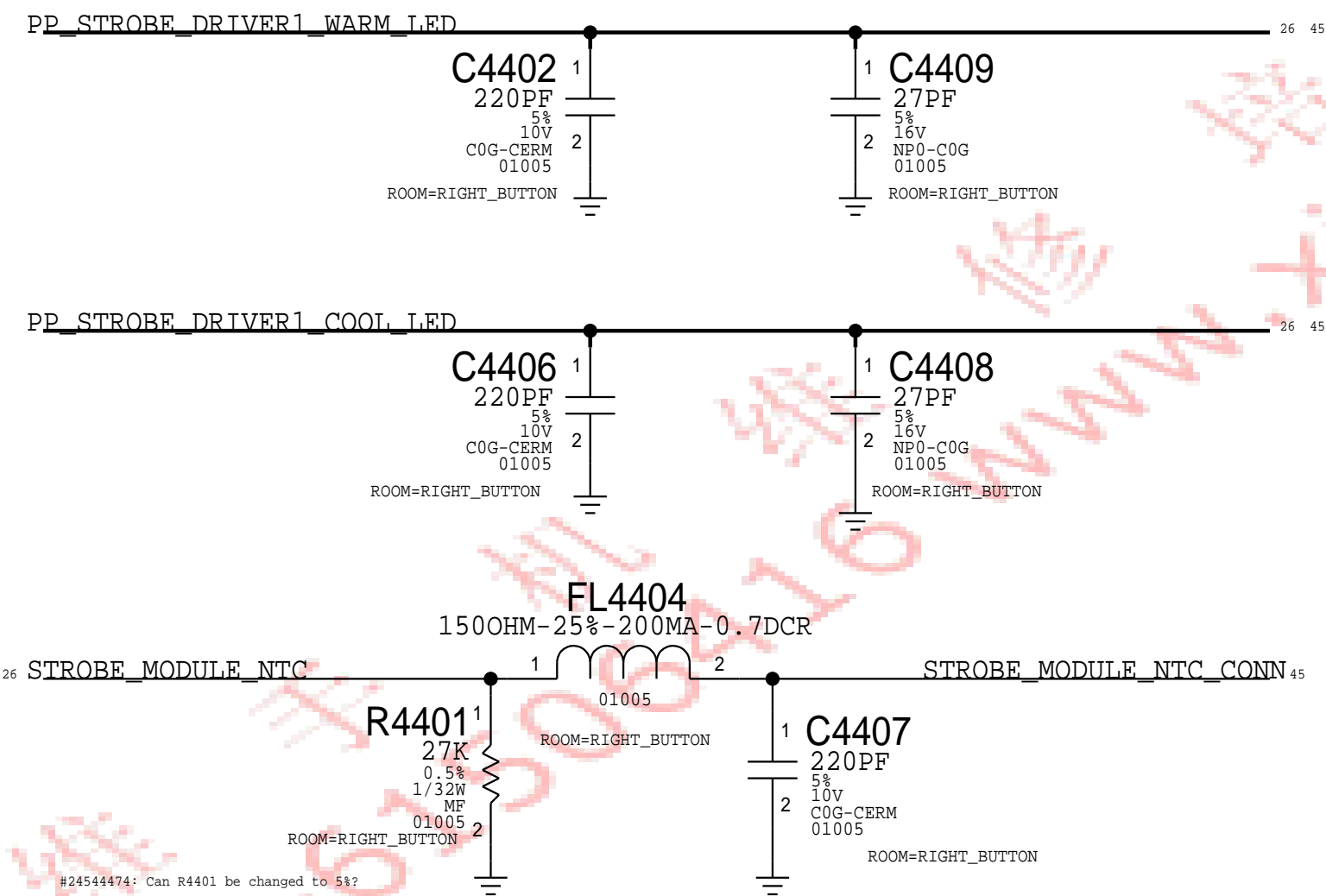
PAGE TITLE			DRAWING NUMBER		SIZE
B2B:DOCK FLEX			051-00482		D
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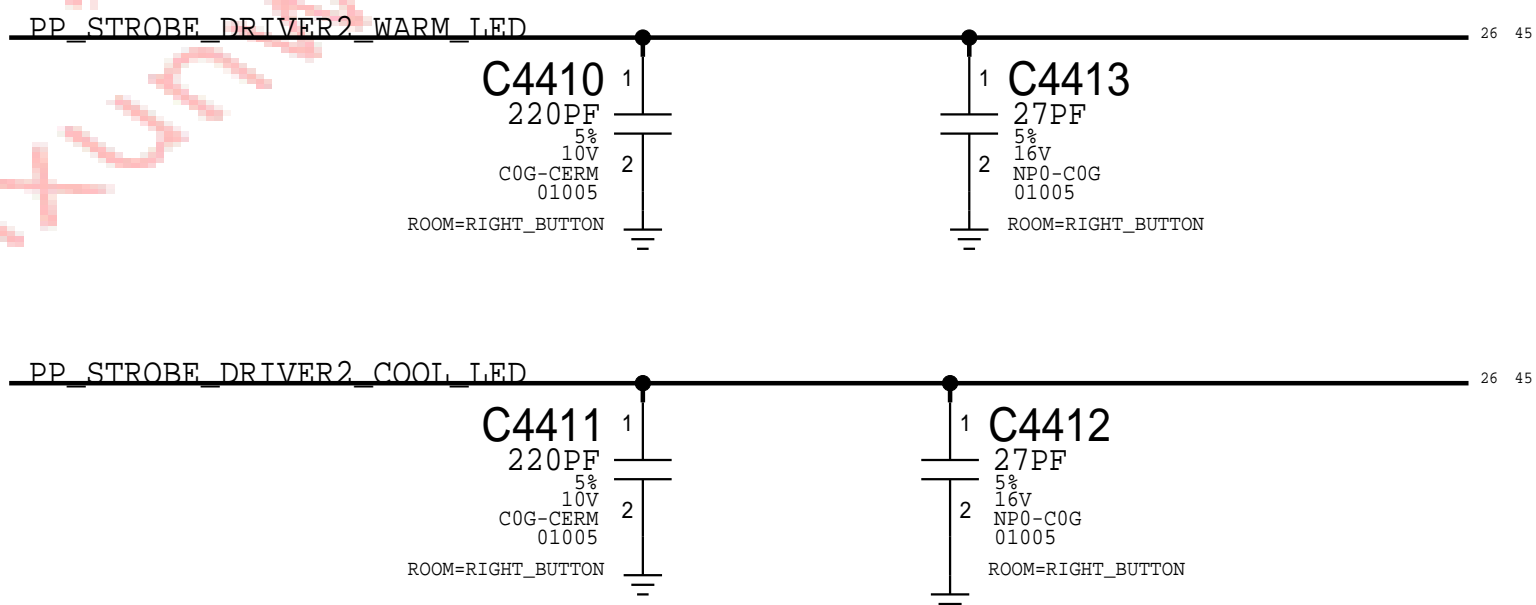




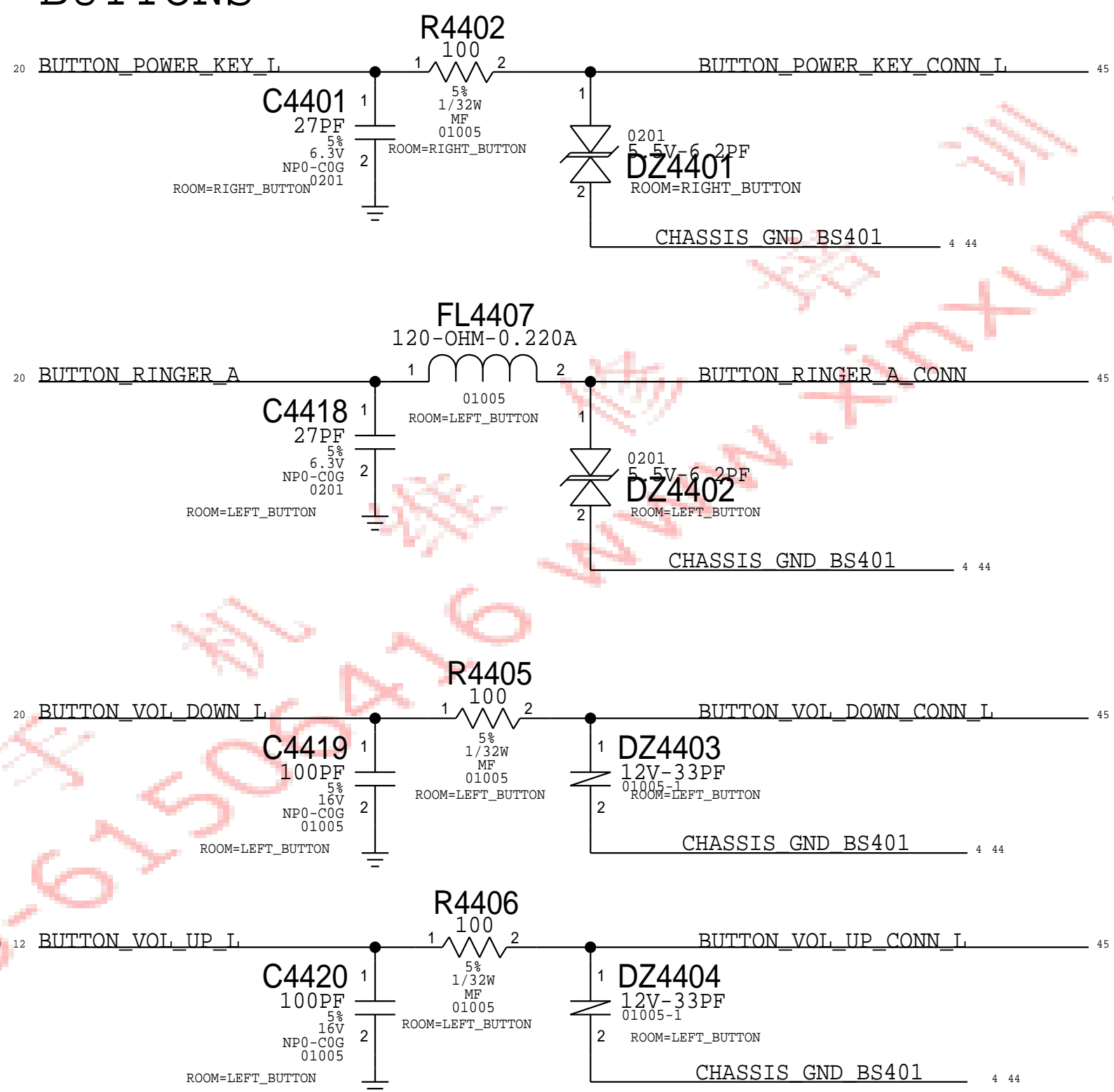
STROBE1



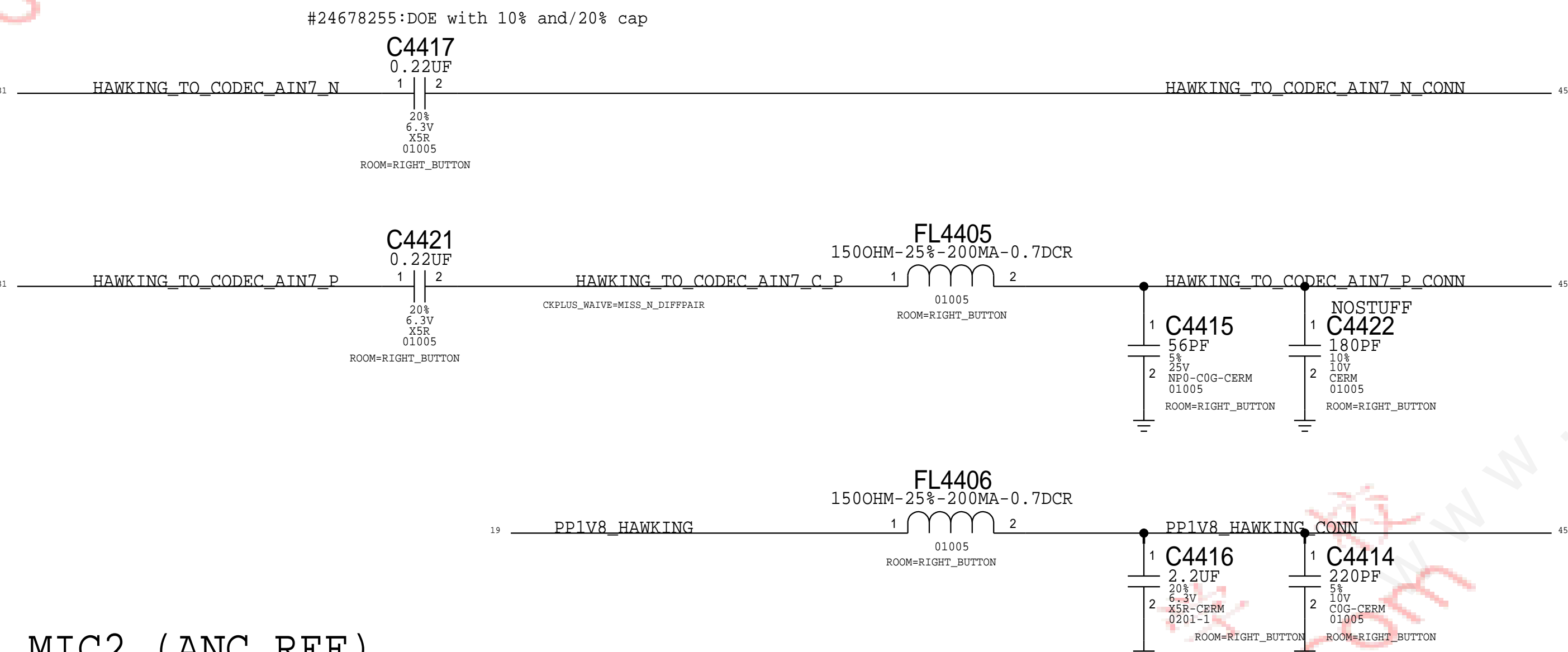
STROBE2



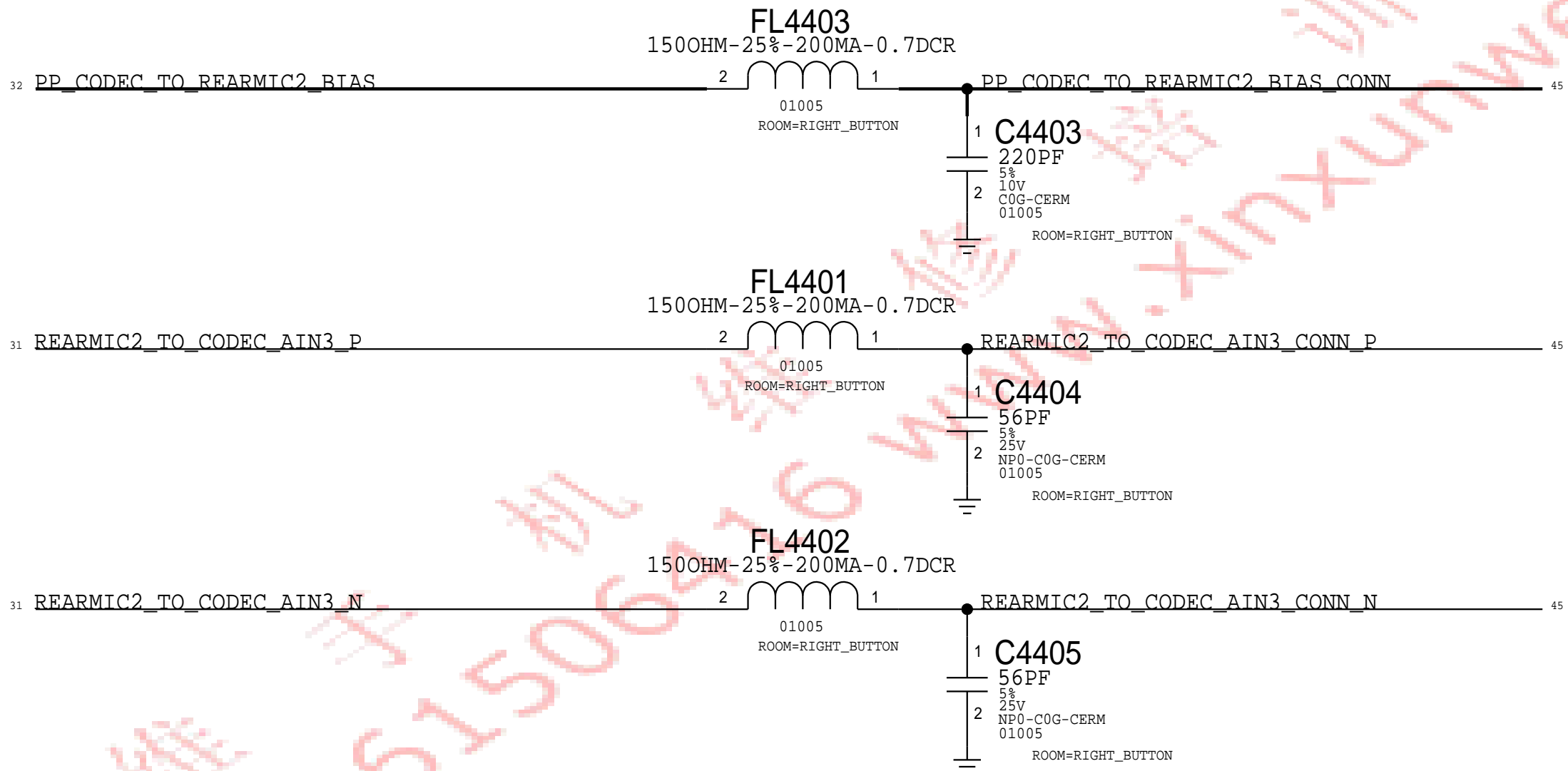
BUTTONS




HAWKING



MIC2 (ANC REF)



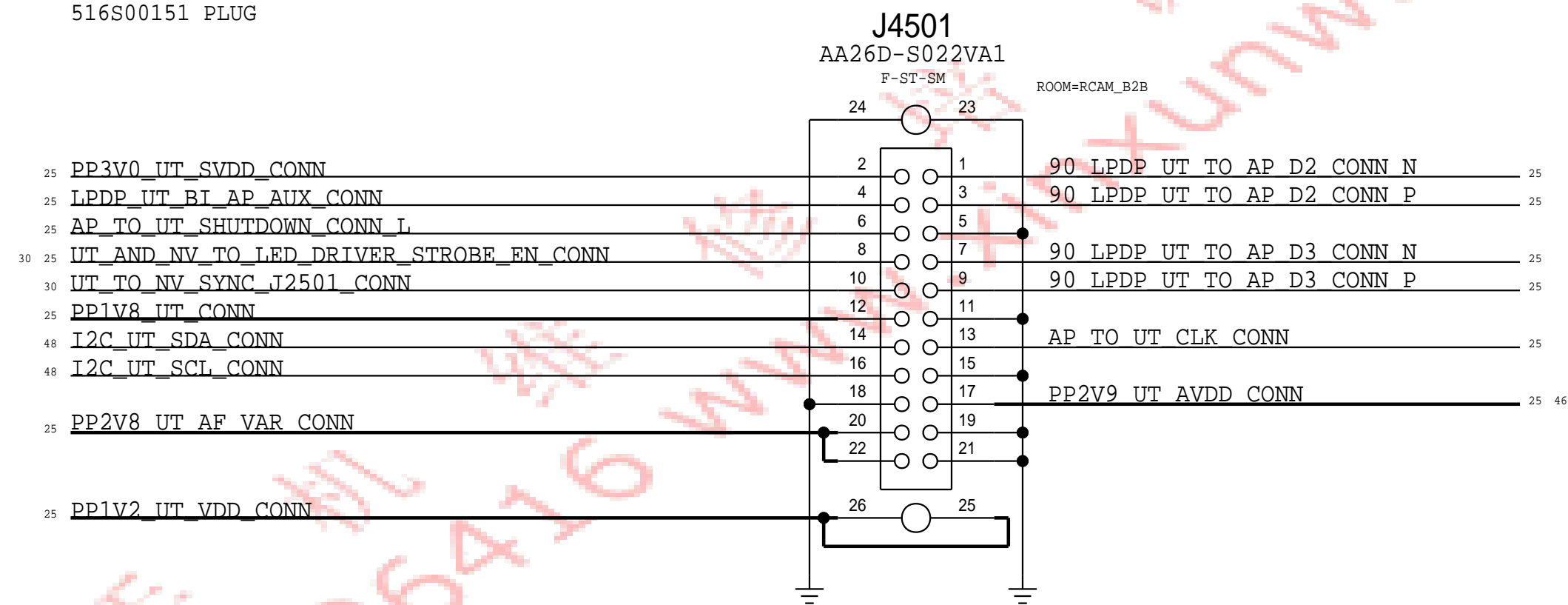
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UTAH-D FLEX CONNECTOR

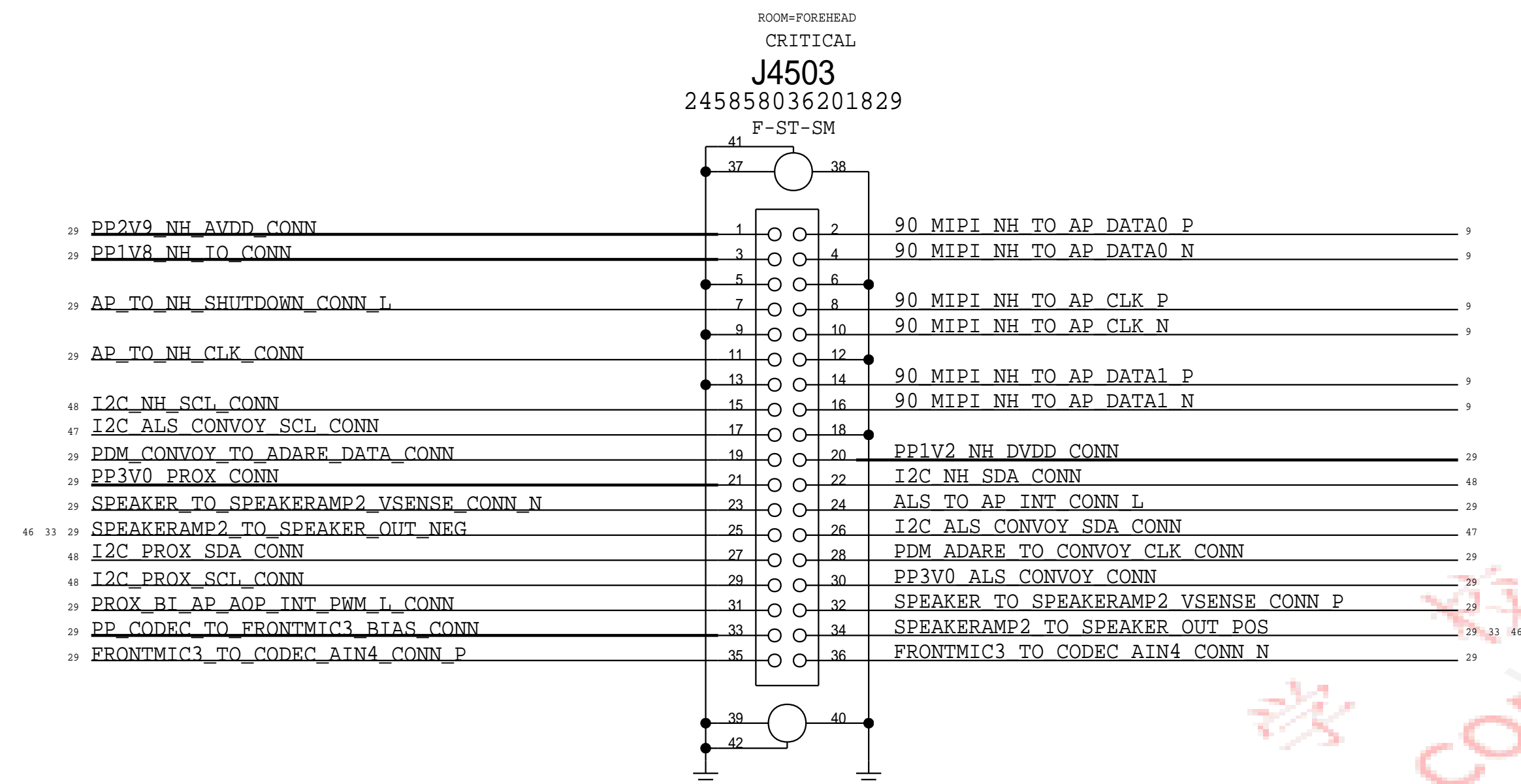
516S00152 RCPT (USED ON MLB)
516S00151 PLUG

<-- THIS ONE



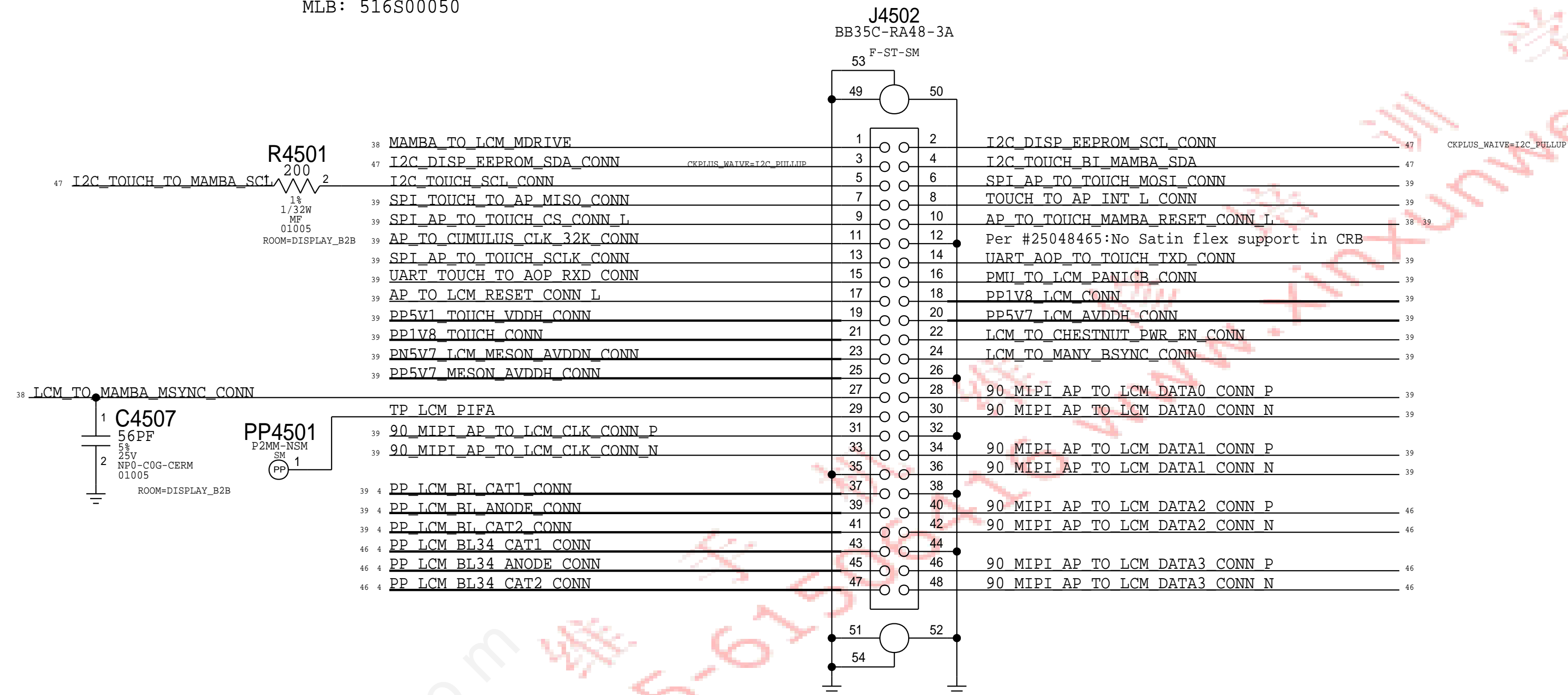
FOREHEAD FLEX CONNECTOR

MLB: 516S00146 (MCO REV 27)



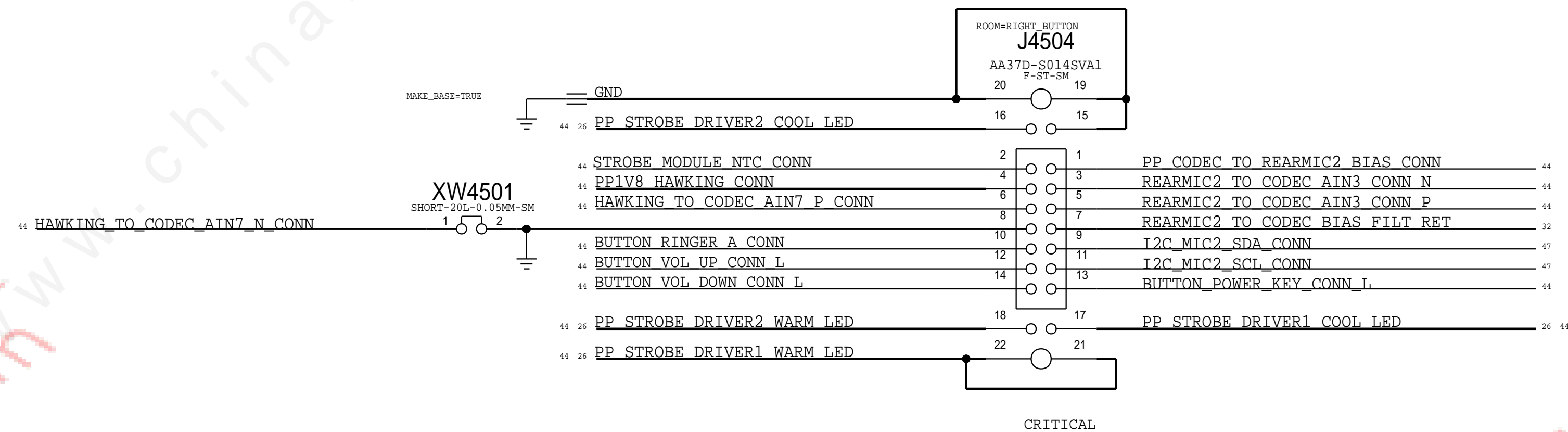
DISPLAY / TOUCH FLEX CONNECTOR


MLB: 516S00050



COMBO BUTTON FLEX CONNECTOR

APPLE APN: 516S00150 <-- THIS ONE ON MLB (matched MCO rev 27)
APPLE APN: 516S00149

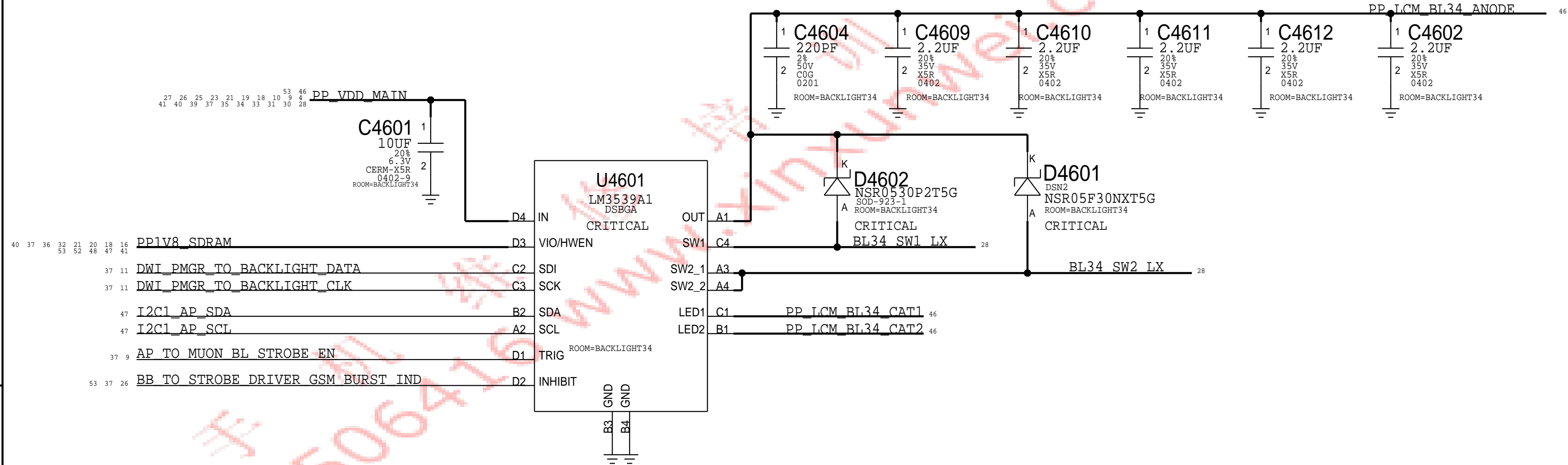


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B2B FF SPECIFIC			
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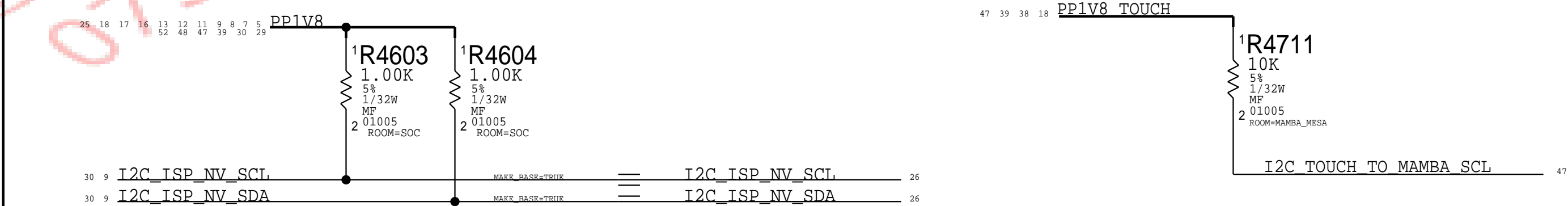
THIS PAGE UNIQUE TO LARGE FORM FACTOR

2ND BACKLIGHT DRIVER - 4LED



ISP I2C1

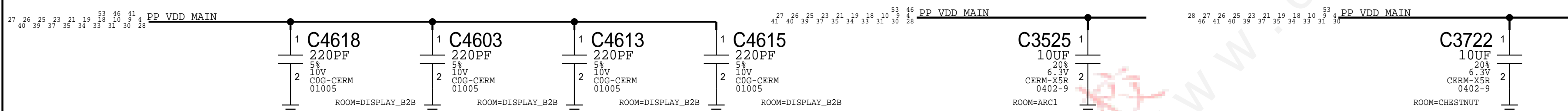
TOUCH I2C



VDD MAIN CAP
AC Coupling Caps

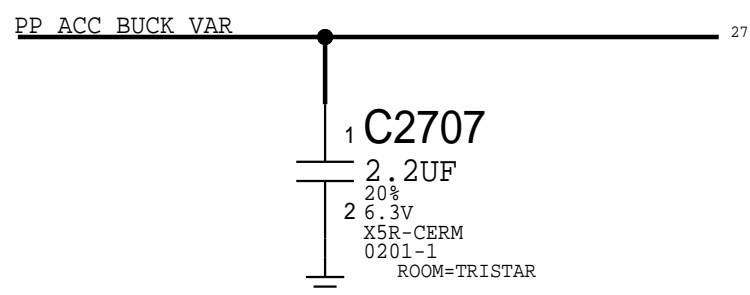
ARC CAP

CHESTNUT CAP

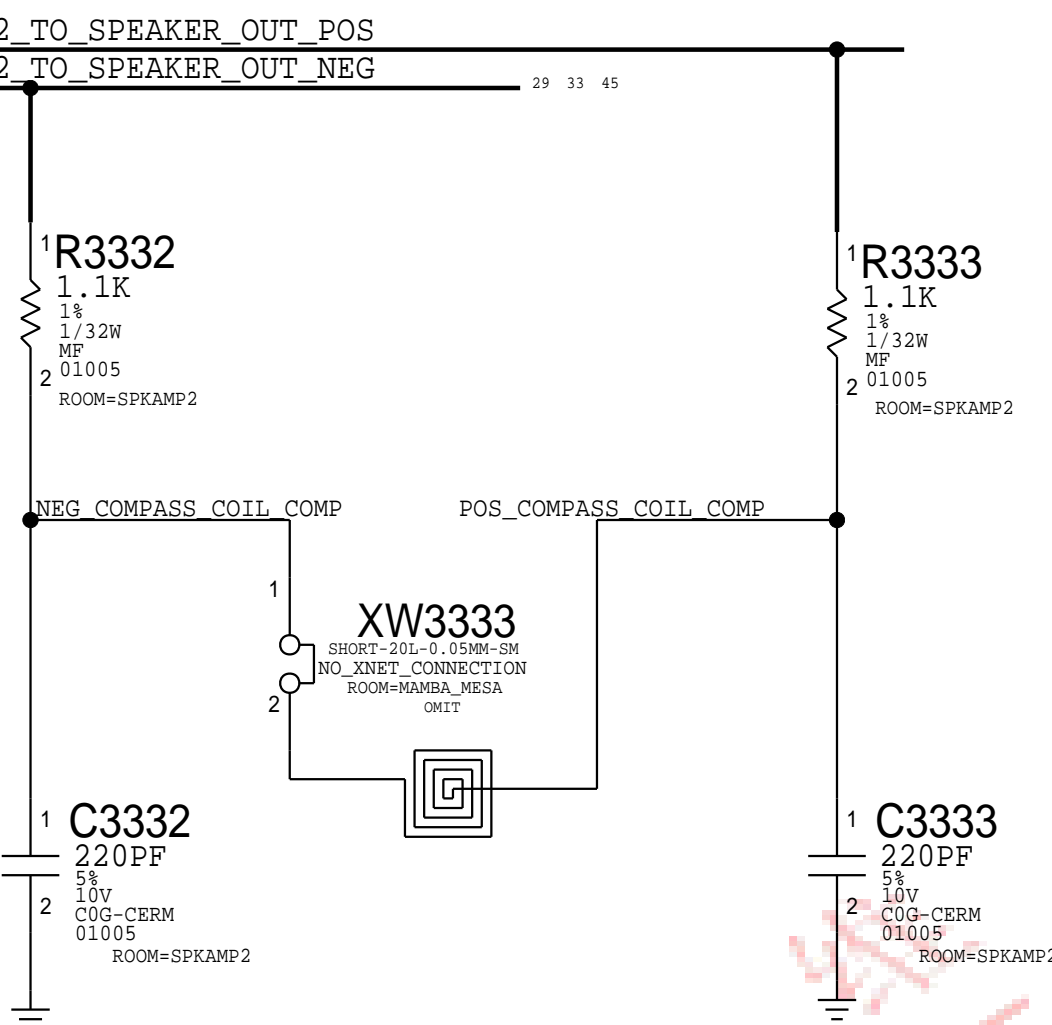


AC return path for LCM MIPI which is referenced to GND and VDD_MAIN

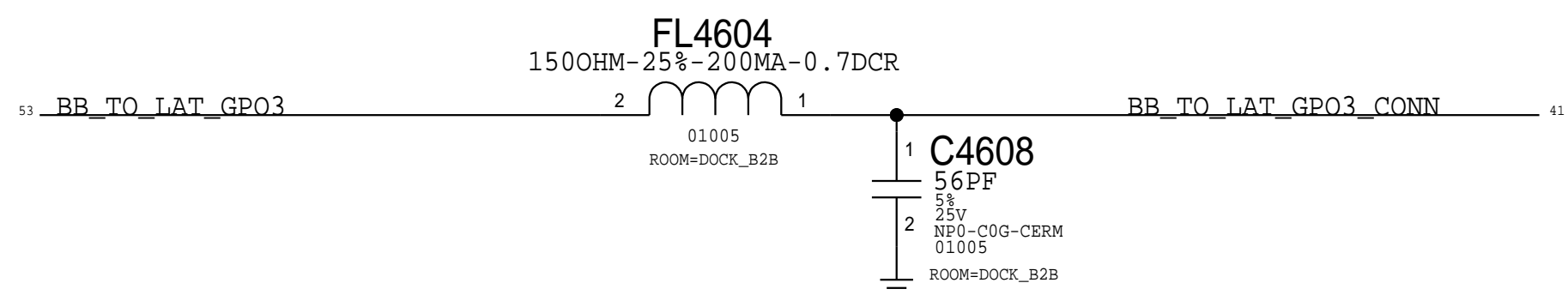
ACC Buck Caps



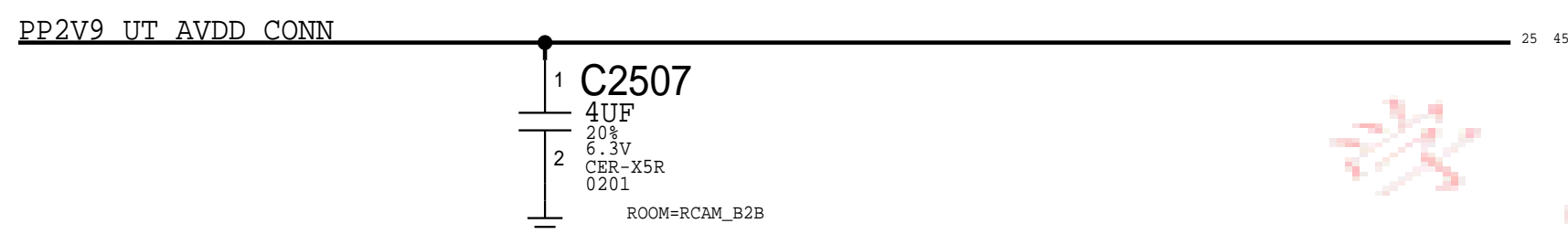
Top Speaker Compass Coil



Dock B2B (Pg 41)



UT B2B

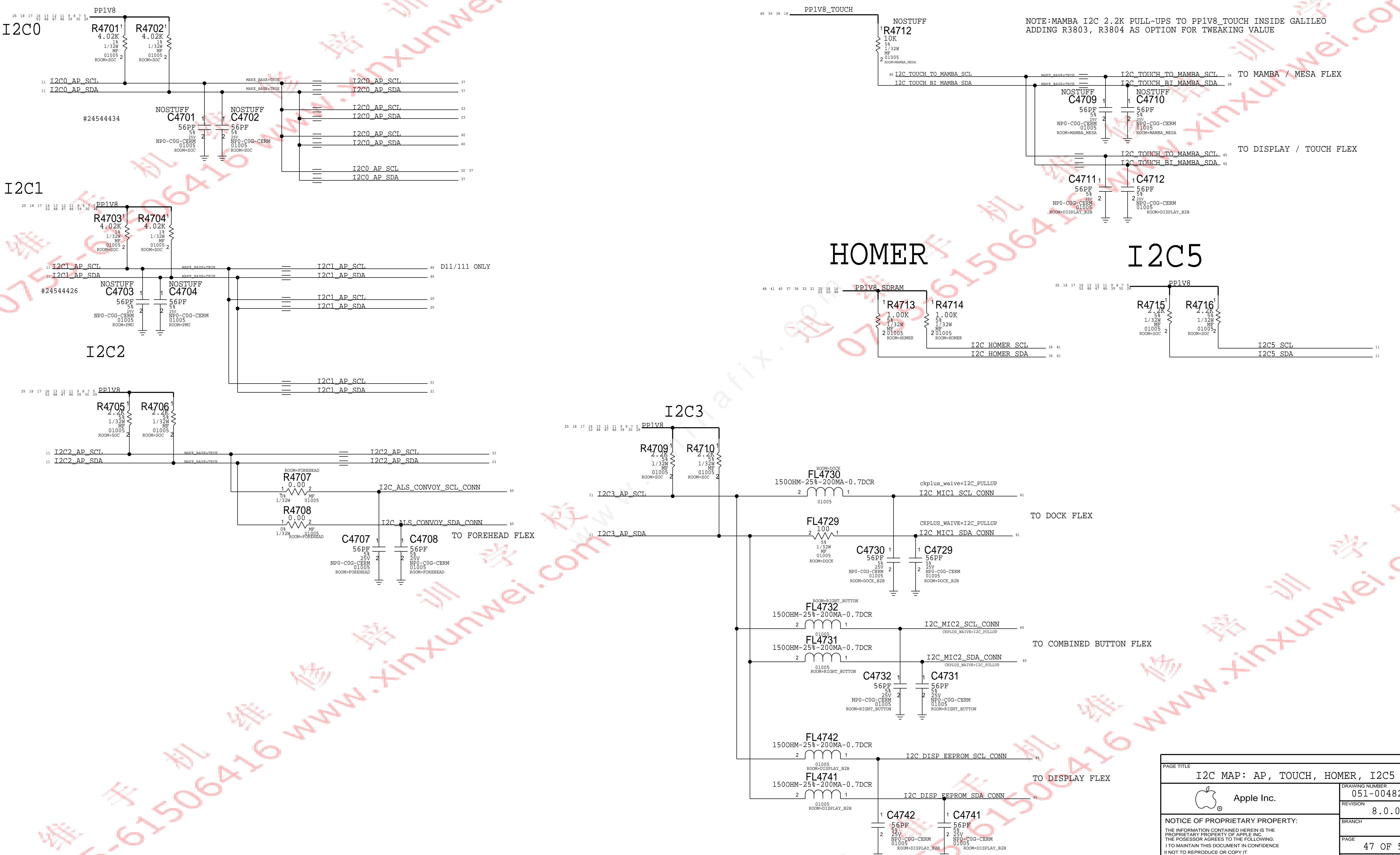


Extra RF GND

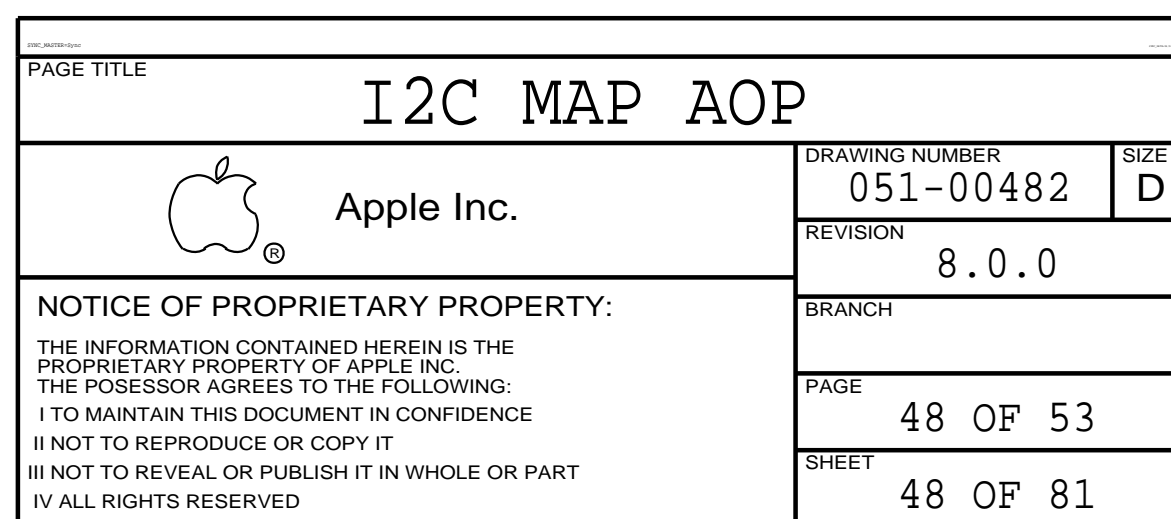
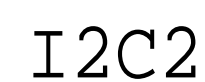
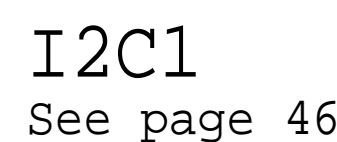
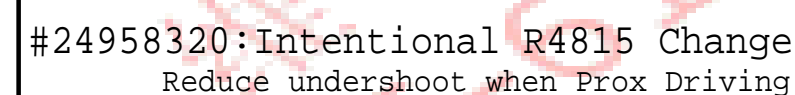


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AP TOUCH




PAGE TITLE		
I2C MAP: AP, TOUCH, HOMER, I2C5		
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	8	7	6	5	4	3	2	1	<div>CHINAFOX</div>
D									
C									
B									
A									
	8	7	6	5	4	3	2	1	

D1x I2C Table

Bus	Device	Binary	7-bit Address	8-bit Address	Max Speed
ISP I2C0 <i>1MHz</i>	SALT LAKE (PRIMARY)	0010000X	0x10	0x20	1 MHz
	SALT LAKE (SECONDARY)	0110000X	0x30	0x60	1 MHz
	GRUNBERG (STANDARD)	0011100X	0x1C	0x38	1 MHz
	GRUNBERG (SPECIAL ABSOLUTE R/W)	0011101X	0x1D	0x3A	1 MHz
	GRUNBERG (SPECIAL DELTA READ)	0011110X	0x1E	0x3C	1 MHz
	STROBE 1	1100011X	0x63	0xC6	1 MHz
ISP I2C1 <i>1MHz</i>	LAS VEGAS (PRIMARY)	0100000X	0x20	0x40	1 MHz
	LAS VEGAS (SECONDARY)	0110000X	0x30	0x60	1 MHz
	EDWIN	1100011X	0x63	0xC6	400 kHz
	STROBE 2	0000110X	0x06	0x0C	1 MHz
ISP I2C2 <i>400kHz</i>	CONCORD	0010000X	0x10	0x20	1 MHz
AOP I2C <i>1MHz</i>	ARC DRIVER 1	100000YX	0x41	0x82	1 MHz
	PROX	1011000X	0x58	0xB0	1 MHz
	SPKAMP1 (L26)	100000YX	0x40	0x80	1 MHz
	TURTLE	0101100X	0x2c	0x58	400 kHz
	MESA	1100001X	0x61	0xC2	400 kHz
Touch I2C <i>600kHz</i>	MAMBA	1100000X	0x60	0xC0	1 MHz
	MESON	1000000X	0x40	0x80	510 kHz
EEPROM I2C	EEPROM	1010001X	0x51	0xA2	400 kHz
AP I2C0 <i>400kHz</i>	BOOST	1110101X	0x75	0xEA	3.4 MHz
	TRISTAR	0011010X	0x1A	0x34	400 kHz
	BACKLIGHT 1	1100010X	0x62	0xC4	1 MHz
	CHESTNUT	0100111X	0x27	0x4E	400 kHz
AP I2C1 <i>400kHz</i>	ADELYN	1110100X	0x74	0xE8	400 kHz
	TIGRIS	1110101X	0x75	0xEA	400 kHz
	BACKLIGHT 2	1100010X	0x62	0xC4	1 MHz
AP I2C2 <i>400kHz</i>	ALS	0101001X	0x29	0x52	400 kHz
	CONVOY	0100001X	0x21	0x42	3.4 MHz
		0100010X	0x22	0x44	400 kHz
	SPKAMP 2 (L26)	10000ZYX	0x40	0x80	400 MHz
AP I2C3 <i>100kHz</i>	DOCK FLEX (MIC1)	1010100X	0x54	0xA8	1 MHz
	COMBINED BUTTON FLEX (MIC2)	1010100X	0x56	0xAC	1 MHz
	DISP EEPROM	1010001X	0x51	0xA2	400kHz
Homer I2C <i>1MHz</i>	SCHRODINGER (STANDARD)	0001110X	0xE	0x1C	1 MHz
	SCHRODINGER (CAL REG'S)	0001111X	0xF	0x1D	1 MHz
	EEPROM (STANDARD)	1010000X	0x50	0xA0	
	EEPROM (CONFIG)	1010000X	0x50	0xA0	

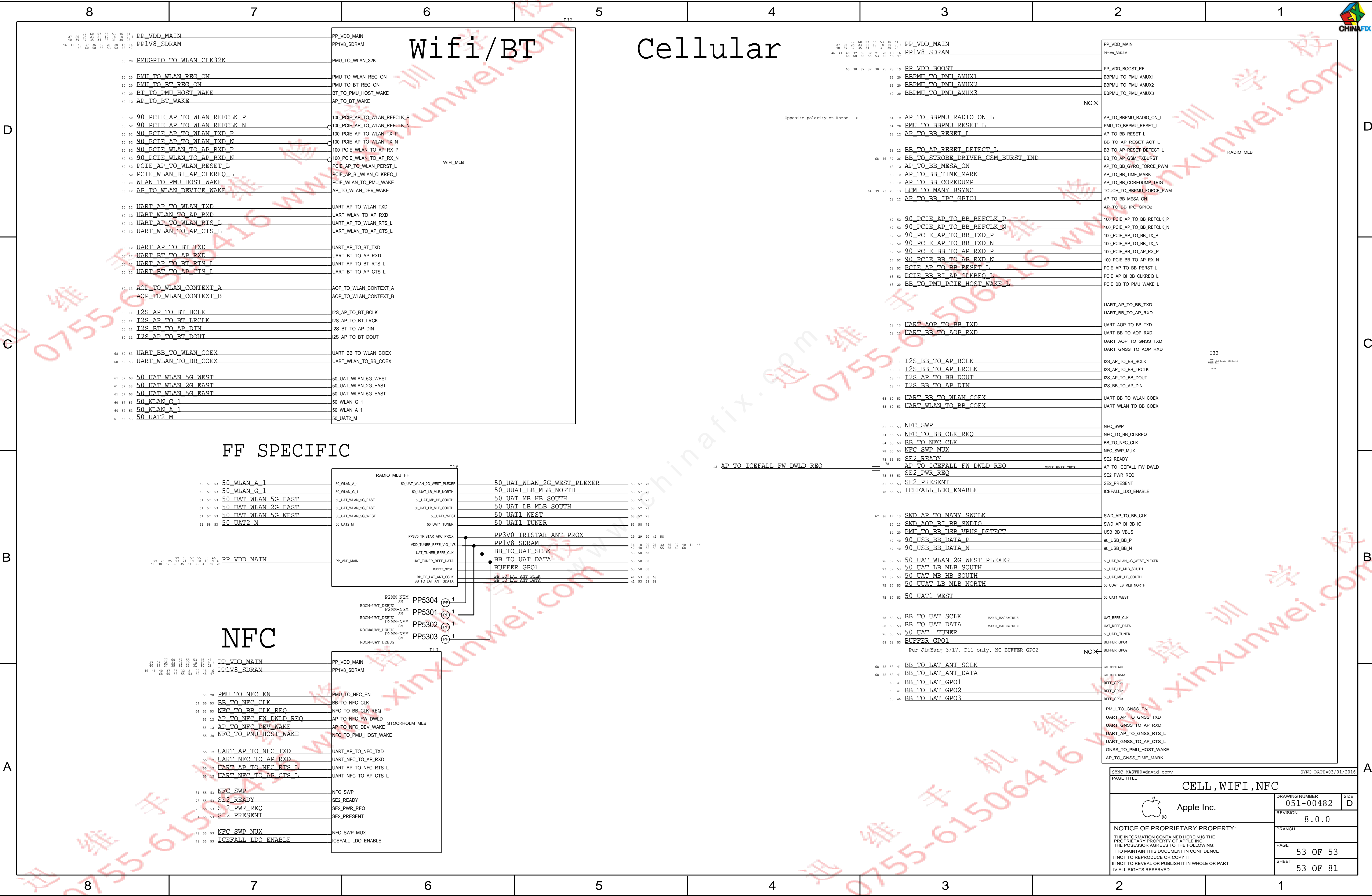
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spare

spare

8	90 AP PCIE2 RXD C P	C0816	1	2	0.1UF	90 PCIE BB TO AP RXD P	53	MAKE_BASE=TRUE
		ROOM+SUC	20A	6.3V	QMS_VCC2+TRCE			
		X5R-CERM		01005				
8	90 AP PCIE2 RXD C N	C0817	1	2	0.1UF	90 PCIE BB TO AP RXD N	53	MAKE_BASE=TRUE
		ROOM+SUC	20A	6.3V	QMS_VCC2+TRCE			
		X5R-CERM		01005				
8	90 AP PCIE2 TXD C P	C0815	1	2	0.1UF	90 PCIE AP TO BB TXD P	53	MAKE_BASE=TRUE
		ROOM+SUC	20A	6.3V	QMS_VCC2+TRCE			
		X5R-CERM		01005				
8	90 AP PCIE2 TXD C N	C0818	1	2	0.1UF	90 PCIE AP TO BB TXD N	53	MAKE_BASE=TRUE
		ROOM+SUC	20A	6.3V	QMS_VCC2+TRCE			
		X5R-CERM		01005				
52	90 AP PCIE3 RXD C P	C0811	1	2	0.1UF	90 PCIE WLAN TO AP RXD P	53	MAKE_BASE=TRUE
		ROOM+SUC	20A	6.3V	QMS_VCC2+TRCE			
		X5R-CERM		01005				
52	90 AP PCIE3 RXD C N	C0812	1	2	0.1UF	90 PCIE WLAN TO AP RXD N	53	MAKE_BASE=TRUE
		ROOM+SUC	20A	6.3V	QMS_VCC2+TRCE			
		X5R-CERM		01005				
8	90 AP PCIE3 TXD C P	C0814	1	2	0.1UF	90 PCIE AP TO WLAN TXD P	53	MAKE_BASE=TRUE
		ROOM+SUC	20A	6.3V	QMS_VCC2+TRCE			
		X5R-CERM		01005				
8	90 AP PCIE3 TXD C N	C0813	1	2	0.1UF	90 PCIE AP TO WLAN TXD N	53	MAKE_BASE=TRUE
		ROOM+SUC	20A	6.3V	QMS_VCC2+TRCE			
		X5R-CERM		01005				
8	90 PCIE AP TO WLAN REFCLK P					90 PCIE AP TO WLAN REFCLK P	53	MAKE_BASE=TRUE
8	90 PCIE AP TO WLAN REFCLK N					90 PCIE AP TO WLAN REFCLK N	53	MAKE_BASE=TRUE
8	90 PCIE AP TO BB REFCLK P					90 PCIE AP TO BB REFCLK P	53	MAKE_BASE=TRUE
8	90 PCIE AP TO BB REFCLK N					90 PCIE AP TO BB REFCLK N	53	MAKE_BASE=TRUE
8	PCIE AP TO WLAN RESET L					PCIE AP TO WLAN RESET L	53	MAKE_BASE=TRUE
8	PCIE AP TO BB RESET L					PCIE AP TO BB RESET L	53	MAKE_BASE=TRUE
8	PCIE WLAN BI AP CLKREQ L					PCIE WLAN BI AP CLKREQ L	52 53	MAKE_BASE=TRUE
8	PCIE BB BI AP CLKREQ L					PCIE BB BI AP CLKREQ L	52 53	MAKE_BASE=TRUE





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STOCKHOLM




NFC CONTROLLER

5V BOOSTER

NFC FRONT END

NFC LOAD SWITCH

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1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%. 2. ALL CAPACITANCE VALUES ARE IN MICROFARADS. 3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.												REV	ECN	DESCRIPTION OF REVISION	CK APPD DATE																												
												8	0006532879	ENGINEERING RELEASED	2016-07-06																												
D11 RADIO_MLB_FF																																											
FEB 05, 2016																																											
PDF PAGE												CONTENTS																															
2												METROCIRC																															
LMBRF:																																											
<table><tr><td>152S2001</td><td>1</td><td>2.4MS,600MA,0201</td><td>L8008_RF</td><td>CRITICAL</td><td>LMBRF</td></tr><tr><td>152S0983</td><td>1</td><td>6.8MH,2K,1.5A,0.0550MH,0402</td><td>L8009_RF</td><td>CRITICAL</td><td>LMBRF</td></tr></table>																152S2001	1	2.4MS,600MA,0201	L8008_RF	CRITICAL	LMBRF	152S0983	1	6.8MH,2K,1.5A,0.0550MH,0402	L8009_RF	CRITICAL	LMBRF																
152S2001	1	2.4MS,600MA,0201	L8008_RF	CRITICAL	LMBRF																																						
152S0983	1	6.8MH,2K,1.5A,0.0550MH,0402	L8009_RF	CRITICAL	LMBRF																																						
NOLMBRF:																																											
<table><tr><td>PART#</td><td>QTY</td><td>DESCRIPTION</td><td>REFERENCE DESIGNATOR(S)</td><td>CRITICAL</td><td>BOM OPTION</td></tr><tr><td>152S2024</td><td>1</td><td>1.4MS,1.1A,0201</td><td>L8008_RF</td><td>CRITICAL</td><td>NOLMBRF</td></tr><tr><td>152S00610</td><td>1</td><td>3.6MH,2K,1.7A,0.0450MH,0402</td><td>L8009_RF</td><td>CRITICAL</td><td>NOLMBRF</td></tr></table>																PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION	152S2024	1	1.4MS,1.1A,0201	L8008_RF	CRITICAL	NOLMBRF	152S00610	1	3.6MH,2K,1.7A,0.0450MH,0402	L8009_RF	CRITICAL	NOLMBRF										
PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION																																						
152S2024	1	1.4MS,1.1A,0201	L8008_RF	CRITICAL	NOLMBRF																																						
152S00610	1	3.6MH,2K,1.7A,0.0450MH,0402	L8009_RF	CRITICAL	NOLMBRF																																						
?																																											
<table><tr><td colspan="2">DRAWING TITLE</td><td colspan="2">SCH,MLB,D11</td></tr><tr><td colspan="2" rowspan="2"> Apple Inc.</td><td>DRAWING NUMBER</td><td>051-00482</td><td>SIZE</td><td>D</td></tr><tr><td>REVISION</td><td colspan="3">8.0.0</td></tr><tr><td colspan="2" rowspan="3">NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED</td><td>BRANCH</td><td colspan="3"></td></tr><tr><td>PAGE</td><td colspan="3">1 OF 3</td></tr><tr><td>SHEET</td><td colspan="3">56 OF 81</td></tr></table>																DRAWING TITLE		SCH,MLB,D11		 Apple Inc.		DRAWING NUMBER	051-00482	SIZE	D	REVISION	8.0.0			NOTICE OF PROPRIETARY PROPERTY: THE INFORMATION CONTAINED HEREIN IS THE PROPRIETARY PROPERTY OF APPLE INC. THE POSSESSOR AGREES TO THE FOLLOWING: I TO MAINTAIN THIS DOCUMENT IN CONFIDENCE II NOT TO REPRODUCE OR COPY IT III NOT TO REVEAL OR PUBLISH IT IN WHOLE OR PART IV ALL RIGHTS RESERVED		BRANCH				PAGE	1 OF 3			SHEET	56 OF 81		
DRAWING TITLE		SCH,MLB,D11																																									
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		PAGE	1 OF 3																																								
		SHEET	56 OF 81																																								
8		7		6		5		4		3		2		1																													

METROCIRC

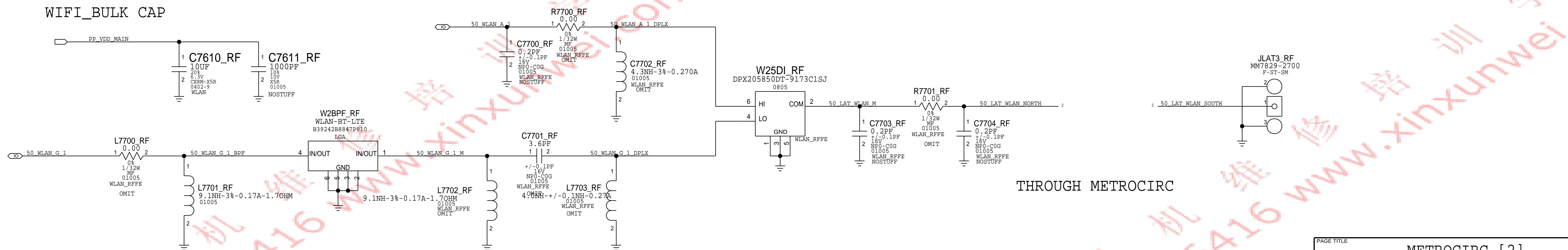
D11 NORTH-SOUTH METROCIRC
339S00147


D11 EAST-WEST METROCIRC
339S00146




WIFI LOWER ANTENNA FEED

COAX



PAGE TITLE			
METROCIRC [2]			
 Apple Inc.	DRAWING NUMBER	051-00482	SIZE
	REVISION	8.0.0	D
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UAT MATCH AND TUNER [3]		051-00482		D	
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		8.0.0			
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		3 OF 3			
		SHEET			
		58 OF 81			

WIFI/BT

D

C

B

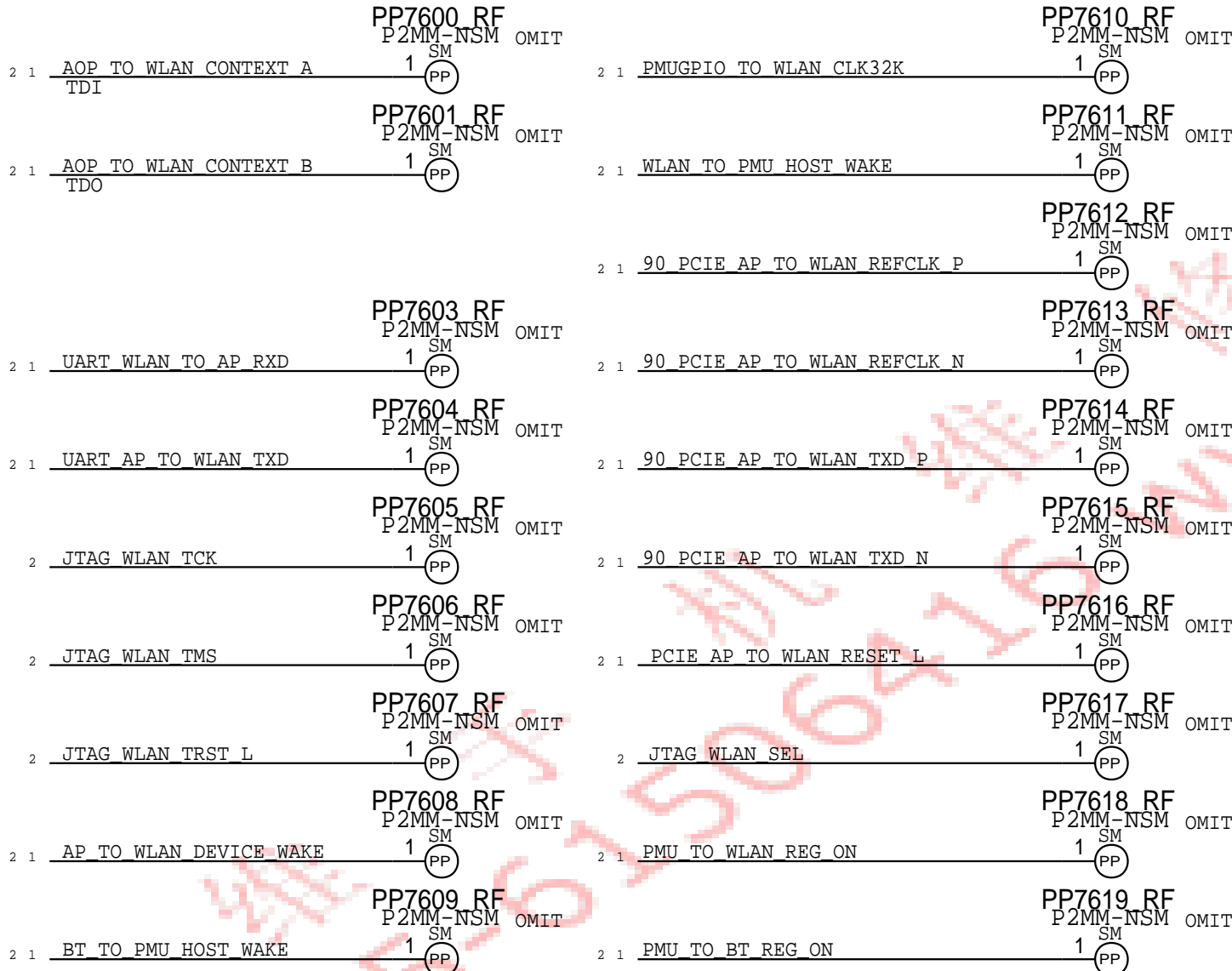
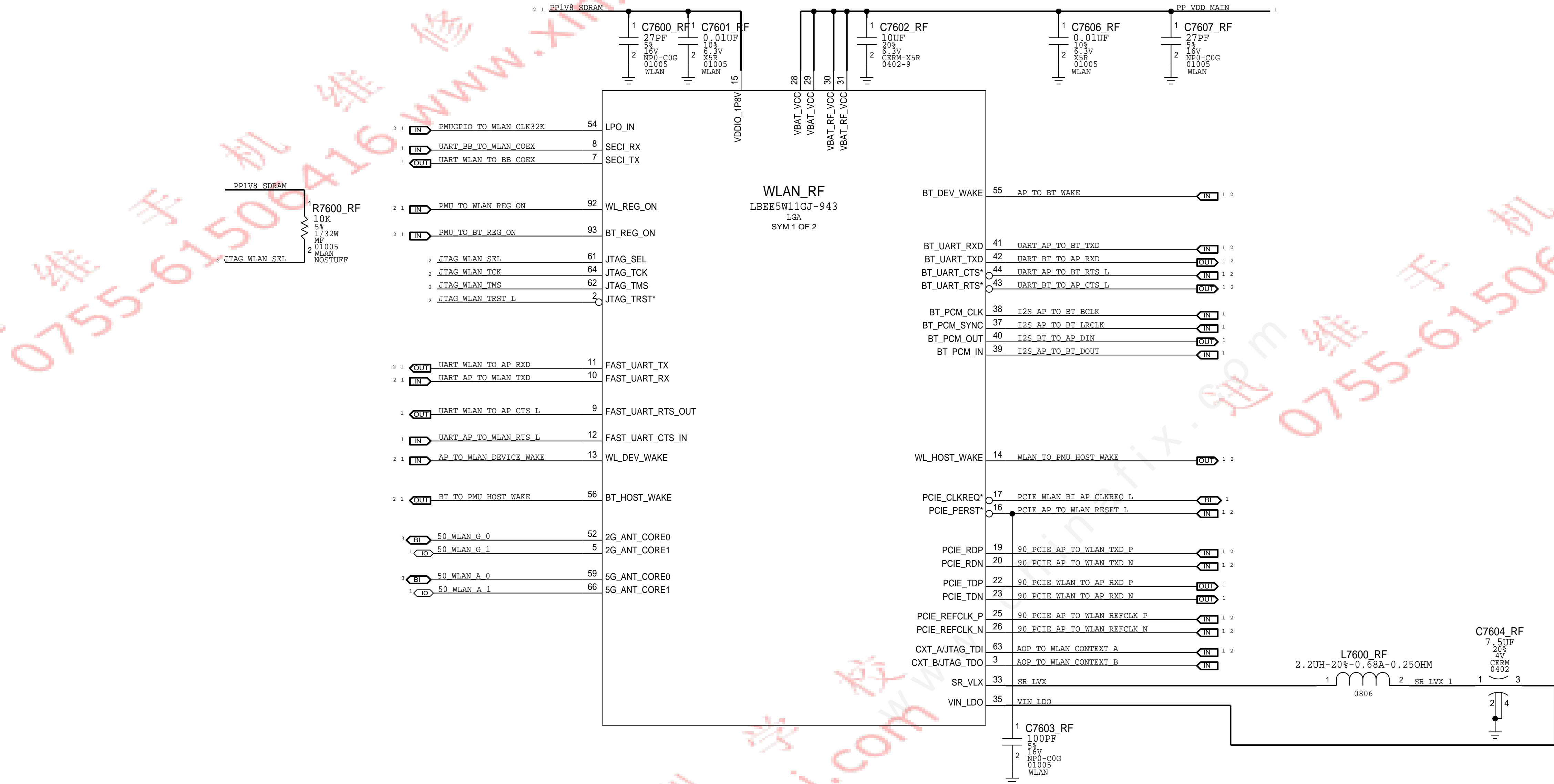
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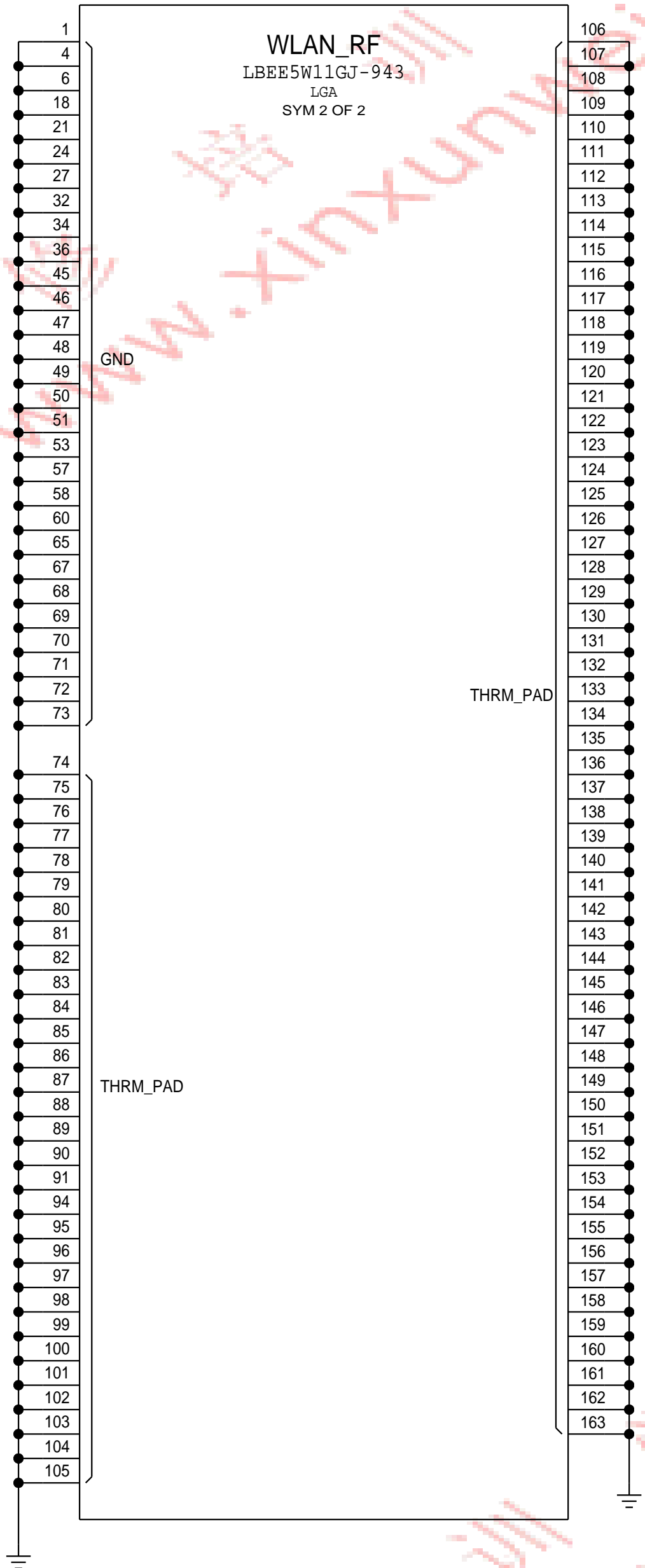
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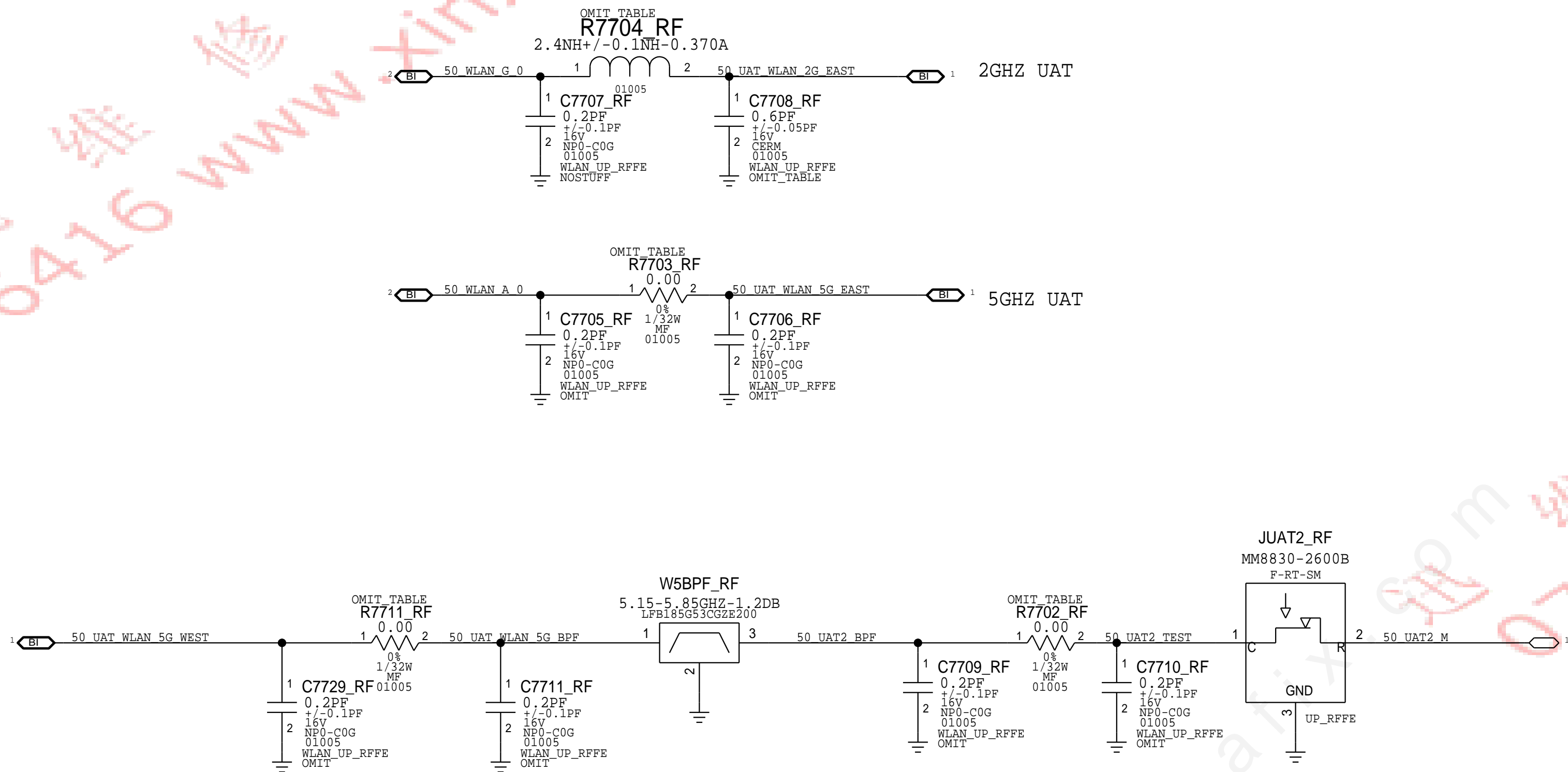
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		SIZE	D
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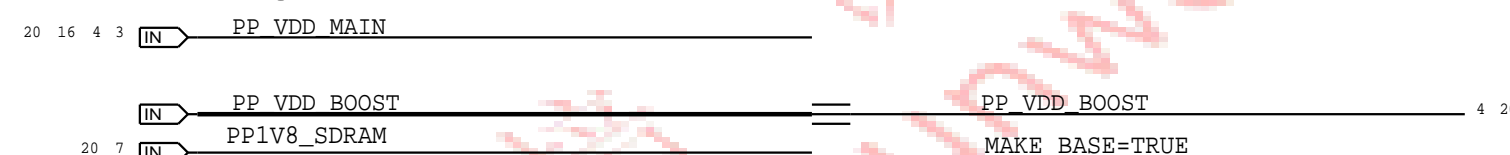
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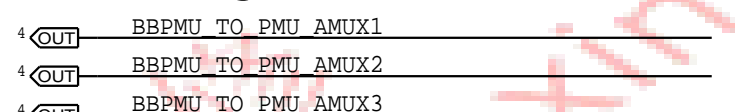
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AP CONNECTIONS

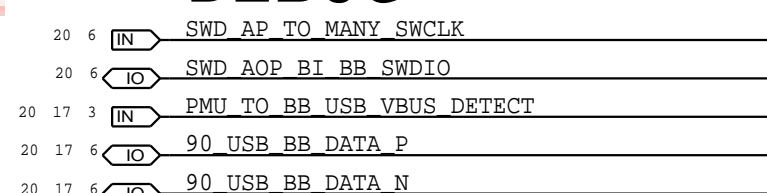
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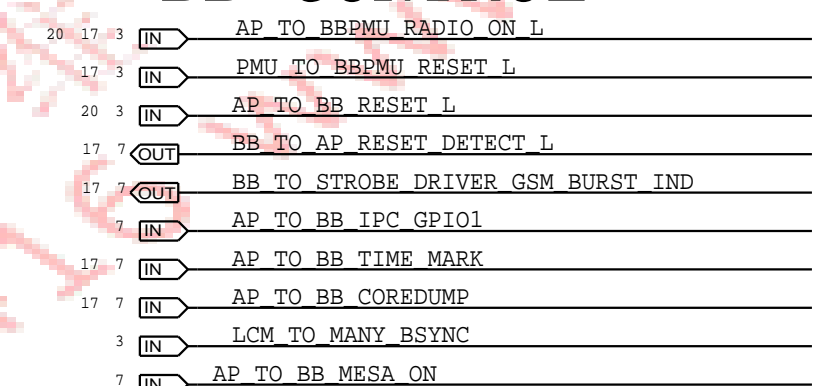
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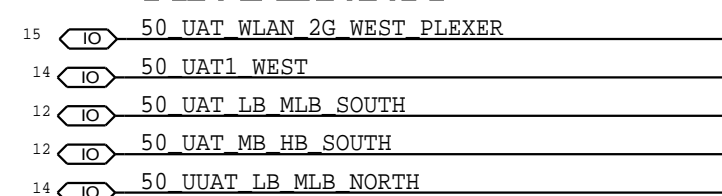
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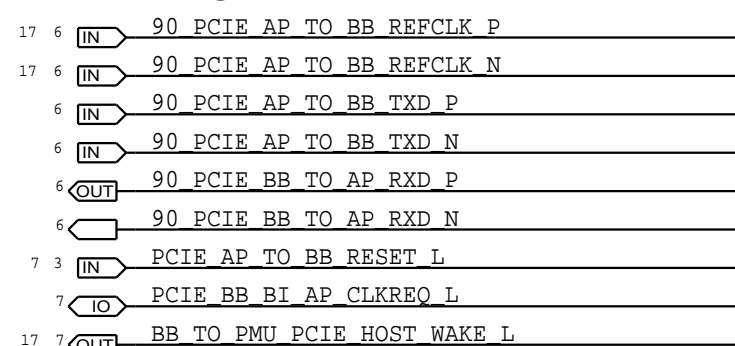
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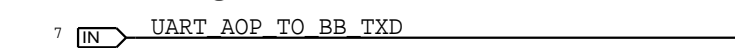
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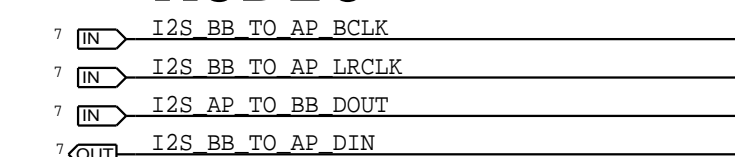
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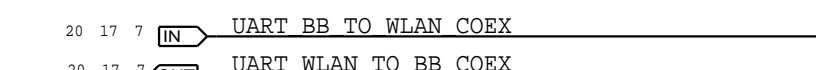
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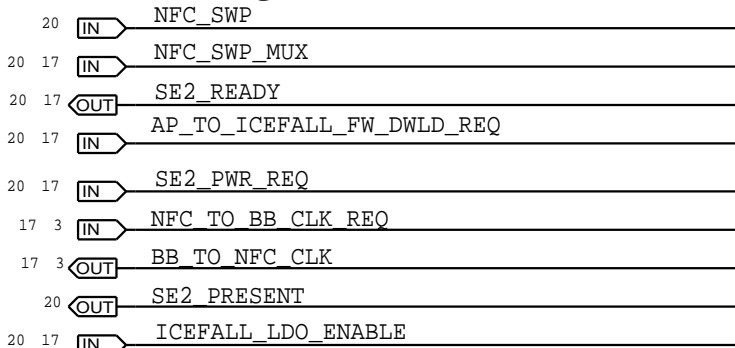
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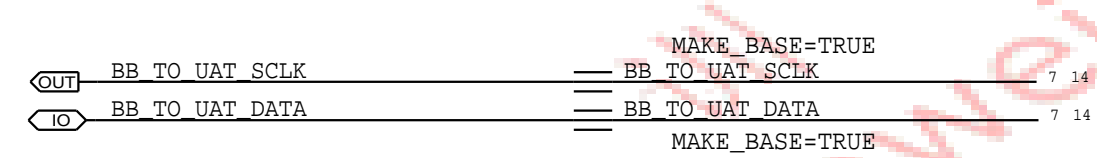
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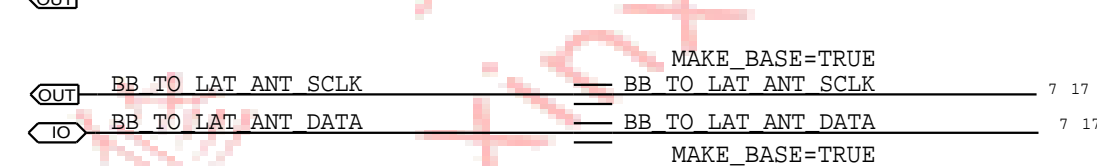
NFC




TUNER



DOCK



page1

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ALTERNATES

PART NUMBER	ALTERNATE FOR PART NUMBER	BOM OPTION	REF DES	COMMENTS:
19780565	19780593	ALTERNATE	Y5501_RF	19P2 MHZ XTAL
19780598	19780593	ALTERNATE	Y5501_RF	19P2 MHZ XTAL
138800101	138800095	ALTERNATE	ALL	TRANSITION CAP
335800013	335800894	ALTERNATE	EPROM_RF	IC EPROM
353800253	353800121	ALTERNATE	SNPMX_RF	IC SWITCH SPDT
155800235	155800234	ALTERNATE	UDDP1_RF	NOLIMRF

ALL:C5616 RF-C5618 RF,C5632 RF-C5634 RF

```
81      ICEFALL, SIM, DEBUG_CONN
```

1. ALL RESISTANCE VALUES ARE IN OHMS, 0.1 WATT +/- 5%.

2. ALL CAPACITANCE VALUES ARE IN MICROFARADS

3. ALL CRYSTALS & OSCILLATOR VALUES ARE IN HERTZ.

MAV16 RADIO MLB

LAST_MODIFICATION=Wed Jun 29 12:06:01 2016

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62	1	page1	<SYNC_MASTER1>	<SYNC_DATE1>
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64	3	PMU: CONTROL AND CLOCKS	<SYNC_MASTER3>	<SYNC_DATE3>
65	4	PMU: SWITCHERS AND LDOS	<SYNC_MASTER4>	<SYNC_DATE4>
66	5	BASEBAND: POWER2	<SYNC_MASTER5>	<SYNC_DATE5>
67	6	BASEBAND: CONTROL		
68	7	BASEBAND GPIOs		
69	8	TRANSCEIVER0/1: POWER		
70	9	TRANSCEIVER0/1: TX PORTS		
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72	11	RECEIVE MATCHING		
73	12	LOWER ANTENNA & COUPLERS		
74	13	DIVERSITY RECEIVE ASM'S		
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77	16	PMU: ET MODULATOR		
78	17	TEST POINTS & BOOT CONFIG		
79	18	TDD TRANSMIT		
80	19	FDD TRANSMIT		
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SCH: 951-00964

BOM: 939-00826

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LMBRF:

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
35300723	1	MLB PAD	MLBPA_RF	CRITICAL	LMBRF
11780002	1	MLB PAD LAT OUTPUT MATCH	R7105_RF	CRITICAL	LMBRF
15282042	1	MLB PAD UAT OUTPUT MATCH	R7106_RF	CRITICAL	LMBRF
35300627	1	MLB LMA	MLBLM_RF	CRITICAL	LMBRF
11780002	1	MLB LMA OUTPUT MATCH	R6710_RF	CRITICAL	LMBRF
155000139	1	PENTAPLEXER	PPLXR_RF	CRITICAL	LMBRF
1180643	1	RES,RF,4.99K OHM,01005	R7506_RF	CRITICAL	LMBRF
155000193	1	FILTR,DIPLEXER,LA-MB-HB,DPX,SHIELD,0805	UATDI_RF	CRITICAL	LMBRF
155000158	1	FILTR,DIPLEXER,LA-MB/HB/MIRROR,0805	UPPDI_RF	CRITICAL	LMBRF
337800284	1	IC,SECURE ELEMENT,80K20211,ML80A25	SE2_RF	CRITICAL	LMBRF
1380739	1	CAP,1UF,0201	C7201_RF	CRITICAL	LMBRF
1380739	1	CAP,1UF,0201	C7528_RF	CRITICAL	LMBRF
13260316	1	CAP,0.1UF,01005	C7501_RF	CRITICAL	LMBRF
1380739	1	CAP,1UF,0201	C7523_RF	CRITICAL	LMBRF
1380739	1	CAP,1UF,0201	C7524_RF	CRITICAL	LMBRF
138000032	1	CAP,2.2UF,0201	C7529_RF	CRITICAL	LMBRF
138000032	1	CAP,2.2UF,0201	C7530_RF	CRITICAL	LMBRF
13180217	1	CAP,100PF,01005	C7531_RF	CRITICAL	LMBRF
1180627	1	RES,10KOHM,01005	R7512_RF	CRITICAL	LMBRF
353000026	1	LDO,REG,2X2	SE2LDO_RF	CRITICAL	LMBRF
13180630	1	CAP,CER,NPO/COG,27PF,2K,16V,01005	C6345_RF	CRITICAL	LMBRF
13180341	1	CAP,CER,COG,3.0PF,+/-0.05,25V,0201,HI-Q	C6348_RF	CRITICAL	LMBRF
15282006	1	IND,FILM,6.2MH,3K,400MA,UH-Q,0201	L6322_RF	CRITICAL	LMBRF
13180630	1	CAP,CER,NPO/COG,27PF,2K,16V,01005	C6315_RF	CRITICAL	LMBRF
15282006	1	IND,FILM,6.2MH,3K,400MA,UH-Q,0201	L6305_RF	CRITICAL	LMBRF
13180341	1	CAP,CER,COG,3.0PF,+/-0.05PF,25V,0201,HI-Q	C6306_RF	CRITICAL	LMBRF
13180630	1	CAP,CER,NPO/COG,27PF,2K,16V,01005	C6106_RF	CRITICAL	LMBRF
15282051	1	IND,1.3MH,1.1A,0201	R6404_RF	CRITICAL	LMBRF
15282000	1	IND,2.0MH,600MA,0201	R7104_RF	CRITICAL	LMBRF
131800001	1	CAP,CER,0.1PF,25V,0201	C7119_RF	CRITICAL	LMBRF
1180724	1	RES,RF,00HM,1/20W,0201	R6703_RF	CRITICAL	LMBRF
1180724	1	RES,RF,00HM,1/20W,0201	R6708_RF	CRITICAL	LMBRF
13180363	1	CAP,CER,COG,RQ,0.6PF,+/-0.05PF,25V,0201	C7123_RF	CRITICAL	LMBRF
15282002	1	IND,2.7MH,+/-0.1MH,600MA,0201,UH-Q	R6605_RF	CRITICAL	LMBRF
13180337	1	CAP,1.5PF,+/-0.05PF,25V,0201,RQ	C6610_RF	CRITICAL	LMBRF
1180724	1	RES,RF,00HM,1/20W,0201	R6606_RF	CRITICAL	LMBRF
13180275	1	CAP,CER,COG,0.3PF,+/-0.1PF,25V,0201,RQ	C6416_RF	CRITICAL	LMBRF
11806008	1	RES,RF,1K OHM,1K,1/32W,01005	R5911_RF	CRITICAL	LMBRF
15281356	1	IND,10MH,3K,250MA,HI-Q,0201	C6613_RF	CRITICAL	LMBRF

NOLMBRF:

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
13180444	1	DCS/PCS RX FILTER MATCH	R6301_RF	CRITICAL	NOLMBRF
15282020	1	DCS/PCS RX FILTER MATCH	L6320_RF	CRITICAL	NOLMBRF
155800149	1	DCS/PCS RX FILTER	GSMRX_RF	CRITICAL	NOLMBRF
15282005	1	DCS/PCS RX MATCH (DCS)	L6318_RF	CRITICAL	NOLMBRF
13180319	1	DCS/PCS RX MATCH (DCS)	C6332_RF	CRITICAL	NOLMBRF
13180630	1	DCS/PCS RX MATCH (DCS)	C6340_RF	CRITICAL	NOLMBRF
15282005	1	DCS/PCS RX MATCH (DCS)	L6319_RF	CRITICAL	NOLMBRF
13180385	1	DCS/PCS RX MATCH (DCS)	C6333_RF	CRITICAL	NOLMBRF
13180630	1	DCS/PCS RX MATCH (DCS)	C6341_RF	CRITICAL	NOLMBRF
155800163	1	QUADPLEXER	PPLXR_RF	CRITICAL	NOLMBRF
155800199	1	FILTR,DPX,PASS THRU,LA-MB-HB,SHLD,0805	UATDI_RF	CRITICAL	NOLMBRF
155800234	1	FILTR,DPX,PASS THRU,LA-MB-HB,UNSHLD,0805	UPPDI_RF	CRITICAL	NOLMBRF
1180724	1	RES,RF,00HM,1/20W,0201	R6404_RF	CRITICAL	NOLMBRF
15282044	1	IND,2.2MH,+/-0.1MH,600MA,0201	R7104_RF	CRITICAL	NOLMBRF
152800157	1	1.2MH,+/-0.05MH,1.1A,UH-Q,0201	R6703_RF	CRITICAL	NOLMBRF
13180549	1	18PF,0201,25V	R6708_RF	CRITICAL	NOLMBRF
13180445	1	CAP,CER,12PF,5K,25V,0201,HI-Q	R6606_RF	CRITICAL	NOLMBRF

D10 SPECIFIC:

PART#	QTY	DESCRIPTION	REFERENCE DESIGNATOR(S)	CRITICAL	BOM OPTION
13180278	1	CAP,CER,COG,1PF,+/-0.1PF,25V,0201,HI-Q	C7120_RF	CRITICAL	D10

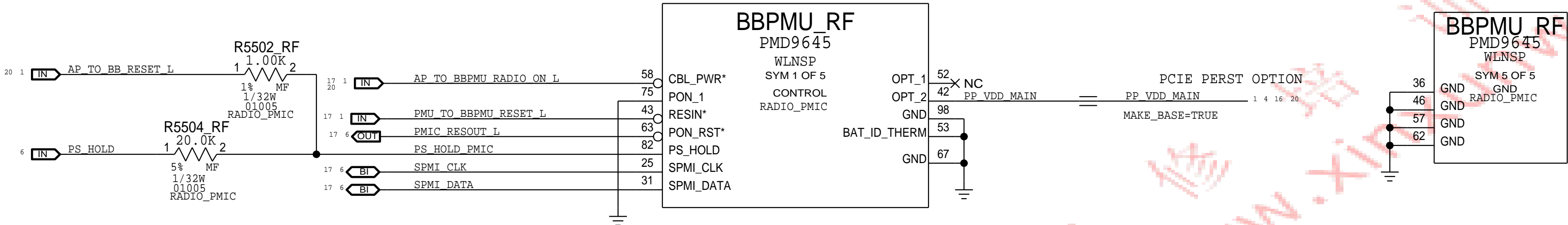
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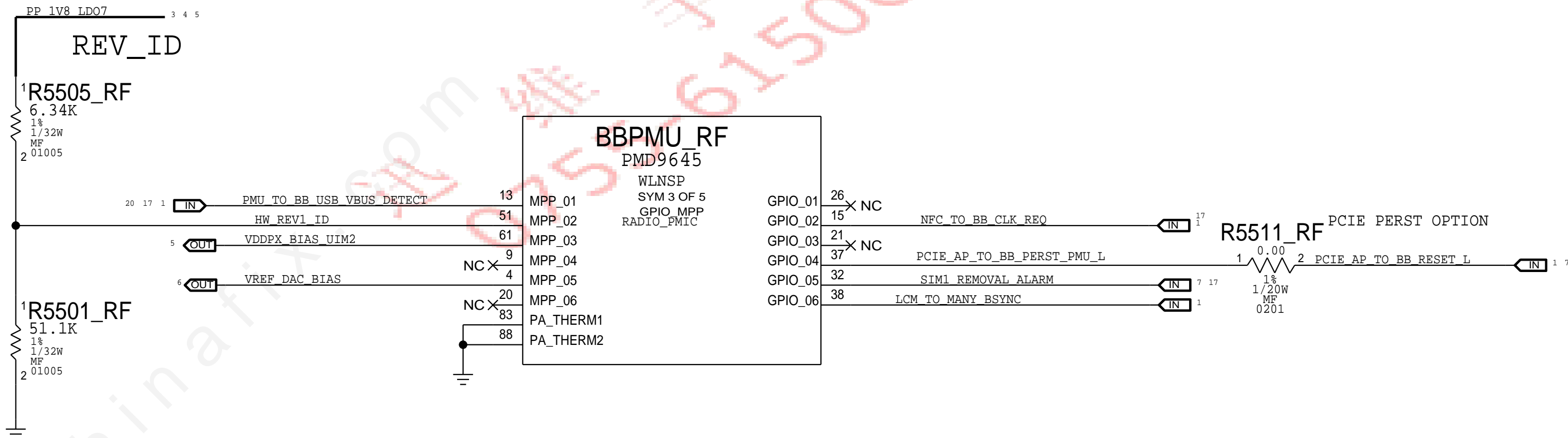
PMU: CONTROL AND CLOCKS

HW_REV_ID	R5505	R5501	REVISION
0.10V	887K	51.1K	DEV1
0.20V	422K	51.1K	DEV2
0.30V	255K	51.1K	DEV 2.1
0.40V	180K	51.1K	DEV 3
0.50V	124K	51.1K	T181
0.60V	102K	51.1K	PP/P1
0.70V	82.5K	51.1K	DEV4
0.80V	63.4K	51.1K	P2
0.90V	51.1K	51.1K	DEV5
1.00V	51.1K	63.4K	EVT
1.10V	51.1K	82.5K	EVT DOE
1.20V	50.0K	100K	EVT ALT/CARBON
1.30V	39.0K	100K	DEV6
1.40V	14.7K	51.1K	CARRIER
1.50V	40.2K	200K	DVT
1.60V	6.34K	51.1K	PVT

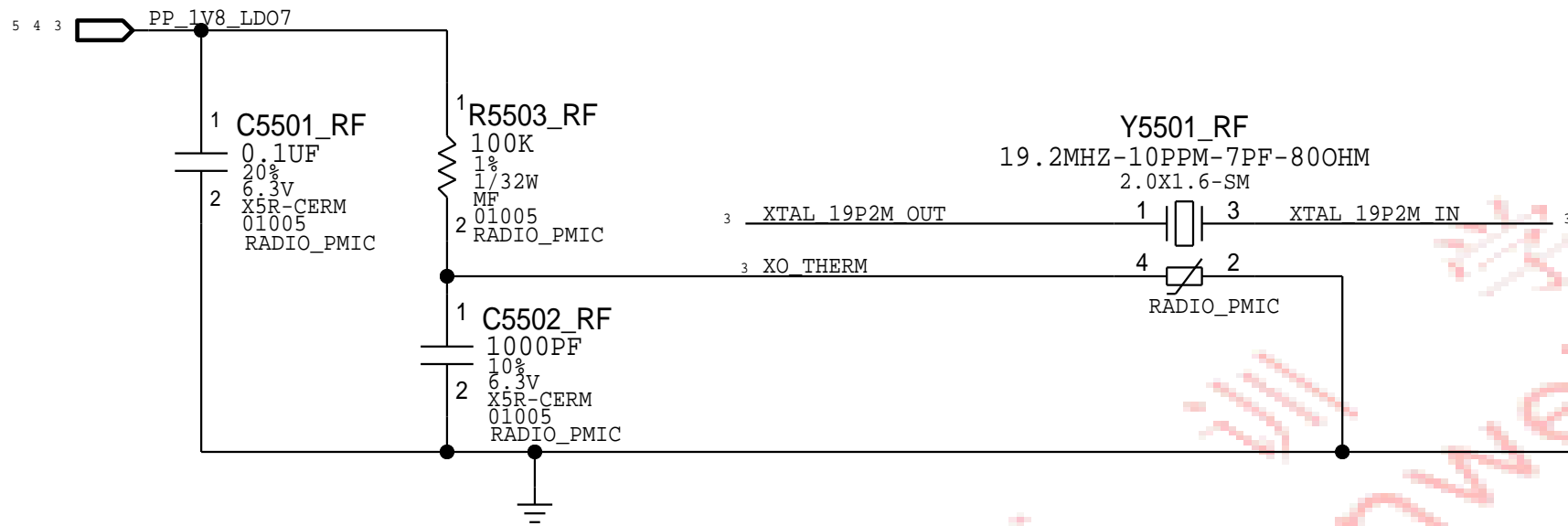
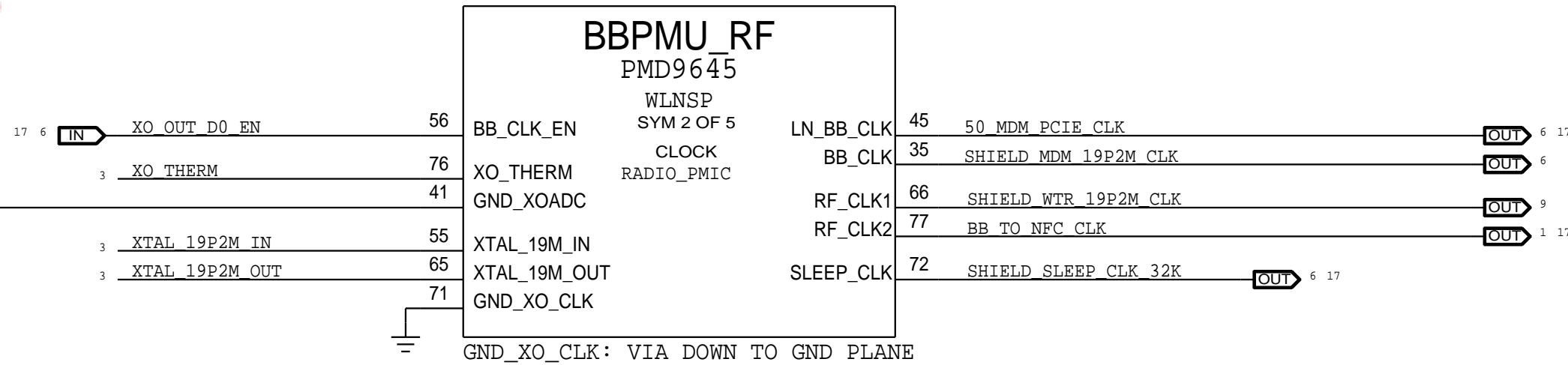
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MPPS AND GPIOs: PMU

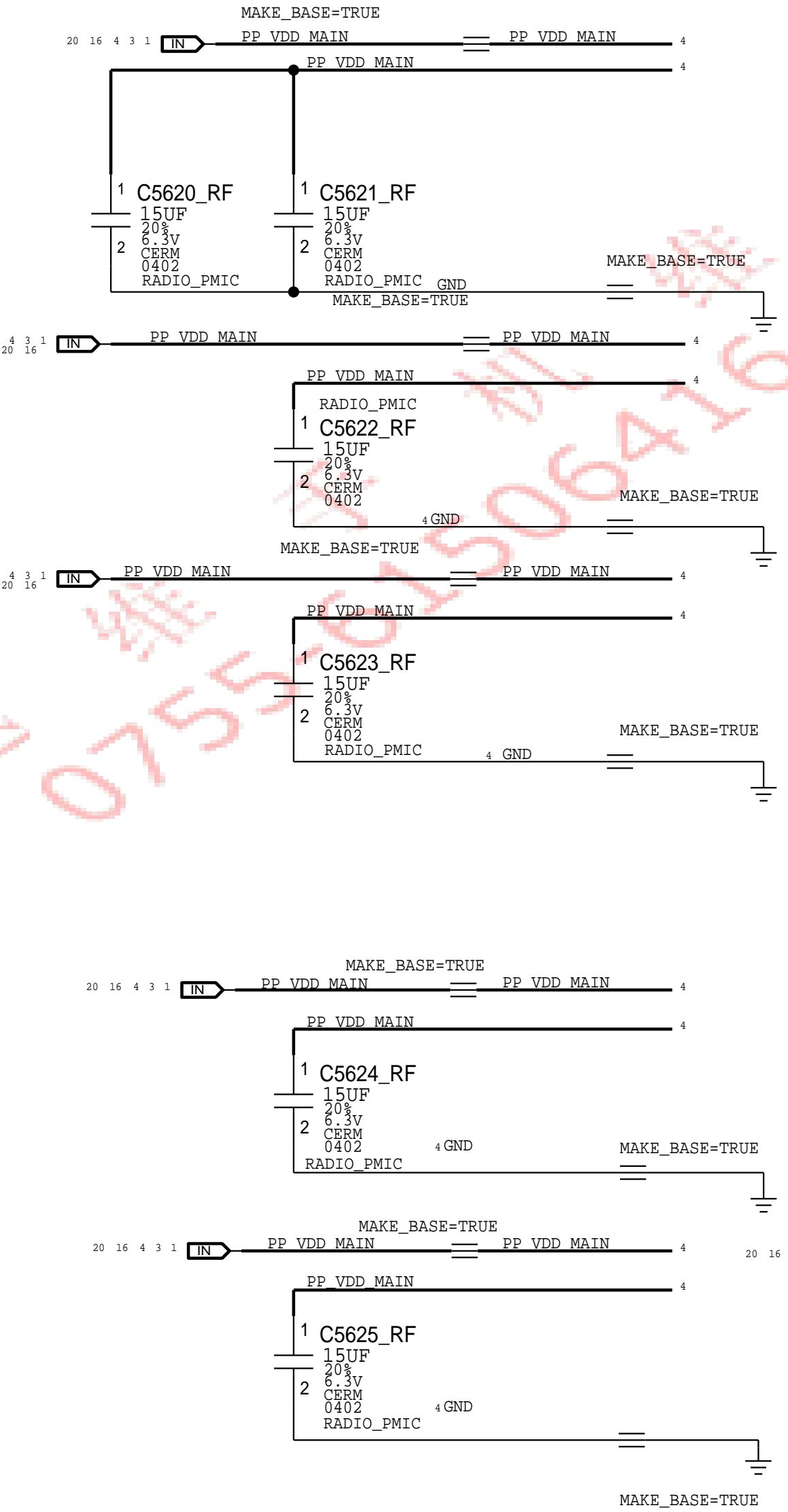


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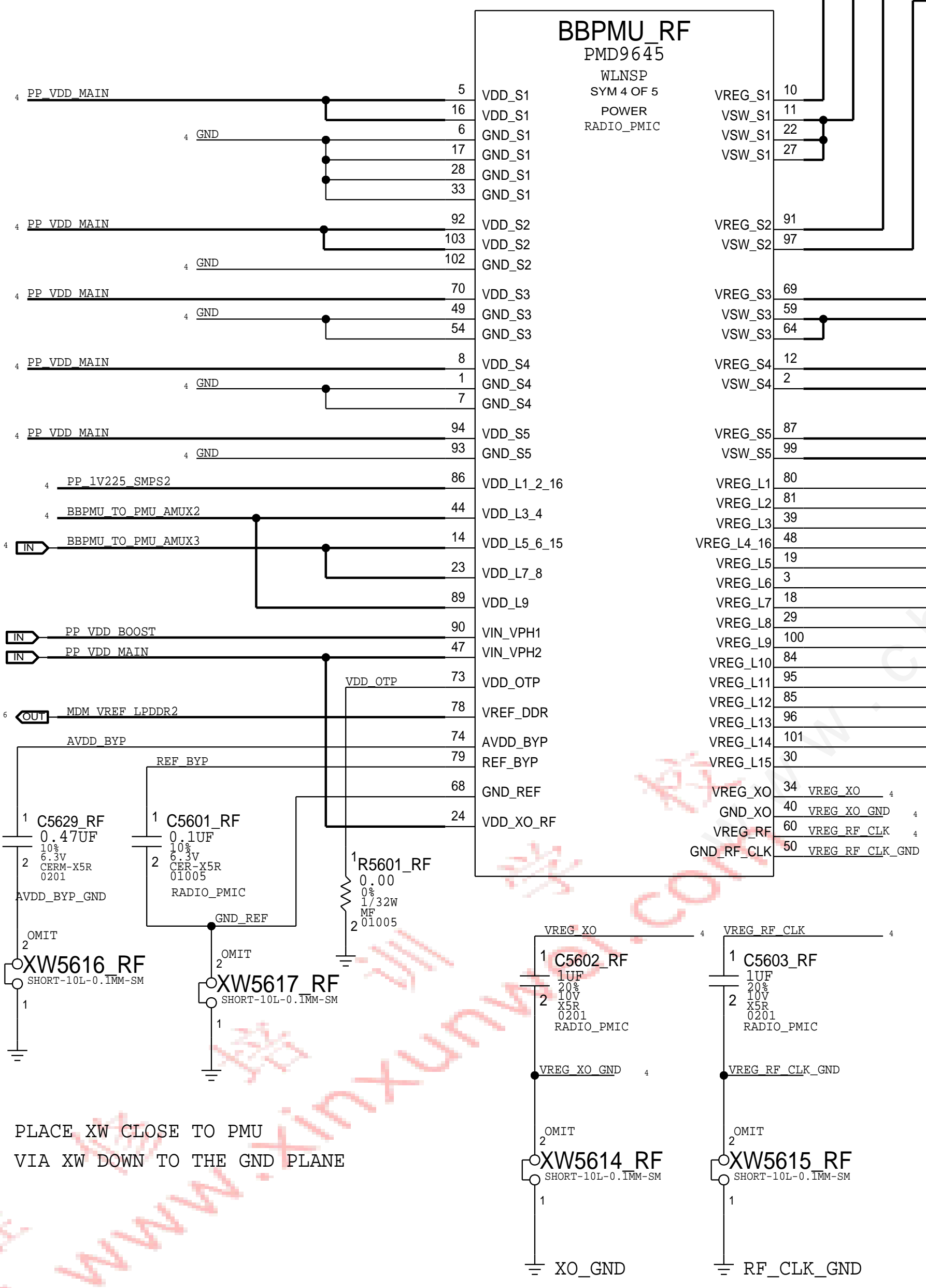
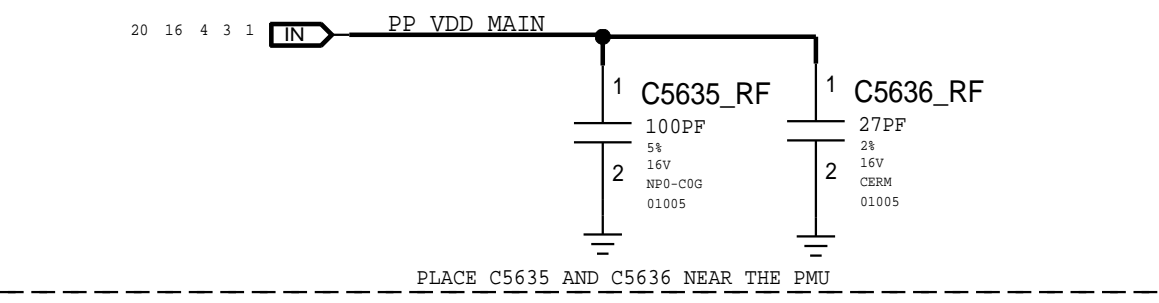


PMU: SWITCHERS AND LDOS

SWITCHERS BULK CAPS

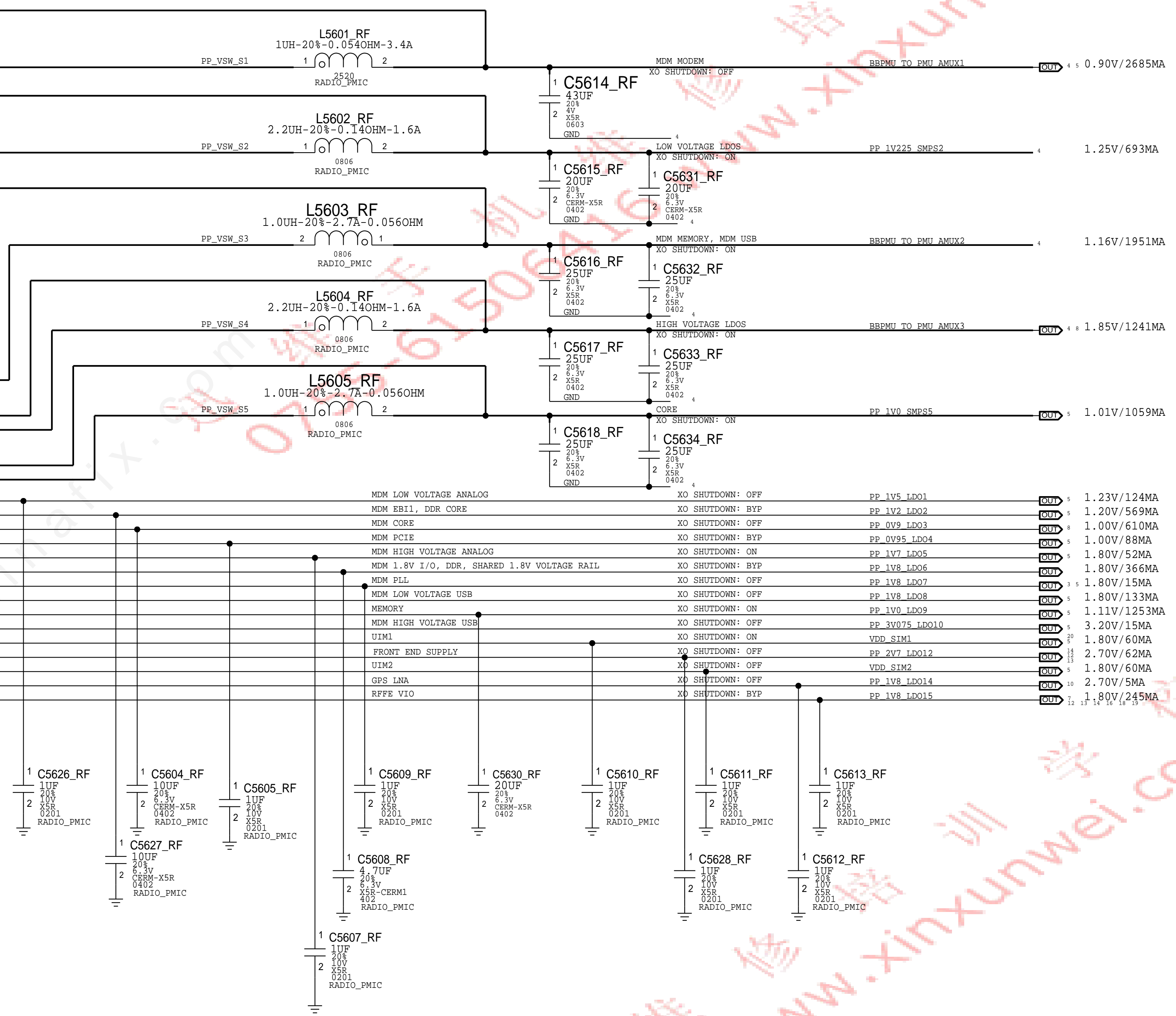


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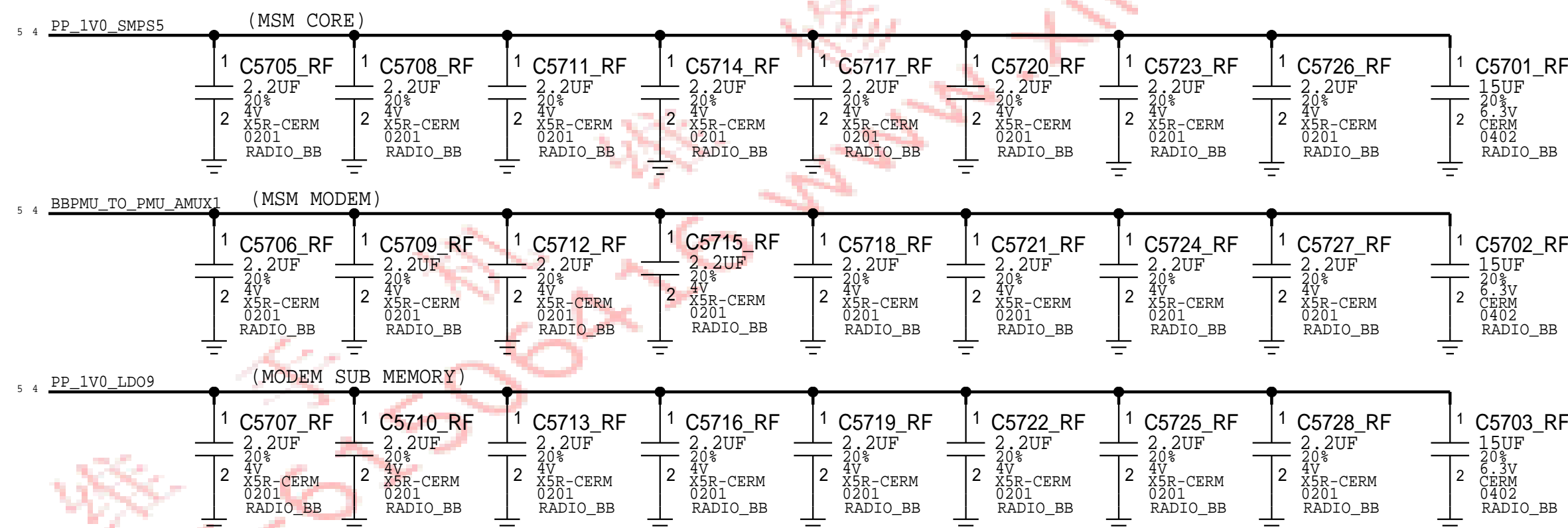
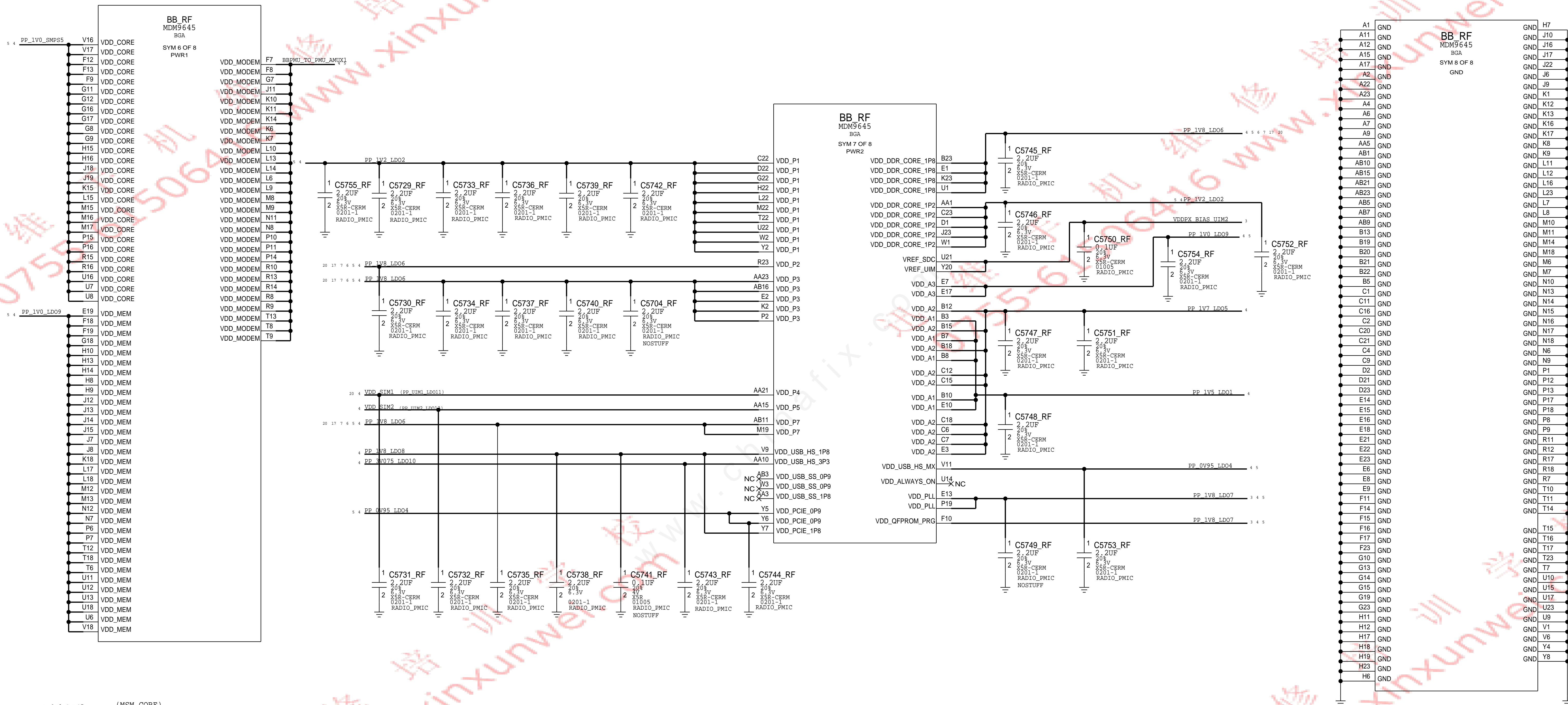
PLACE XW CLOSE TO PMU
VIA XW DOWN TO THE GND PLANE

PLACE XW CLOSE TO PMU
VIA XW DOWN TO THE GND PLANE

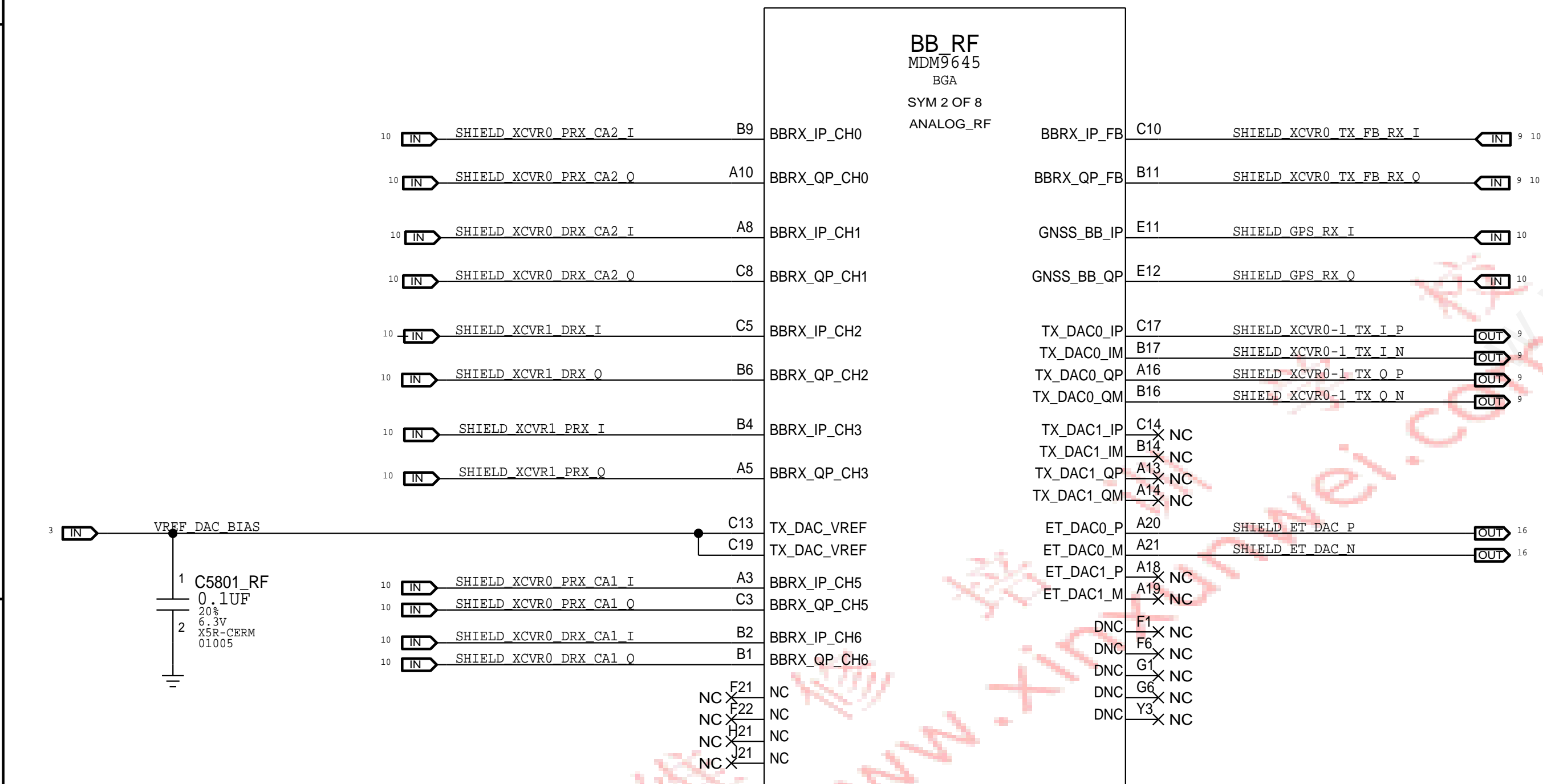
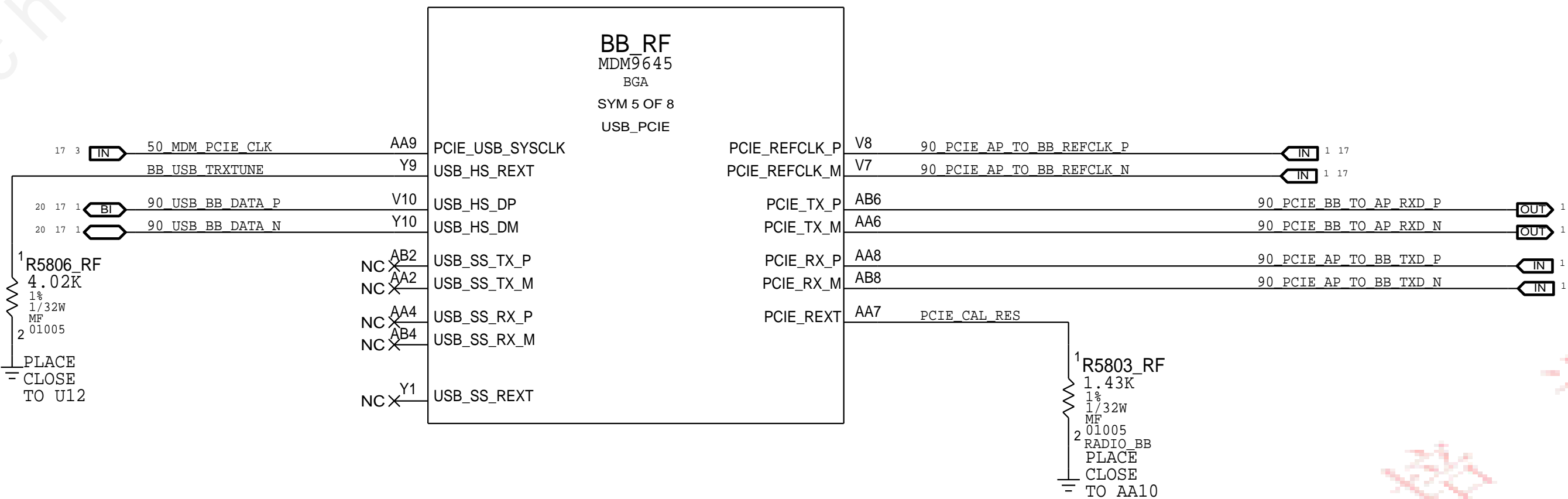
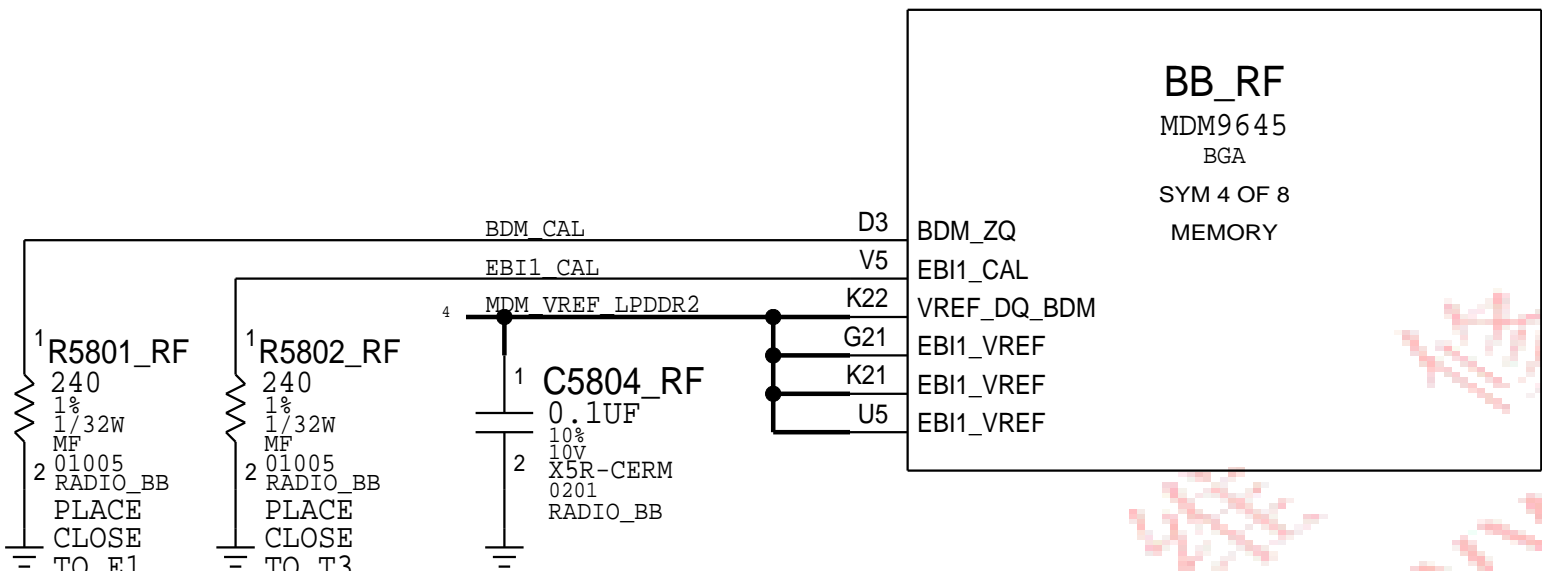


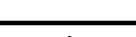
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BASEBAND: POWER



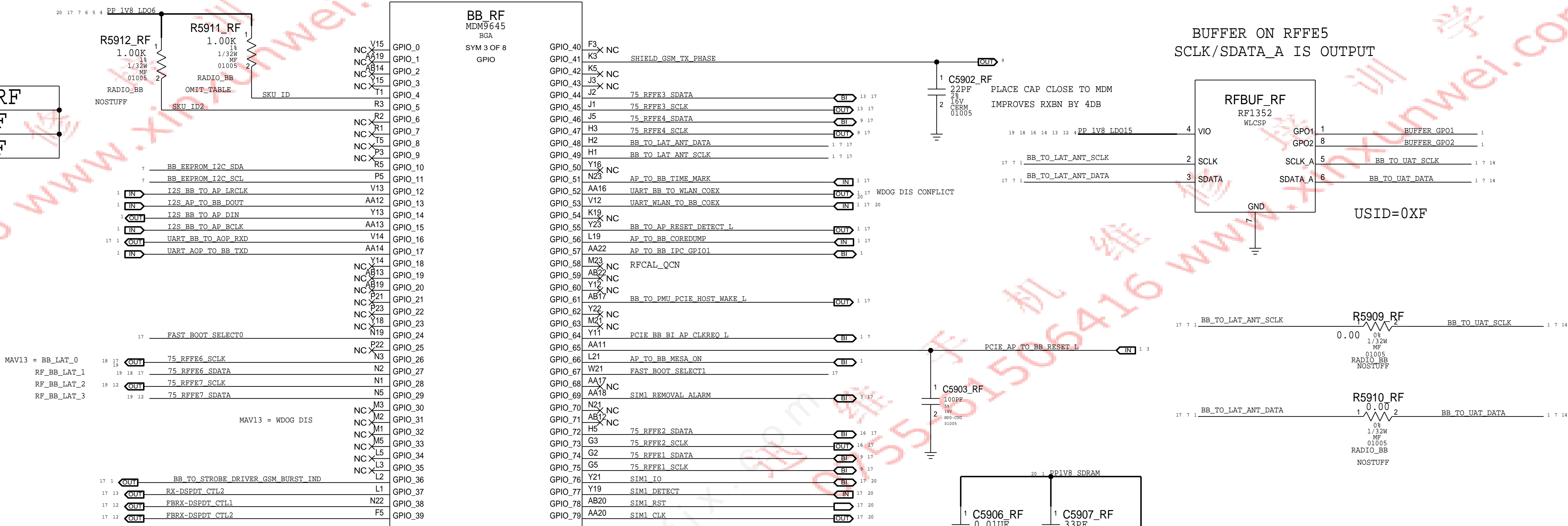
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		PAGE		58 OF 72	
		SHEET		67 OF 81	

BASEBAND: GPIOs

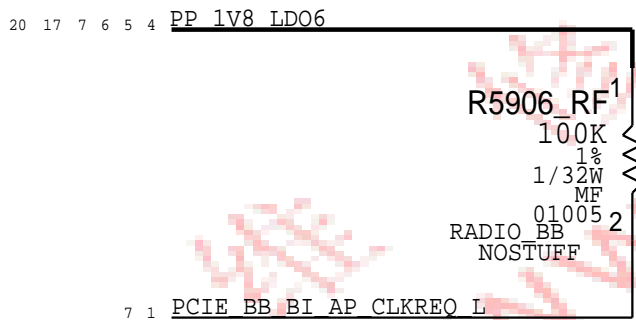
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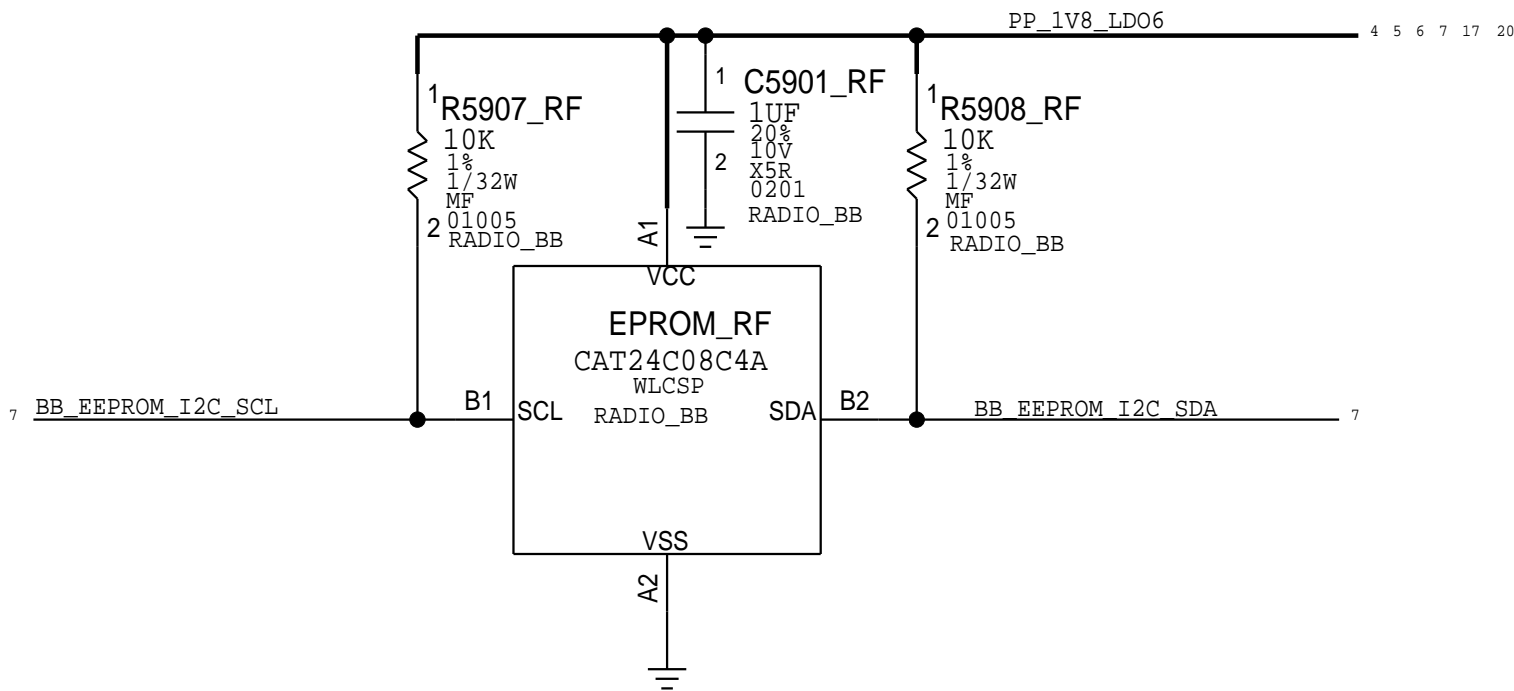
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- RFFE2 QFE3100
- RFFE3 DIV MODULES
- RFFE4 WTR4905
- RFFE5 TUNERS + ELNAS
- RFFE6 2G PA,MLB PA,MB/HB TDD PA,MB/HB FDD PA
- RFFE7 LB PA, COUPLERS

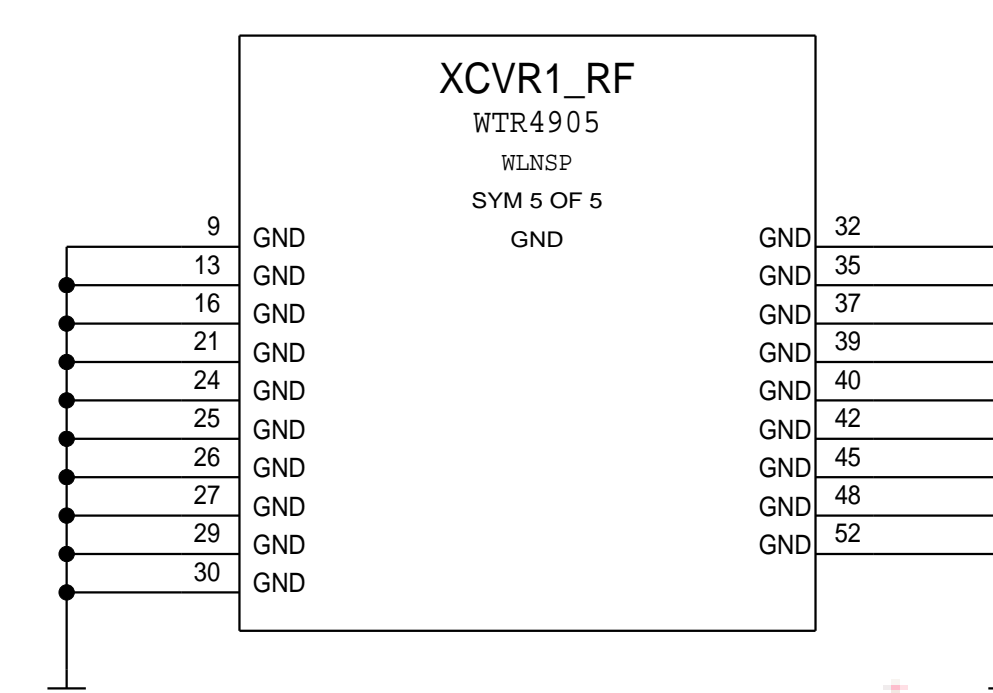
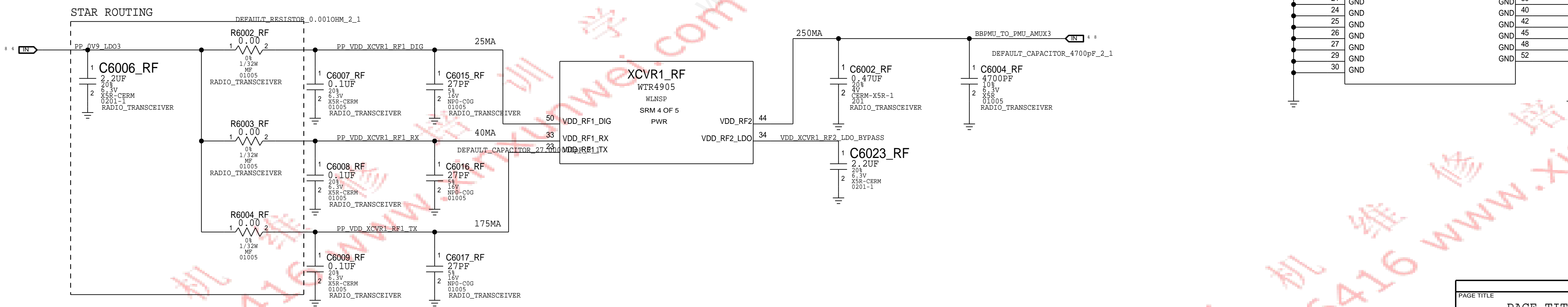
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BB EEPROM

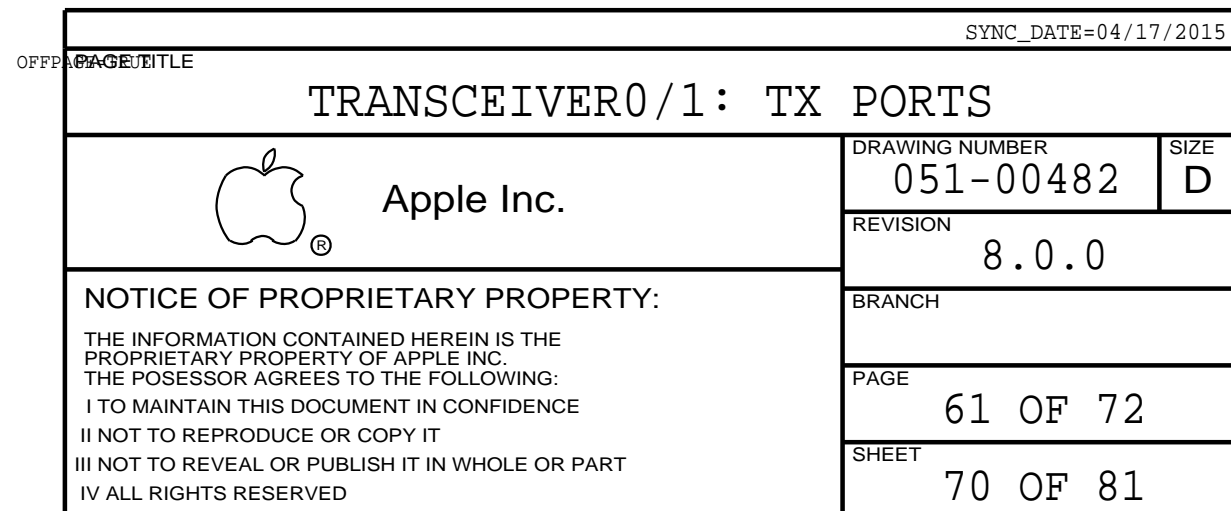


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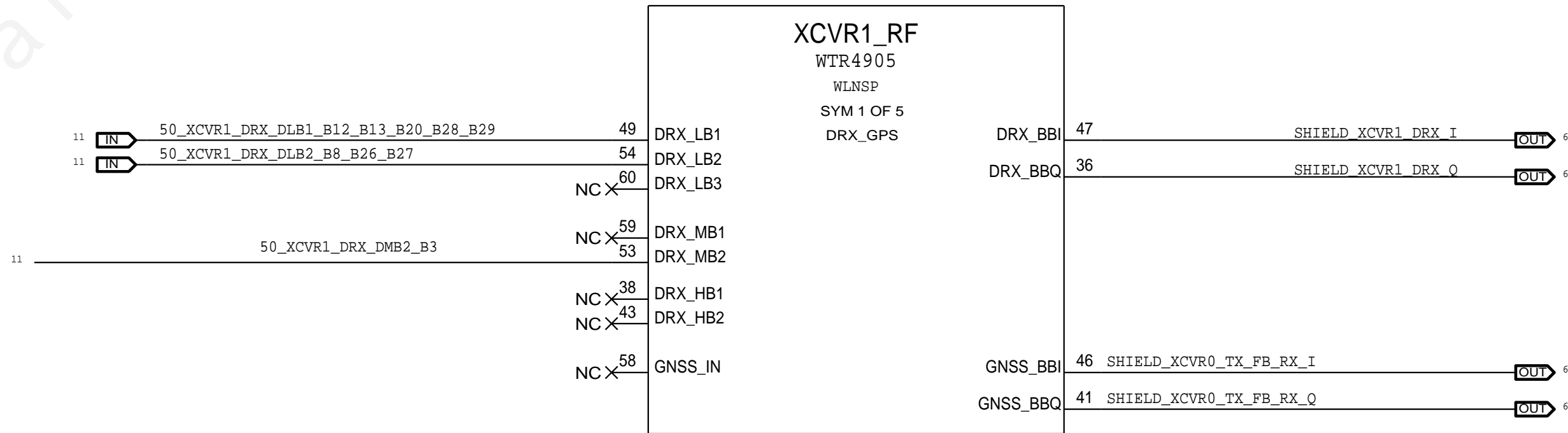
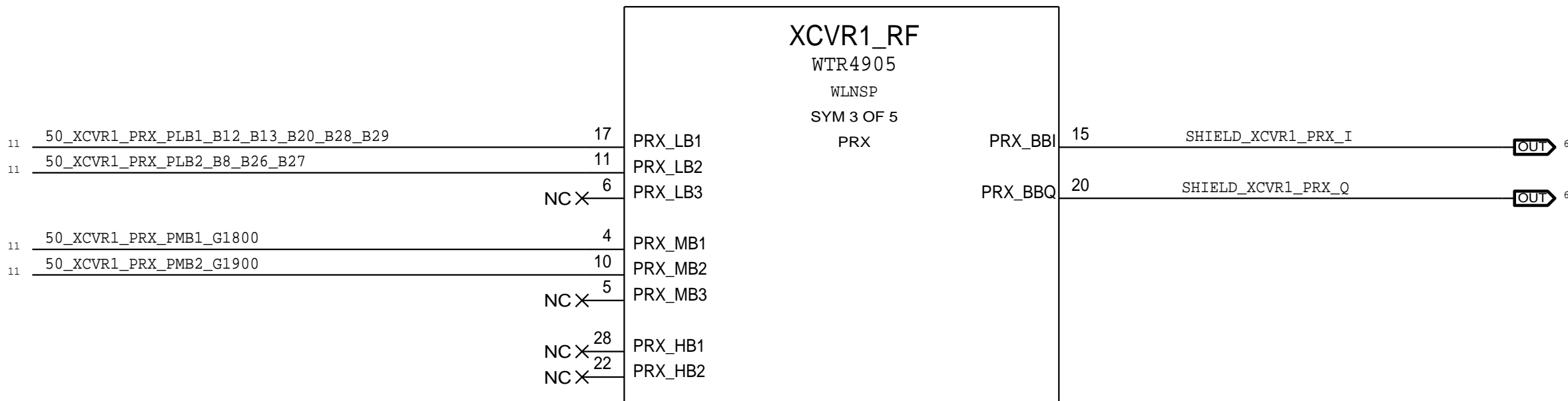
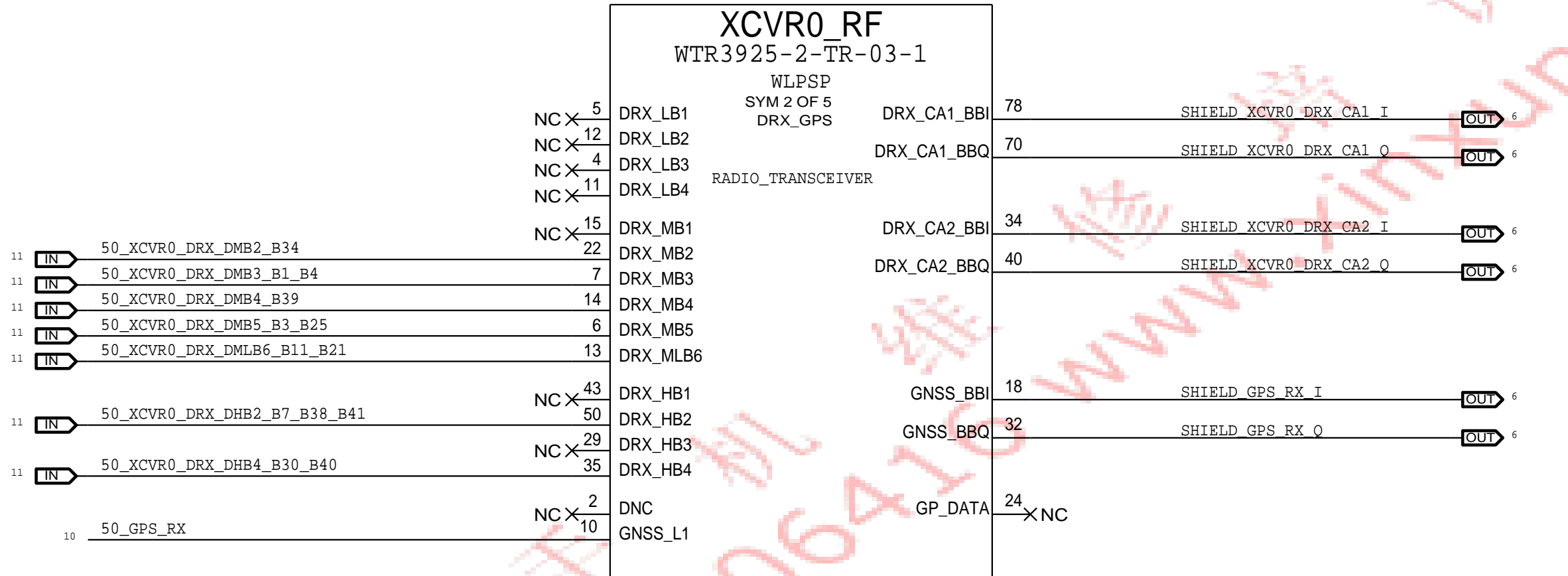
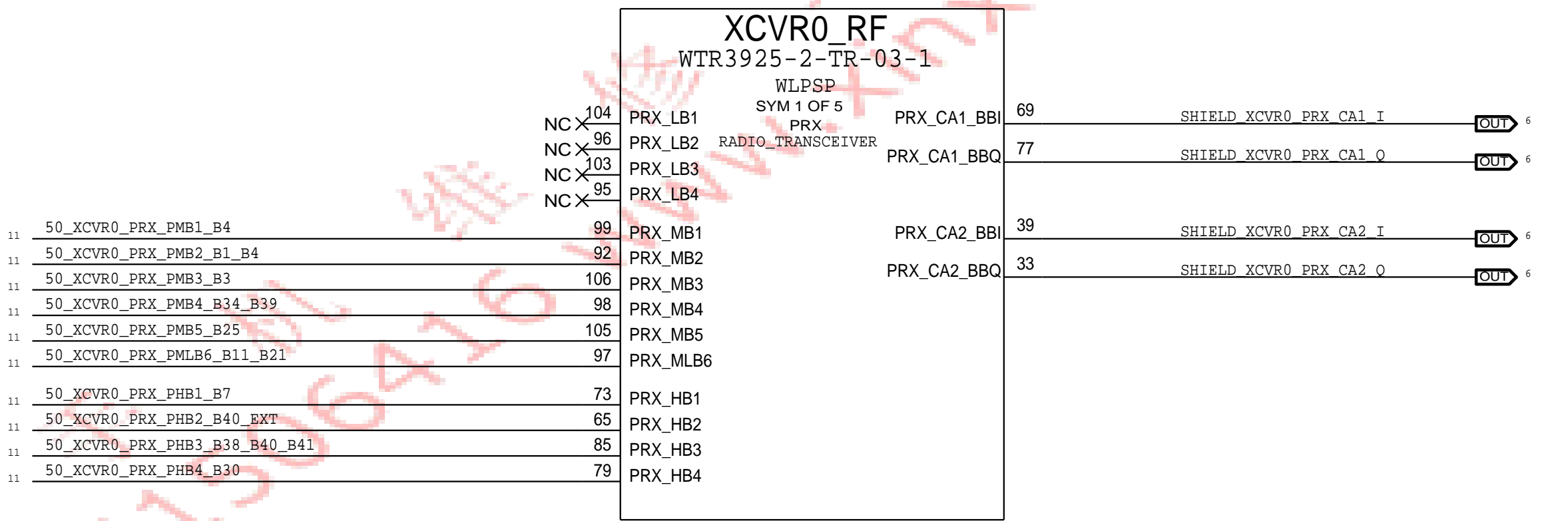


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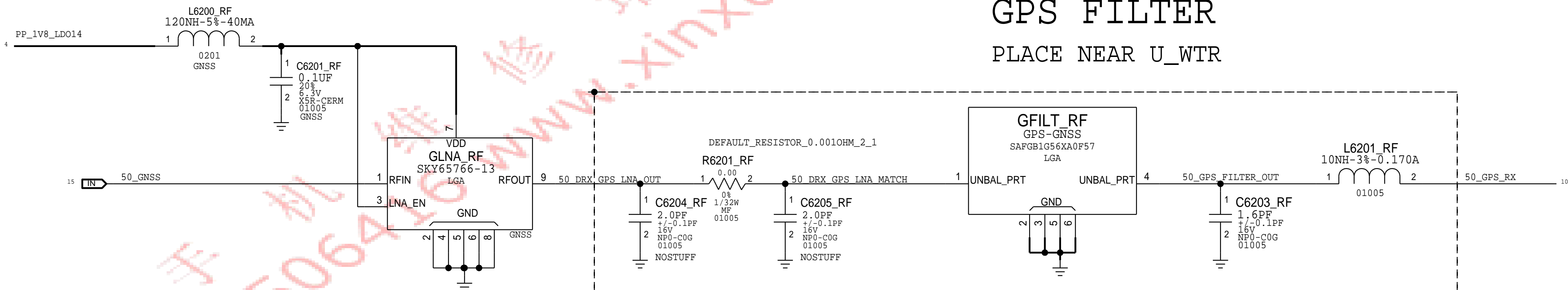
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TRANSCEIVER: PRX, DRX, & GPS PORTS



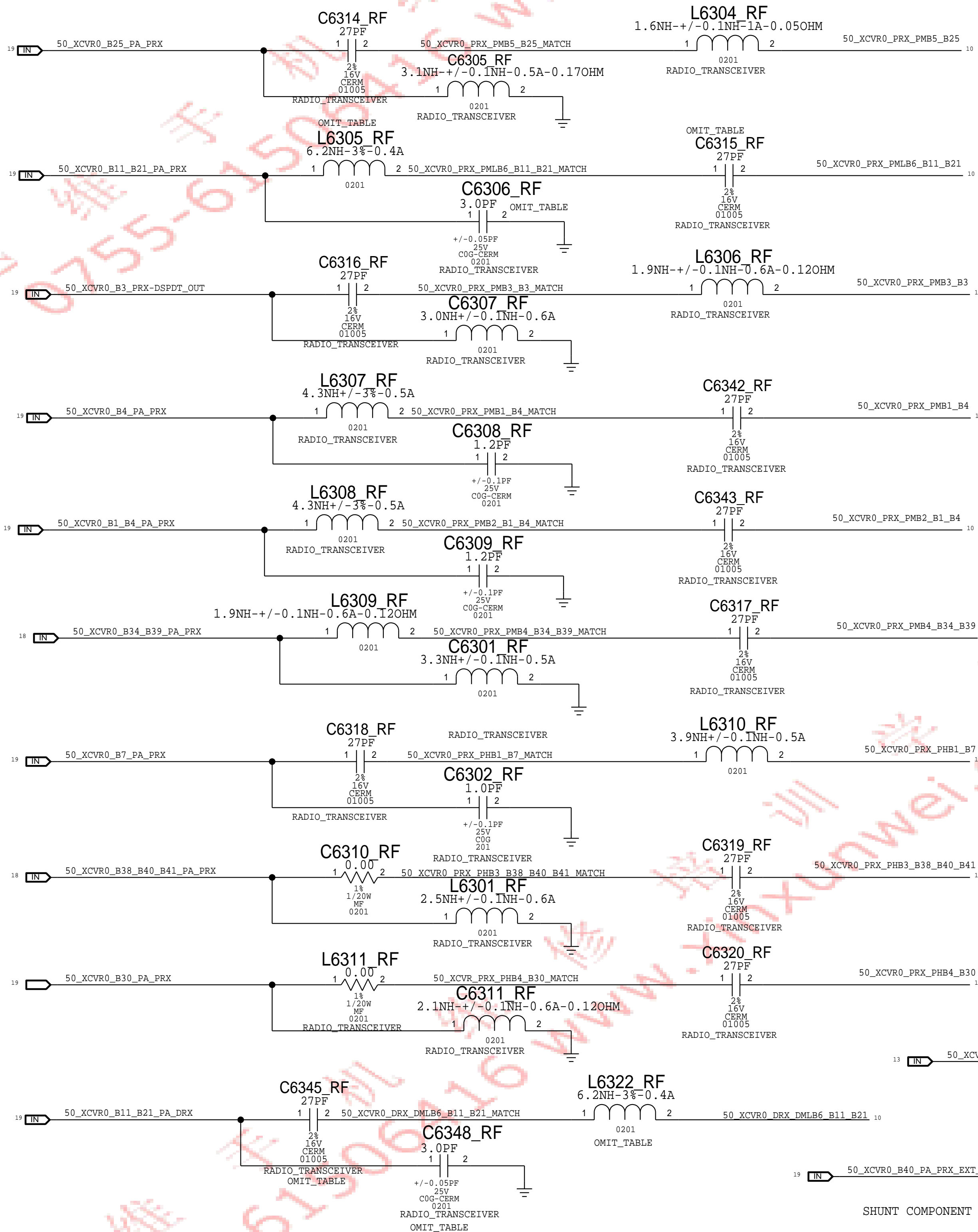
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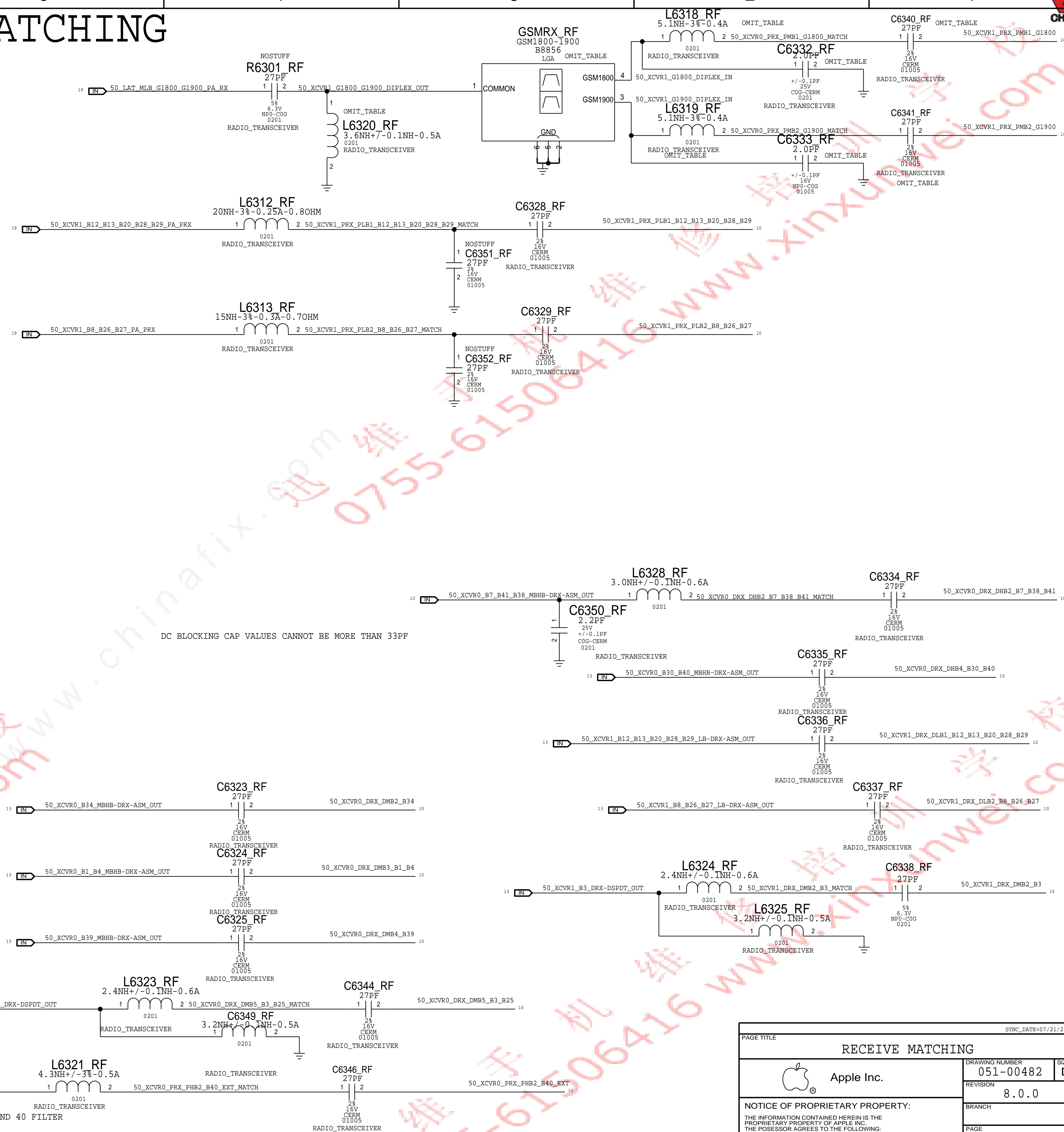
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PRIMARY & DIVERSITY RECEIVE MATCHING

DC BLOCKING CAP VALUES CANNOT BE MORE THAN 33PF




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DC BLOCKING CAP VALUES CANNOT BE MORE THAN 33PF

SHUNT COMPONENT WITH BAND 40 FILTER

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	PAGE		63 OF 72
		SHEET	72 OF 81

LOWER ANTENNA AND COUPLER

D

C

D

C


B

B

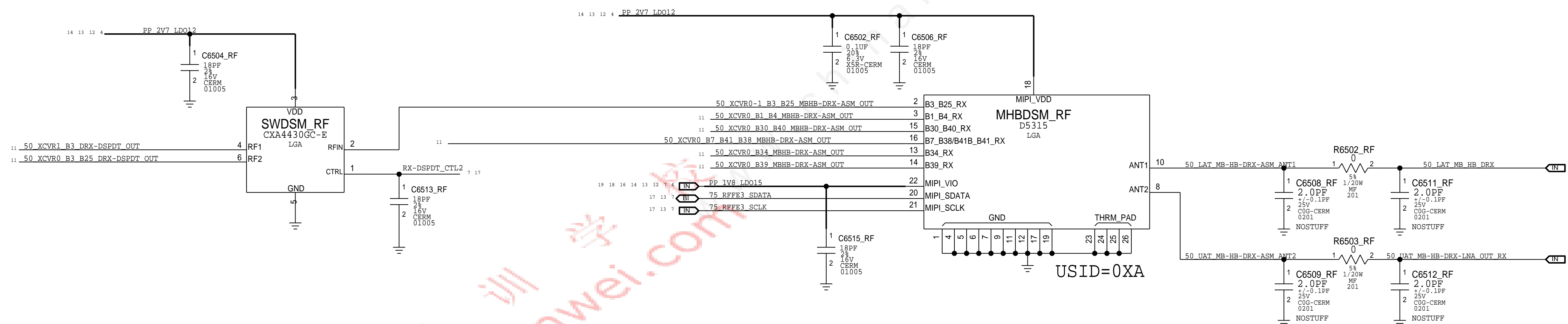
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A

UPPER ANTENNA COUPLER

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			PAGE	64 OF 72
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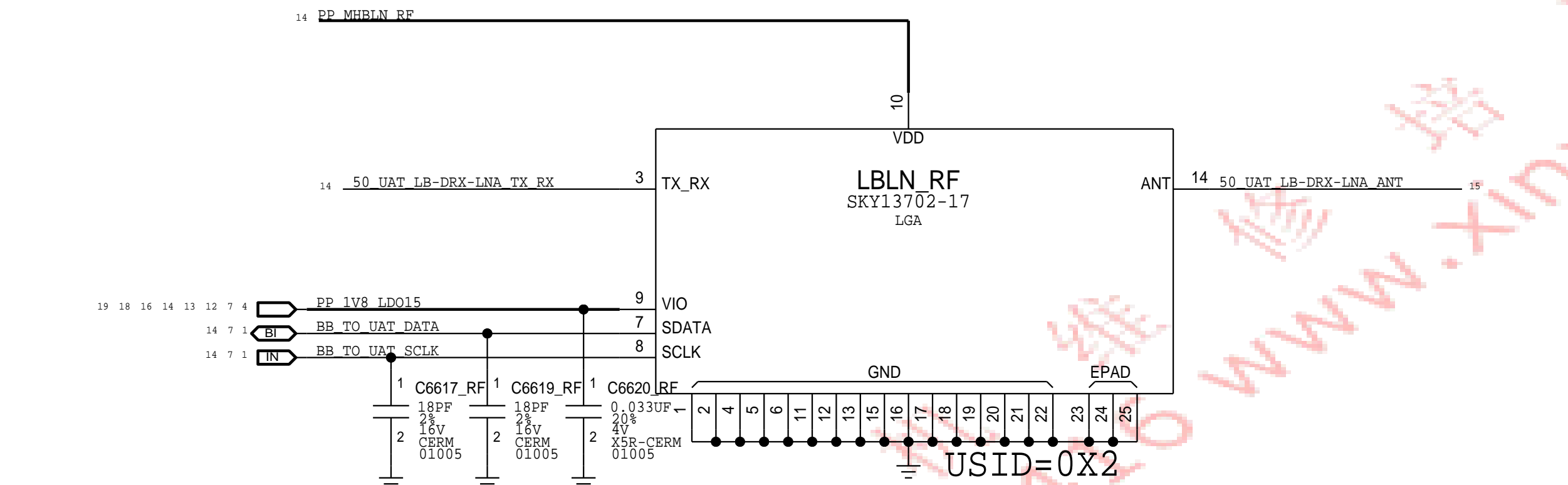
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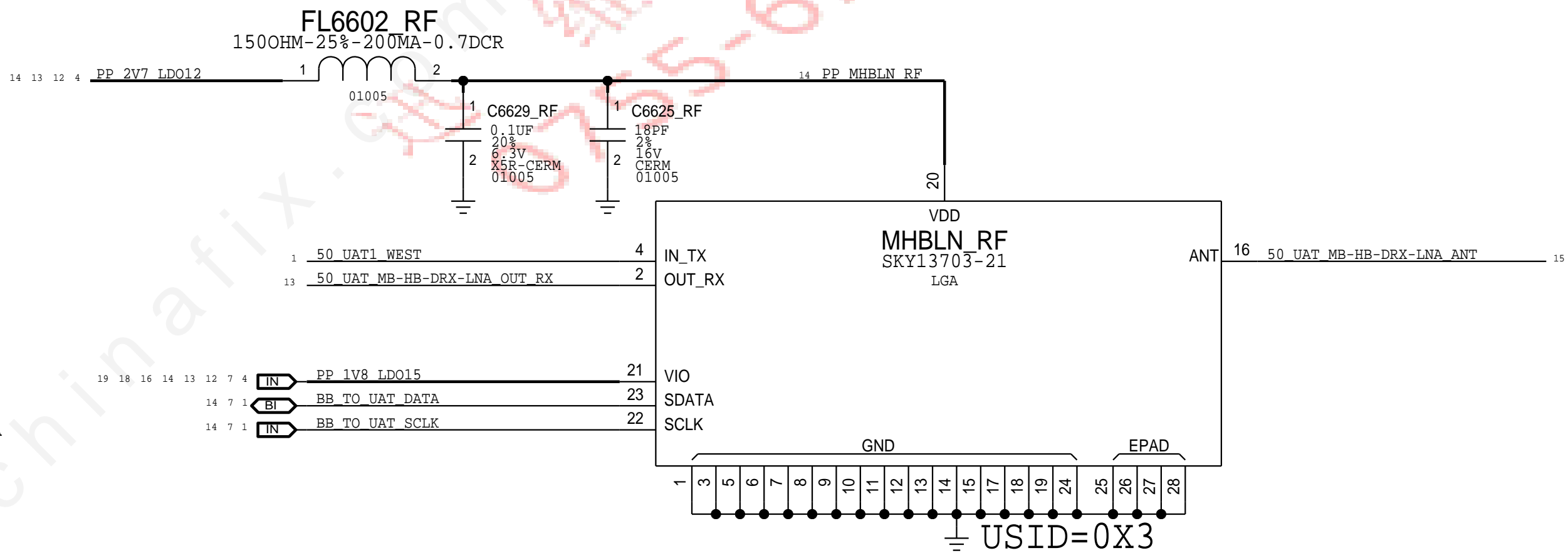
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DIVERSITY RECEIVE LNAs

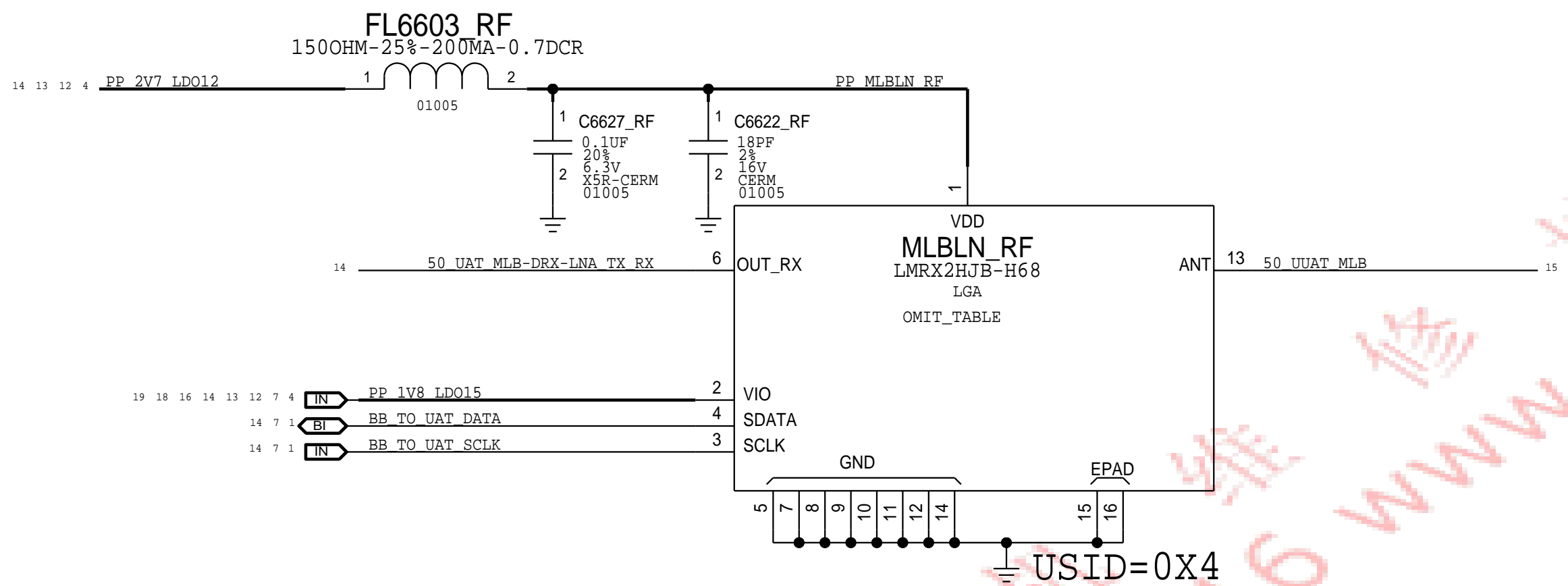
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MB/HB DRX LNA



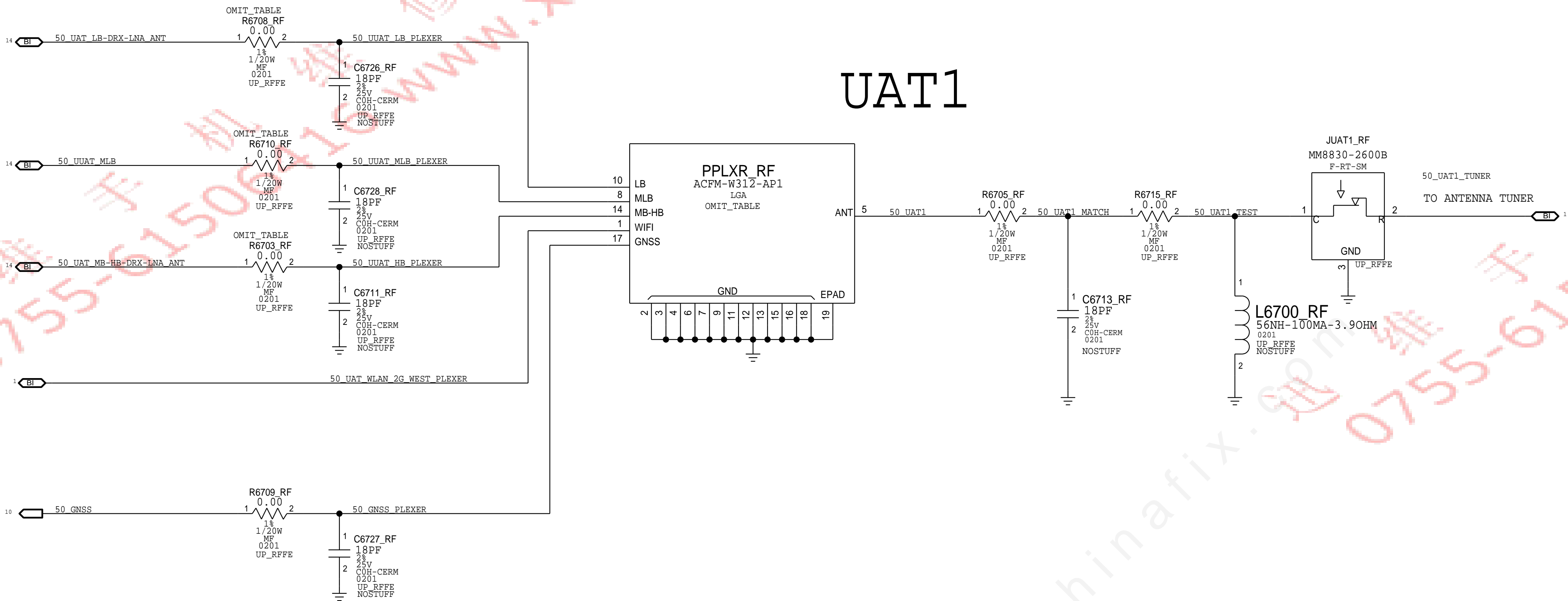
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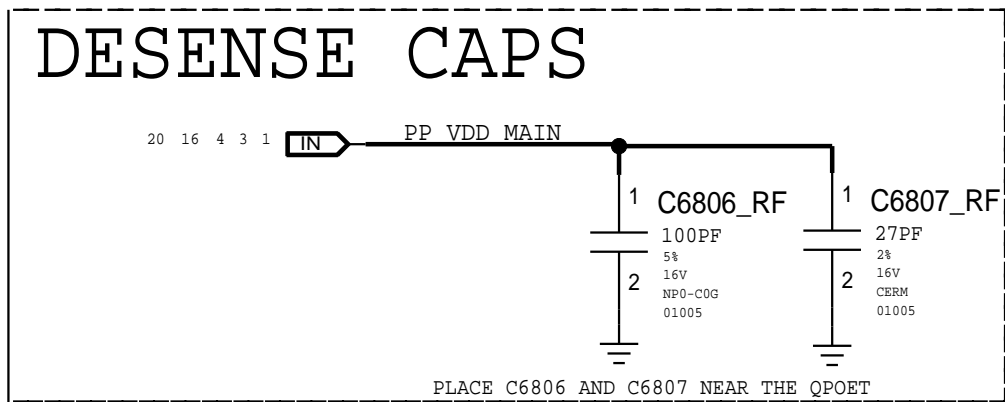
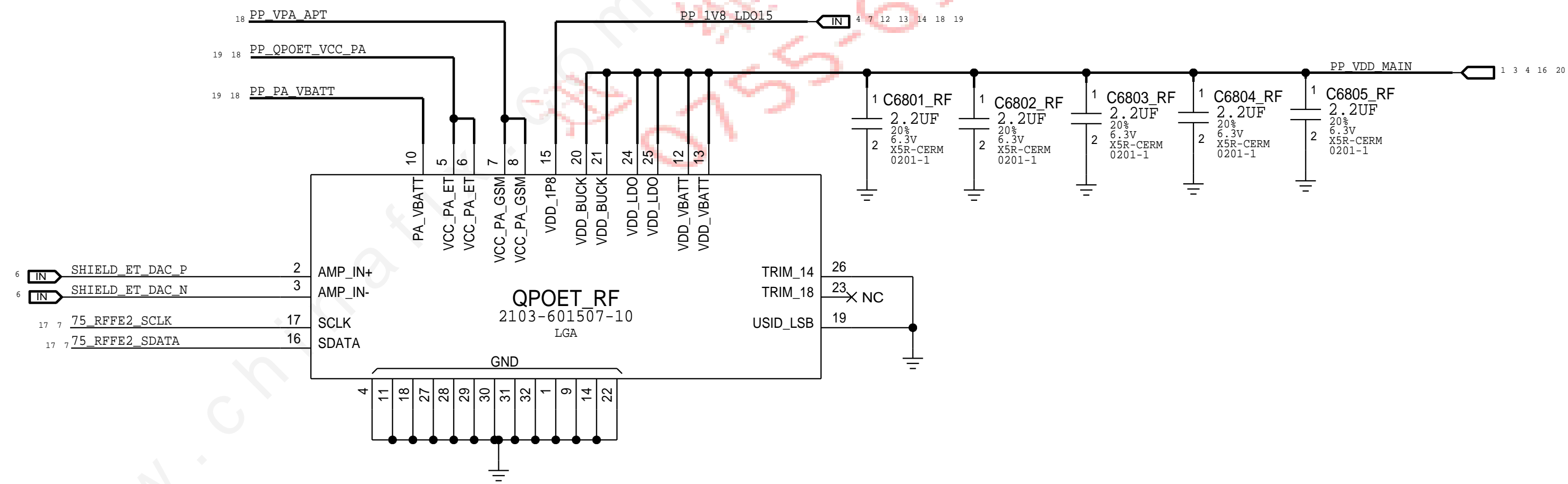
UPPER ANTENNA FEEDS


UAT1



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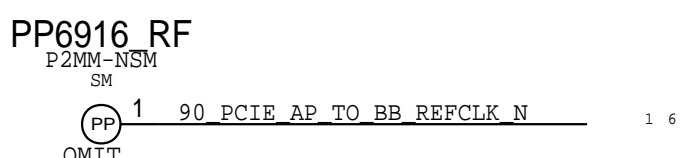
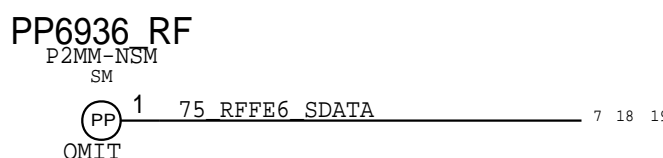
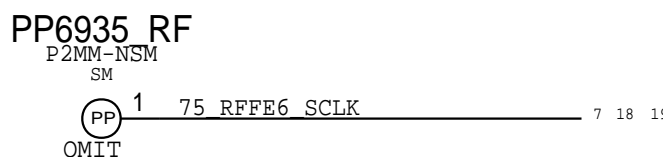
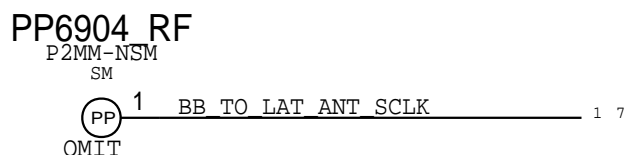
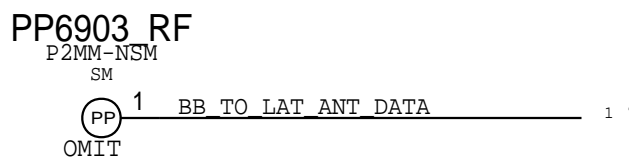
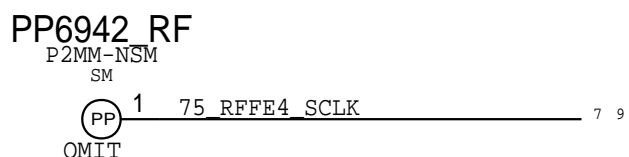
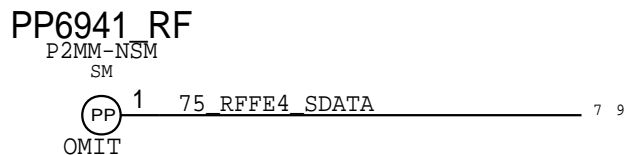
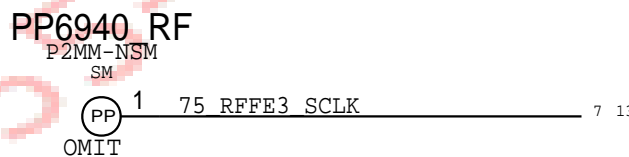
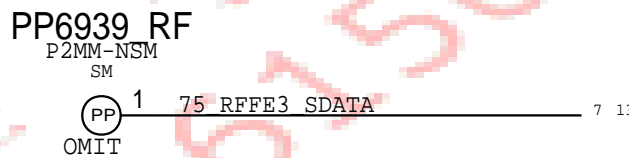
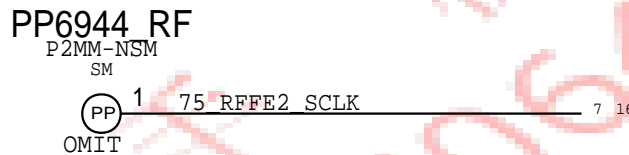
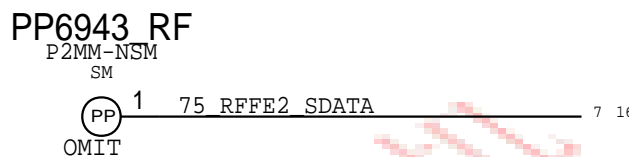
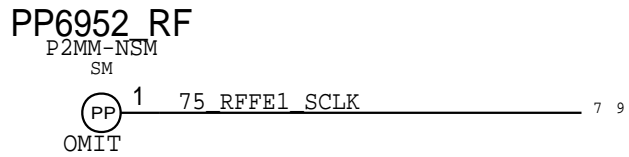
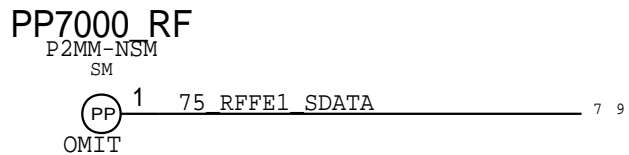
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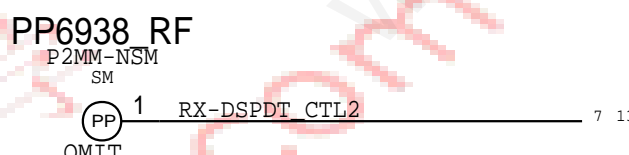
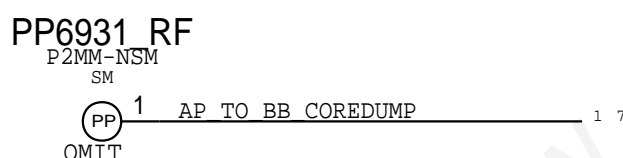
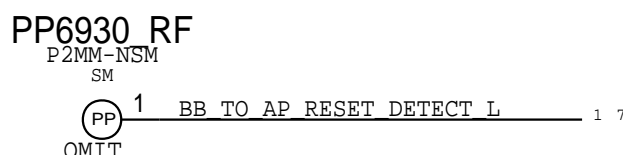
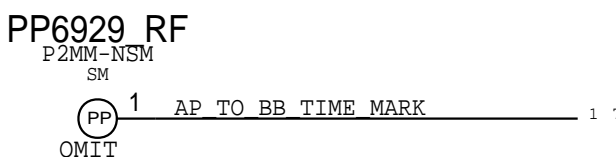
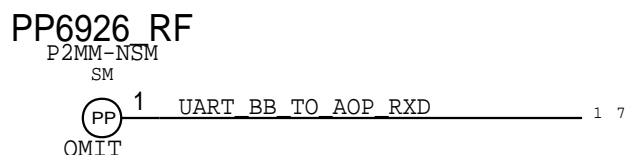
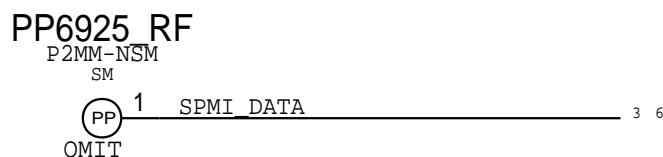
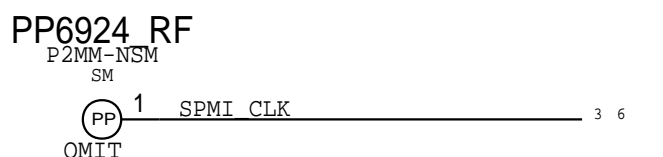
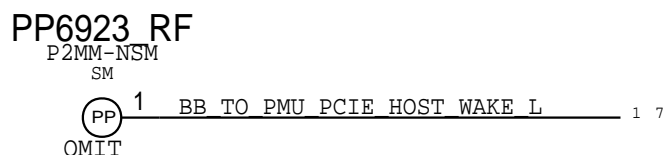
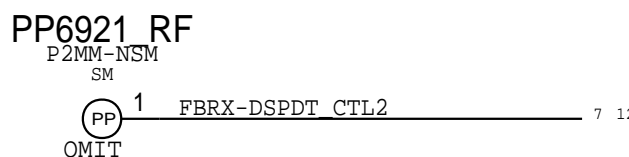
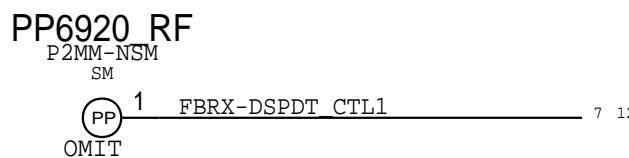
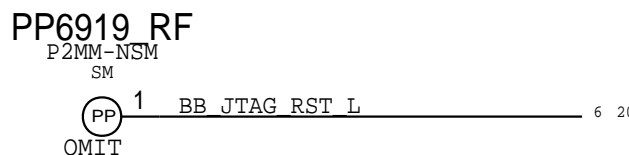
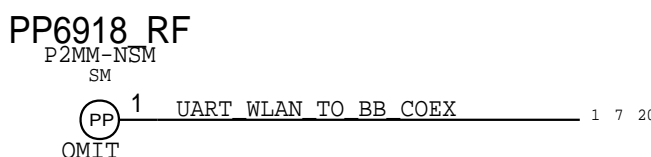
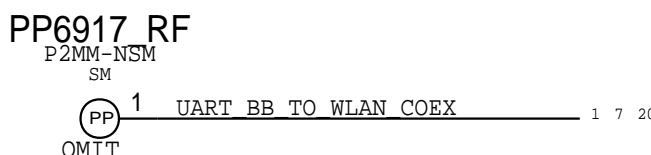
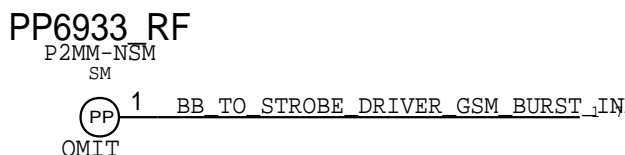
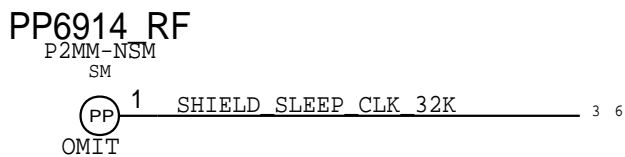
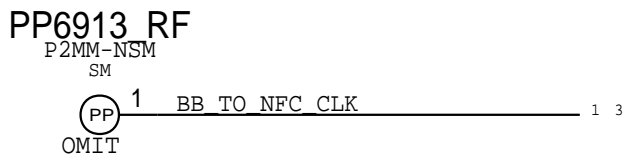
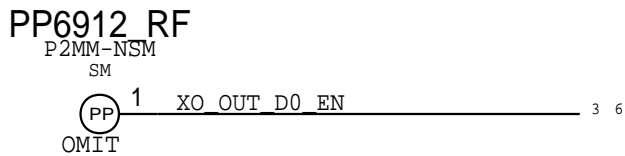
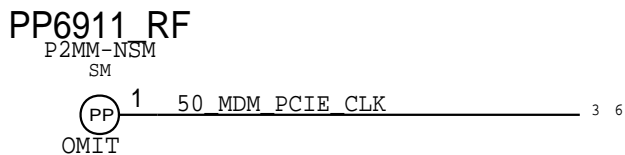
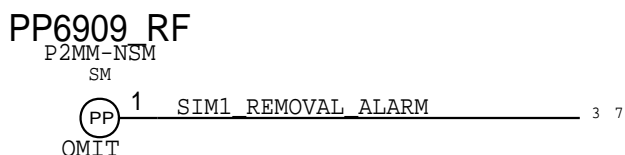
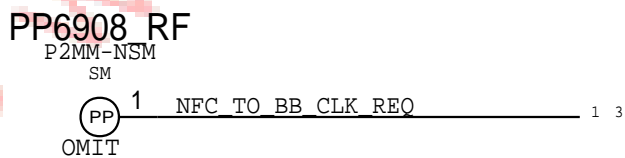
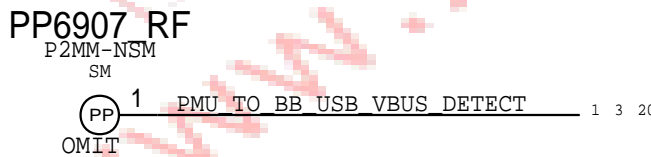
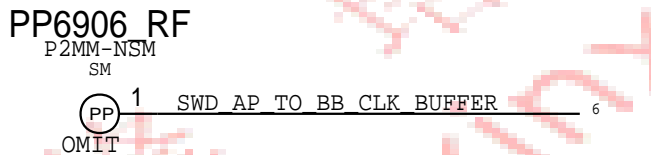
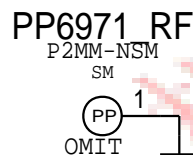
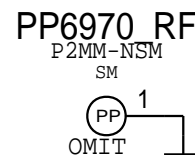
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MLB TEST POINTS

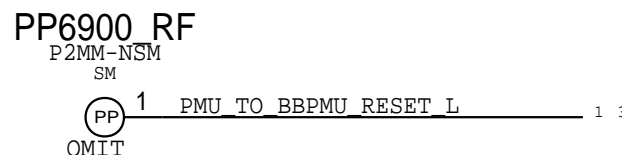
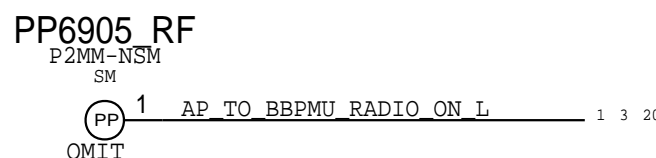
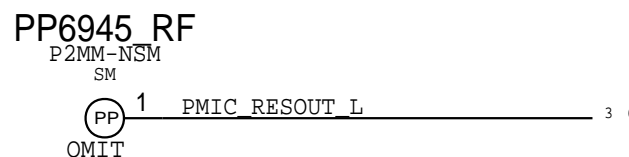
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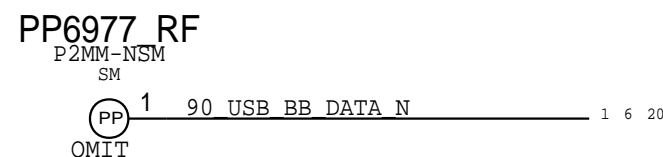
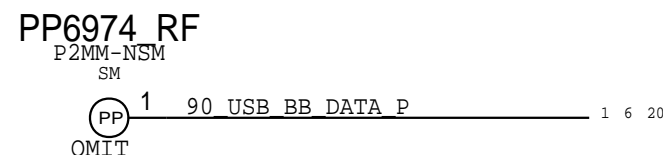
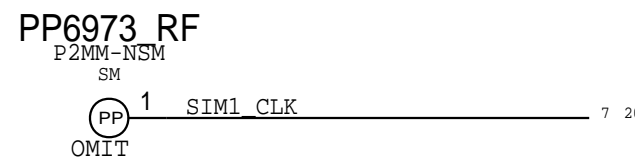
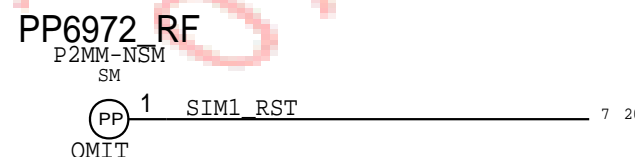
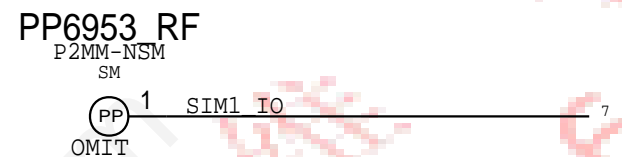
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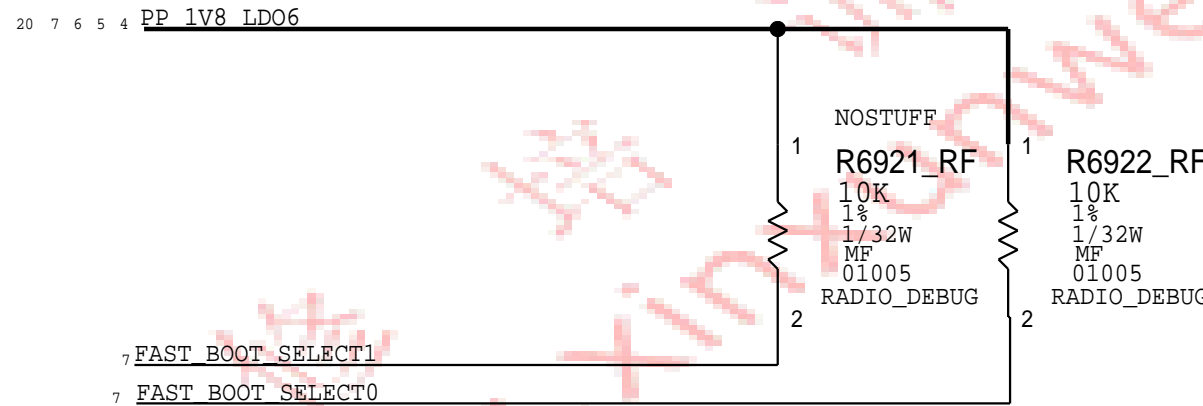
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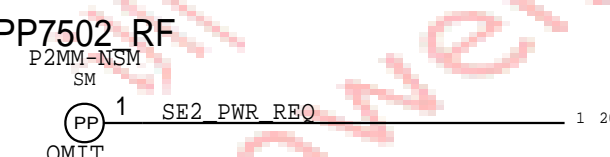
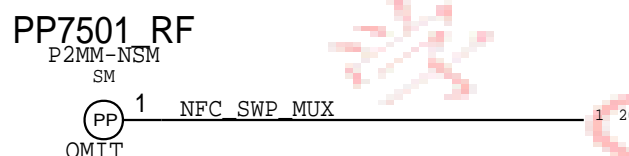
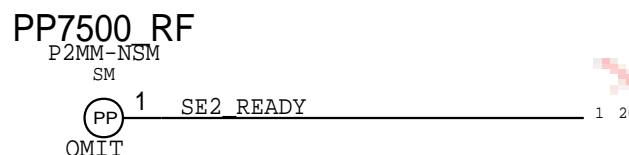
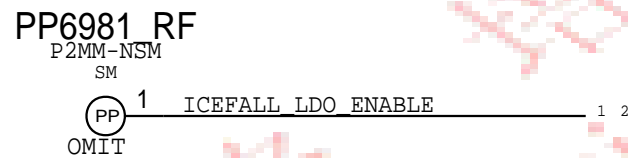
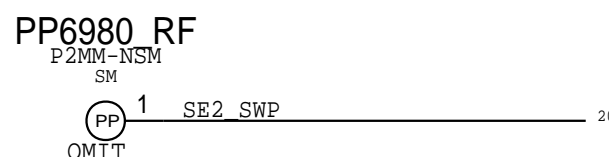
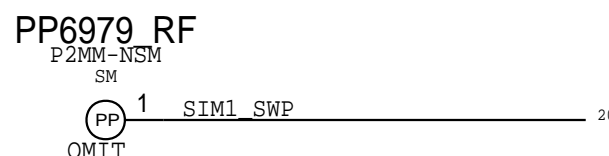
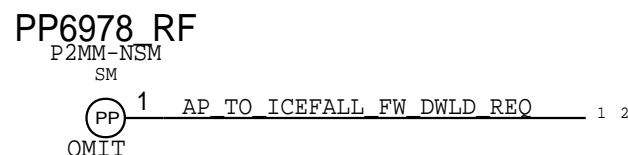
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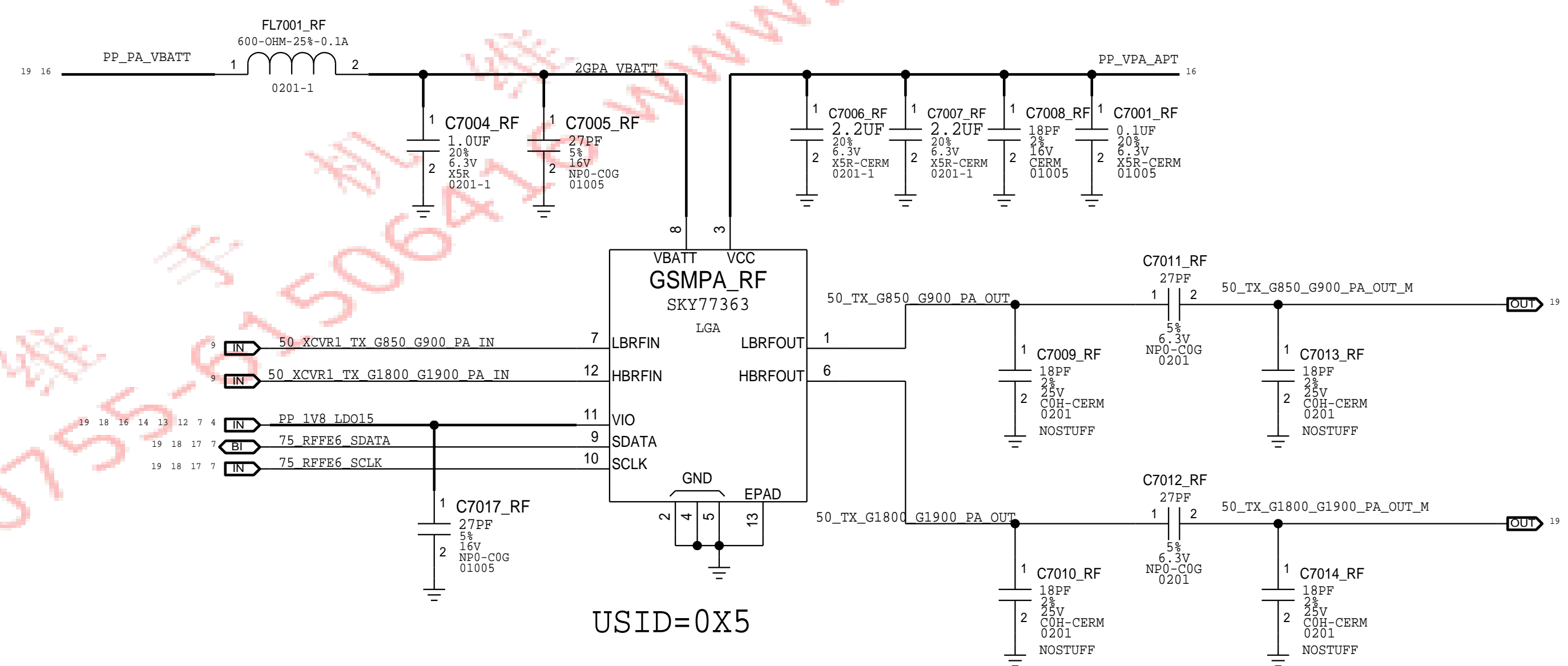


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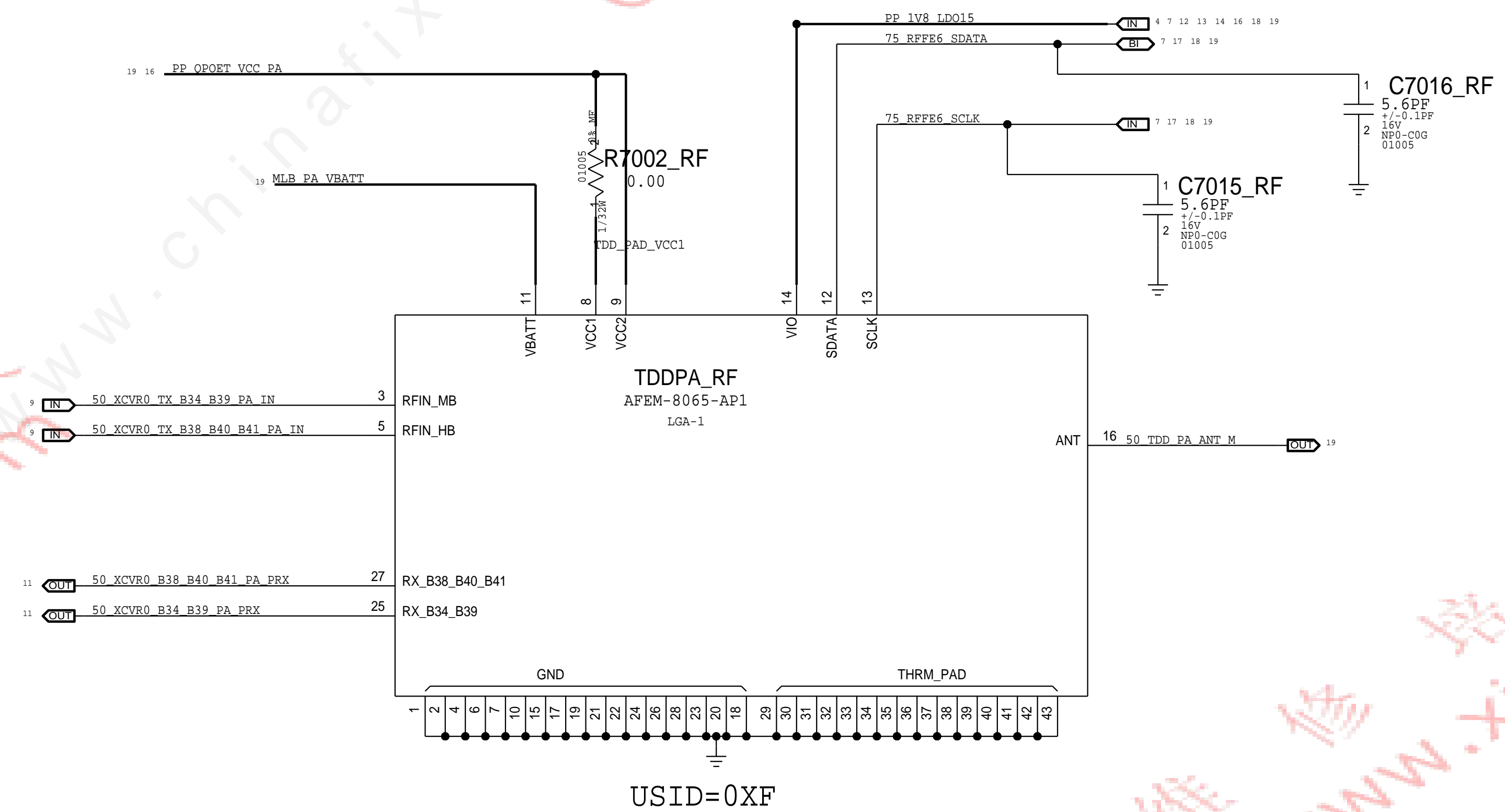



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